

RECONFIGURABLE COMPUTING

ACCELERATING COMPUTATION WITH
FIELD-PROGRAMMABLE GATE ARRAYS

By

Maya Gokhale and Paul S. Graham



Springer

Reconfigurable Computing Accelerating Computation With Field Programmable Gate Arrays

Khaled Elleithy



Reconfigurable Computing Accelerating Computation With Field Programmable Gate Arrays:

Reconfigurable Computing Maya B. Gokhale, Paul S. Graham, 2006-07-04 A one of a kind survey of the field of Reconfigurable Computing Gives a comprehensive introduction to a discipline that offers a 10X 100X acceleration of algorithms over microprocessors Discusses the impact of reconfigurable hardware on a wide range of applications signal and image processing network security bioinformatics and supercomputing Includes the history of the field as well as recent advances Includes an extensive bibliography of primary sources

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Reconfigurable Computing Systems Engineering Lev Kirischian, 2017-12-19 Reconfigurable Computing Systems Engineering Virtualization of Computing Architecture describes the organization of reconfigurable computing system RCS architecture and discusses the pros and cons of different RCS architecture implementations Providing a solid understanding of RCS technology and where it s most effective this book Details the architecture organization of RCS platforms for application specific workloads Covers the process of the architectural synthesis of hardware components for system on chip SoC for the RCS Explores the virtualization of RCS architecture from the system and on chip levels Presents methodologies for RCS architecture run time integration according to mode of operation and rapid adaptation to changes of multi parametric constraints Includes illustrative examples case studies homework problems and references to important literature A solutions manual is available with qualifying course adoption Reconfigurable Computing Systems Engineering Virtualization of Computing Architecture offers a complete road map to the synthesis of RCS architecture exposing hardware design engineers system architects and students specializing in designing FPGA based embedded systems to novel concepts in RCS architecture organization and virtualization

Embedded Systems Krzysztof Iniewski, 2012-10-26 Covers the significant embedded computing technologies highlighting their applications in wireless communication and computing power An embedded system is a computer system designed for specific control functions within a larger system often with real time computing constraints It is embedded as part of a complete device often including hardware and mechanical parts Presented in three parts Embedded Systems Hardware Design and Implementation provides readers with an immersive introduction to this rapidly growing segment of the computer industry Acknowledging the fact that embedded systems control many of today s most common devices such as smart phones PC tablets as well as hardware embedded in cars TVs and even refrigerators and heating systems the book starts with a basic introduction to embedded computing systems It hones in on system on a chip SoC multiprocessor system on chip MPSoC and network on chip NoC It then covers on chip

integration of software and custom hardware accelerators as well as fabric flexibility custom architectures and the multiple I/O standards that facilitate PCB integration Next it focuses on the technologies associated with embedded computing systems going over the basics of field programmable gate array FPGA digital signal processing DSP and application specific integrated circuit ASIC technology architectural support for on chip integration of custom accelerators with processors and OS support for these systems Finally it offers full details on architecture testability and computer aided design CAD support for embedded systems soft processors heterogeneous resources and on chip storage before concluding with coverage of software support in particular OS Linux Embedded Systems Hardware Design and Implementation is an ideal book for design engineers looking to optimize and reduce the size and cost of embedded system products and increase their reliability and performance

Reconfigurable Computing Joao Cardoso, Michael Hübner, 2011-08-17 As the complexity of modern embedded systems increases it becomes less practical to design monolithic processing platforms As a result reconfigurable computing is being adopted widely for more flexible design Reconfigurable Computers offer the spatial parallelism and fine grained customizability of application specific circuits with the postfabrication programmability of software To make the most of this unique combination of performance and flexibility designers need to be aware of both hardware and software issues FPGA users must think not only about the gates needed to perform a computation but also about the software flow that supports the design process The goal of this book is to help designers become comfortable with these issues and thus be able to exploit the vast opportunities possible with reconfigurable logic

Reconfigurable Computing: Architectures, Tools and Applications Andreas Koch, Ram Krishnamurthy, John McAllister, Roger Woods, Tarek El-Ghazawi, 2011-03-15 This book constitutes the refereed proceedings of the 7th International Symposium on Reconfigurable Computing Architectures Tools and Applications ARC 2011 held in Belfast UK in March 2011 The 40 revised papers presented consisting of 24 full papers 14 poster papers and the abstracts of 2 plenary talks were carefully reviewed and selected from 88 submissions The topics covered are reconfigurable accelerators design tools reconfigurable processors applications device architecture methodology and simulation and system architecture

Parallel and Distributed Computing Alberto Ros, 2010-01-01 The 14 chapters presented in this book cover a wide variety of representative works ranging from hardware design to application development Particularly the topics that are addressed are programmable and reconfigurable devices and systems dependability of GPUs General Purpose Units network topologies cache coherence protocols resource allocation scheduling algorithms peertopeer networks largescale network simulation and parallel routines and algorithms In this way the articles included in this book constitute an excellent reference for engineers and researchers who have particular interests in each of these topics in parallel and distributed computing

Parallel Computing Technologies Victor Malyshev, 2015-07-24 This book constitutes the proceedings of the 13th International Conference on Parallel Computing Technologies PaCT 2015 held in Petrozavodsk Russia during August September 2015 The 37 full papers and 14 short papers presented were carefully

reviewed and selected from 87 submissions The papers are organized in topical sections on parallel models algorithms and programming methods unconventional computing cellular automata distributed computing special processors programming techniques applications **Reconfigurable System Design and Verification** Pao-Ann Hsiung,Marco D.

Santambrogio,Chun-Hsian Huang,2018-10-08 Reconfigurable systems have pervaded nearly all fields of computation and will continue to do so for the foreseeable future Reconfigurable System Design and Verification provides a compendium of design and verification techniques for reconfigurable systems allowing you to quickly search for a technique and determine if it is appropriate to the task at hand It bridges the gap between the need for reconfigurable computing education and the burgeoning development of numerous different techniques in the design and verification of reconfigurable systems in various application domains The text explains topics in such a way that they can be immediately grasped and put into practice It starts with an overview of reconfigurable computing architectures and platforms and demonstrates how to develop reconfigurable systems This sets up the discussion of the hardware software and system techniques that form the core of the text The authors classify design and verification techniques into primary and secondary categories allowing the appropriate ones to be easily located and compared The techniques discussed range from system modeling and system level design to co simulation and formal verification Case studies illustrating real world applications detailed explanations of complex algorithms and self explaining illustrations add depth to the presentation Comprehensively covering all techniques related to the hardware software design and verification of reconfigurable systems this book provides a single source for information that otherwise would have been dispersed among the literature making it very difficult to search compare and select the technique most suitable The authors do it all for you making it easy to find the techniques that fit your system requirements without having to surf the net or digital libraries to find the candidate techniques and compare them yourself *Compilation Techniques for Reconfigurable Architectures* João M.P. Cardoso,Pedro C. Diniz,2011-04-02 The extreme exibility of reconfigurable architectures and their performance potential have made them a vehicle of choice in a wide range of computing domains from rapid circuit prototyping to high performance computing The increasing availability of transistors on a die has allowed the emergence of reconfigurable architectures with a large number of computing resources and interconnection topologies To exploit the potential of these reconfigurable architectures programmers are forced to map their applications typically written in high level imperative programming languages such as C or MATLAB to hardware oriented languages such as VHDL or Verilog In this process they must assume the role of hardware designers and software programmers and navigate a maze of program transformations mapping and synthesis steps to produce efficient reconfigurable computing implementations The richness and sophistication of any of these application mapping steps make the mapping of computations to these architectures an increasingly daunting process It is thus widely believed that automatic compilation from high level programming languages is the key to the success of reconfigurable computing This book describes a wide

range of code transformations and mapping techniques for programs described in high level programming languages most notably imperative languages to reconfigurable architectures

Advances in Systems Science Jerzy Swiatek, Adam Grzech, Paweł Swiatek, Jakub M. Tomczak, 2013-08-13 The International Conference on Systems Science 2013 ICSS 2013 was the 18th event of the series of international scientific conferences for researchers and practitioners in the fields of systems science and systems engineering The conference took place in Wroclaw Poland during September 10 12 2013 and was organized by Wroclaw University of Technology and co organized by Committee of Automatics and Robotics of Polish Academy of Sciences Committee of Computer Science of Polish Academy of Sciences and Polish Section of IEEE The papers included in the proceedings cover the following topics Control Theory Databases and Data Mining Image and Signal Processing Machine Learning Modeling and Simulation Operational Research Service Science Time series and System Identification The accepted and presented papers highlight new trends and challenges in systems science and systems engineering

Design of Image Processing Embedded Systems Using Multidimensional Data Flow Joachim Keinert, Jürgen Teich, 2010-11-18 This book presents a new set of embedded system design techniques called multidimensional data flow which combine the various benefits offered by existing methodologies such as block based system design high level simulation system analysis and polyhedral optimization It describes a novel architecture for efficient and flexible high speed communication in hardware that can be used both in manual and automatic system design and that offers various design alternatives balancing achievable throughput with required hardware size This book demonstrates multidimensional data flow by showing its potential for modeling analysis and synthesis of complex image processing applications These applications are presented in terms of their fundamental properties and resulting design constraints Coverage includes a discussion of how far the latter can be met better by multidimensional data flow than alternative approaches Based on these results the book explains the principles of fine grained system level analysis and high speed communication synthesis Additionally an extensive review of related techniques is given in order to show their relation to multidimensional data flow

Reconfigurable Computing: Architectures, Tools, and Applications Roger Woods, Katherine Compton, Christos Bourganis, Pedro C. Diniz, 2008-08-29 Coverage in this proceedings volume includes DNA and string processing applications reconfigurable computing hardware and systems image processing run time behavior instruction set extension as well as random number generation and financial computation

Reconfigurable Computing: Architectures, Tools and Applications Pedro C. Diniz, 2007-03-19 This book constitutes the refereed proceedings of the Third International Workshop on Applied Reconfigurable Computing ARC 2007 held in Mangaratiba Brazil in March 2007 The 27 full papers and 10 short papers presented together with a late comer contribution from ARC 2006 are organized in topical sections on architectures mapping techniques and tools arithmetic and applications

Bioinformatics Bertil Schmidt, 2010-07-15 New sequencing technologies have broken many experimental barriers to genome scale sequencing

leading to the extraction of huge quantities of sequence data This expansion of biological databases established the need for new ways to harness and apply the astounding amount of available genomic information and convert it into substantive biological *Supercomputing Research Advances* Yongge Huáng,2008 Supercomputers are used for highly calculation intensive tasks such as problems involving quantum mechanical physics weather forecasting climate research including research into global warming molecular modelling computing the structures and properties of chemical compounds biological macromolecules polymers and crystals physical simulations such as simulation of aeroplanes in wind tunnels simulation of the detonation of nuclear weapons and research into nuclear fusion cryptanalysis and the like Major universities military agencies and scientific research laboratories are heavy users This book presents the latest research in the field from around the world *Computational Mechanics with Neural Networks* Genki Yagawa,Atsuya Oishi,2021-02-26 This book shows how neural networks are applied to computational mechanics Part I presents the fundamentals of neural networks and other machine learning method in computational mechanics Part II highlights the applications of neural networks to a variety of problems of computational mechanics The final chapter gives perspectives to the applications of the deep learning to computational mechanics *Advances and Innovations in Systems, Computing Sciences and Software Engineering* Khaled Elleithy,2007-08-28 Advances and Innovations in Systems Computing Sciences and Software Engineering includes a set of rigorously reviewed world class manuscripts addressing and detailing state of the art research projects in the areas of Computing Sciences Software Engineering and Systems Advances and Innovations in Systems Computing Sciences and Software Engineering includes selected papers form the conference proceedings of the International Conference on Systems Computing Sciences and Software Engineering SCSS 2006 which was part of the International Joint Conferences on Computer Information and Systems Sciences and Engineering CISSE 2006 All aspects of the conference were managed on line not only the reviewing submissions and registration processes but also the actual conference Conference participants authors presenters and attendees only needed an internet connection and sound available on their computers in order to be able to contribute and participate in this international ground breaking conference The on line structure of this high quality event allowed academic professionals and industry participants to contribute work and attend world class technical presentations based on rigorously refereed submissions live without the need for investing significant travel funds or time out of the office Suffice to say that CISSE received submissions from more than 70 countries for whose researchers this opportunity presented a much more affordable dynamic and well planned event to attend and submit their work to versus a classic on the ground conference The CISSE conference audio room provided superb audio even over low speed internet connections the ability to display PowerPoint presentations and cross platform compatibility the conferencing software runs on Windows Mac and any other operating system that supports Java In addition the conferencing system allowed for an unlimited number of participants which in turn granted CISSE the opportunity to

allow all participants to attend all presentations as opposed to limiting the number of available seats for each session

IAENG Transactions on Engineering Technologies Haeng Kon Kim, Sio-Iong Ao, Burghard B. Rieger, 2012-09-05 This volume contains thirty revised and extended research articles written by prominent researchers participating in an international conference in engineering technologies and physical science and applications The conference serves as good platforms for the engineering community to meet with each other and to exchange ideas The conference has also struck a balance between theoretical and application development The conference is truly international meeting with a high level of participation from many countries Topics covered include chemical engineering circuits communications systems control theory engineering mathematics systems engineering manufacture engineering and industrial applications The book offers the state of art of tremendous advances in engineering technologies and physical science and applications and also serves as an excellent reference work for researchers and graduate students working with on engineering technologies and physical science and applications Security and Trends in Wireless Identification and Sensing Platform Tags: Advancements in RFID Lopez, Pedro Peris, Hernandez-Castro, Julio C., Li, Tieyan, 2012-08-31 This book highlights new research regarding wireless identification and sensing platform WISP tags security and applications serving as a reference on WISP technology and presenting recent advances in this field Provided by publisher

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