

17th Conference on

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Edited by  
Richard B. Brown  
Alexander T. Ichii



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**Seventeenth Conference On Advanced Research In Vlsi**  
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**Ian Pickup**



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Seventeenth Conference on Advanced Research in VLSI Richard B. Brown,Alexander T. Ishii,1997      Seventeenth Conference on Advanced Research in VLSI Richard B. Brown,Alexander T. Ishii,1997 This volume on computational intelligence covers the 17th Conference on Advanced Research in VLSI in 1997 Topics include VLSI architecture asynchronous design circuits layout image sensors optimization system timing CAD and low power design      **Digital System Clocking** Vojin G. Oklobdzija,Vladimir M. Stojanovic,Dejan M. Markovic,Nikola M. Nedovic,2005-02-25 Provides the only up to date source on the most recent advances in this often complex and fascinating topic The only book to be entirely devoted to clocking Clocking has become one of the most important topics in the field of digital system design A must have book for advanced circuit engineers      *Digital Systems Engineering* William J. Dally,John W. Poulton,2008-04-24 What makes some computers slow Why do some digital systems operate reliably for years while others fail mysteriously every few hours How can some systems dissipate kilowatts while others operate off batteries These questions of speed reliability and power are all determined by the system level electrical design of a digital system Digital Systems Engineering presents a comprehensive treatment of these topics It combines a rigorous development of the fundamental principles in each area with real world examples of circuits and methods The book not only serves as an undergraduate textbook filling the gap between circuit design and logic design but can also help practising digital designers keep pace with the speed and power of modern integrated circuits The techniques described in this book once used only in supercomputers are essential to the correct and efficient operation of any type of digital system      *Cumulative Book Index* ,1998 A world list of books in the English language      **Parallel and Distributed Computing** Alberto Ros,2010-01-01 The 14 chapters presented in this book cover a wide variety of representative works ranging from hardware design to application development Particularly the topics that are addressed are programmable and reconfigurable devices and systems dependability of GPUs General Purpose Units network topologies cache coherence protocols resource allocation scheduling algorithms peertopeer networks largescale network simulation and parallel routines and algorithms In this way the articles included in this book constitute an excellent reference for engineers and researchers who have particular interests in each of these topics in parallel and distributed computing      **Publications in Engineering** ,1998      **American Book Publishing Record** ,1998      *Advances in Informatics* Dimitrios I. Fotiadis,Stavros D. Nikolopoulos,2000 This volume addresses the state of the art and future directions of informatics Several senior researchers and graduate students present their research and work here The purpose of the book is to disseminate the latest scientific engineering and technical information in various fields of informatics It covers a wide range of subjects from theoretical computer science software engineering systems and scientific computing to networking and applied research The book can be used either as a reference for related scientific work or as educational material for advanced computer science courses      18th International Conference on VLSI Design International

Conference on VLSI Design,2005      **Integrated Circuit and System Design** Enrico Macii,Vassilis Paliouras,Odysseas Koufopavlou,2004-09-07 This book constitutes the refereed proceedings of the 14th International Workshop on Power and Timing Optimization and Simulation PATMOS 2004 held in Santorini Greece in September 2004 The 85 revised papers presented together with abstracts of 6 invited presentations were carefully reviewed and selected from 152 papers submitted The papers are organized in topical sections on buses and communication circuits and devices low power issues architectures asynchronous circuits systems design interconnect and physical design security and safety low power processing digital design and modeling and simulation      **Proceedings** ,2002      **Conference Record** ,1997      **Correct Hardware Design and Verification Methods** Tiziana Margaria,Tom F. Melham,2001-08-27 This book constitutes the refereed proceedings of the 11th IFIP WG 10.5 Advanced Research Working Conference on Correct Hardware Design and Verification Methods CHARME 2001 held in Livingston Scotland UK in September 2001 The 28 revised full papers and eight short papers presented together with two invited papers and one special paper were carefully reviewed and selected from 56 submissions The book offers topical sections on model checking clocking issues theorem proving with higher order logics hardware compilation tools component verification case studies algorithm verification and duration calculus      **IEEE International Conference on Electronics, Circuits and Systems** ,1999      **Scientific and Technical Books and Serials in Print** ,1989      **Compound Semiconductor** ,1997      *International Books in Print* ,1986      *Advanced Research in VLSI '97, 17th Conference* ,1997      Proceedings Sixteenth Conference on Advanced Research in VLSI ,1995

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