Reliability and Degradation: Semiconductor Devices and Circuits (Wiley Series in Solid State Devices and Circuits)

Howes, M. J.

Reliability And Degradation Semiconductor Devices And Circuits

Patrick Roblin, Hans Rohdin

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High-Speed Heterostructure Devices Patrick Roblin, Hans Rohdin, 2002-03-07 Fuelled by rapid growth in communications technology silicon heterostructures and related high speed semiconductors are spearheading the drive toward smaller faster and lower power devices High Speed Heterostructure Devices is a textbook on modern high speed semiconductor devices intended for both graduate students and practising engineers This book is concerned with the underlying physics of heterostructures as well as some of the most recent techniques for modeling and simulating these devices Emphasis is placed on heterostructure devices of the immediate future such as the MODFET HBT and RTD The principles of operation of other devices such as the Bloch Oscillator RITD Gunn diode quantum cascade laser and SOI and LD MOSFETs are also introduced Initially developed for a graduate course taught at Ohio State University the book comes with a complete set of homework problems and a web link to MATLAB programs supporting the lecture material *Recent Topics on Modeling of* Semiconductor Processes, Devices, and Circuits Rasit Onur Topaloglu, Peng Li, 2011 The last couple of years have been very busy for the semiconductor industry and researchers The rapid speed of production channel length reduction has brought lithographic challenges to semiconductor modeling These include stress optimization transisto Catalog of National VLSI Design and Test for Bureau of Standards Publications, 1966-1976 United States. National Bureau of Standards, 1978 Systems Dependability Shojiro Asai, 2018-07-20 This book discusses the new roles that the VLSI very large scale integration of semiconductor circuits is taking for the safe secure and dependable design and operation of electronic systems. The book consists of three parts Part I as a general introduction to this vital topic describes how electronic systems are designed and tested with particular emphasis on dependability engineering where the simultaneous assessment of the detrimental outcome of failures and cost of their containment is made This section also describes the related research project Dependable VLSI Systems in which the editor and authors of the book were involved for 8 years Part II addresses various threats to the dependability of VLSIs as key systems components including time dependent degradations variations in device characteristics ionizing radiation electromagnetic interference design errors and tampering with discussion of technologies to counter those threats Part III elaborates on the design and test technologies for dependability in such applications as

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