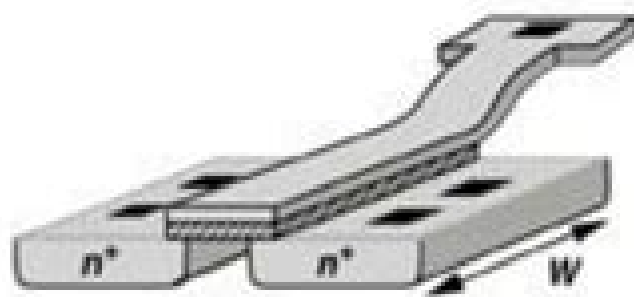
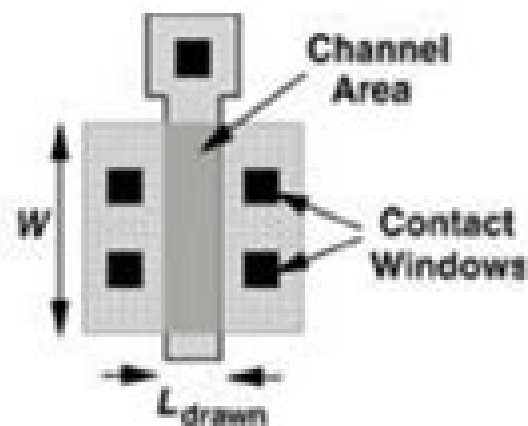


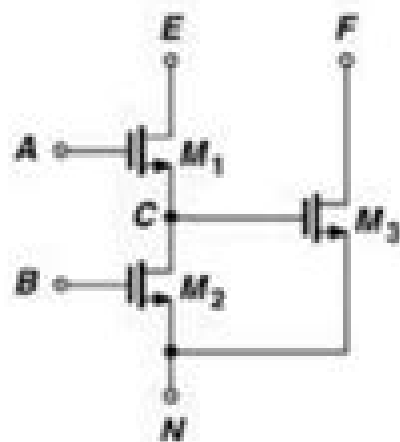
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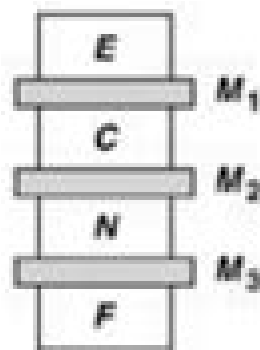
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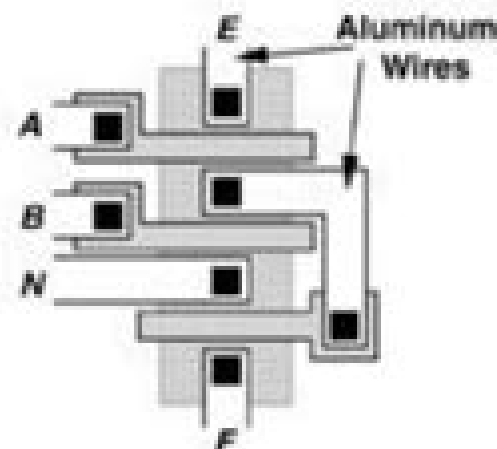
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(a)



(b)



(c)



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T. P. Ma, Paul V. Dressendorfer



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ESD Protection Device and Circuit Design for Advanced CMOS Technologies Oleg Semenov, Hossein Sarbishaei, Manoj Sachdev, 2008-04-26 ESD Protection Device and Circuit Design for Advanced CMOS Technologies is intended for practicing engineers working in the areas of circuit design VLSI reliability and testing domains As the problems associated with ESD failures and yield losses become significant in the modern semiconductor industry the demand for graduates with a basic knowledge of ESD is also increasing Today there is a significant demand to educate the circuits design and reliability teams on ESD issues This book makes an attempt to address the ESD design and implementation in a systematic manner A design procedure involving device simulators as well as circuit simulator is employed to optimize device and circuit parameters for optimal ESD as well as circuit performance This methodology described in ESD Protection Device and Circuit Design for Advanced CMOS Technologies has resulted in several successful ESD circuit design with excellent silicon results and demonstrates its strengths *Ionizing Radiation Effects in MOS Devices and Circuits* T. P. Ma, Paul V. Dressendorfer, 1989-04-18 The first comprehensive overview describing the effects of ionizing radiation on MOS devices as

well as how to design fabricate and test integrated circuits intended for use in a radiation environment Also addresses process induced radiation effects in the fabrication of high density circuits Reviews the history of radiation hard technology providing background information for those new to the field Includes a comprehensive review of the literature and an annotated listing of research activities in radiation hardness research

Statistical Modeling for Computer-Aided Design of MOS VLSI Circuits Christopher Michael, Mohammed Ismail, 2012-12-06 As MOS devices are scaled to meet increasingly demanding circuit specifications process variations have a greater effect on the reliability of circuit performance For this reason statistical techniques are required to design integrated circuits with maximum yield Statistical Modeling for Computer Aided Design of MOS VLSI Circuits describes a statistical circuit simulation and optimization environment for VLSI circuit designers The first step toward accomplishing statistical circuit design and optimization is the development of an accurate CAD tool capable of performing statistical simulation This tool must be based on a statistical model which comprehends the effect of device and circuit characteristics such as device size bias and circuit layout which are under the control of the circuit designer on the variability of circuit performance The distinctive feature of the CAD tool described in this book is its ability to accurately model and simulate the effect in both intra and inter die process variability on analog digital circuits accounting for the effects of the aforementioned device and circuit characteristics Statistical Modeling for Computer Aided Design of MOS VLSI Circuits serves as an excellent reference for those working in the field and may be used as the text for an advanced course on the subject

Compact Transistor Modelling for Circuit Design Henk C. de Graaff, Francois M. Klaassen, 2012-12-06 During the first decade following the invention of the transistor progress in semiconductor device technology advanced rapidly due to an effective synergy of technological discoveries and physical understanding Through physical reasoning a feeling for the right assumption and the correct interpretation of experimental findings a small group of pioneers conceived the major analytic design equations which are currently to be found in numerous textbooks Naturally with the growth of specific applications the description of some characteristic properties became more complicated For instance in integrated circuits this was due in part to the use of a wider bias range the addition of inherent parasitic elements and the occurrence of multi dimensional effects in smaller devices Since powerful computing aids became available at the same time complicated situations in complex configurations could be analyzed by useful numerical techniques Despite the resulting progress in device optimization the above approach fails to provide a required compact set of device design and process control rules and a compact circuit model for the analysis of large scale electronic designs This book therefore takes up the original thread to some extent Taking into account new physical effects and introducing useful but correct simplifying assumptions the previous concepts of analytic device models have been extended to describe the characteristics of modern integrated circuit devices This has been made possible by making extensive use of exact numerical results to gain insight into complicated situations of transistor operation

Low Power

and Low Voltage Circuit Design with the FGMOS Transistor Esther Rodriguez-Villegas, 2006-10-02 Motivated by consumer demand for smaller more portable electronic devices that offer more features and operate for longer on their existing battery packs cutting edge electronic circuits need to be ever more power efficient For the circuit designer this requires an understanding of the latest low voltage and low power LV LP techniques one of the most promising of which makes use of the floating gate MOS FGMOS transistor *The Arts of VLSI Opamp Circuit Design - A Structural Approach Based on Symmetry* Hongjiang Song, 2014-03-21 This text is developed from the notes of a VLSI circuit design class EEE598 the author offered in Engineering School at Arizona State University The materials cover the structural design approaches of VLSI operational amplifier circuits based on the symmetry principle symmetry circuit structures prototype circuits and symmetry scaling transformation techniques **Charge-Based MOS Transistor Modeling** Christian C. Enz, Eric A. Vittoz, 2006-08-14 Modern large scale analog integrated circuits ICs are essentially composed of metal oxide semiconductor MOS transistors and their interconnections As technology scales down to deep sub micron dimensions and supply voltage decreases to reduce power consumption these complex analog circuits are even more dependent on the exact behavior of each transistor High performance analog circuit design requires a very detailed model of the transistor describing accurately its static and dynamic behaviors its noise and matching limitations and its temperature variations The charge based EKV Enz Krummenacher Vittoz MOS transistor model for IC design has been developed to provide a clear understanding of the device properties without the use of complicated equations All the static dynamic noise non quasi static models are completely described in terms of the inversion charge at the source and at the drain taking advantage of the symmetry of the device Thanks to its hierarchical structure the model offers several coherent description levels from basic hand calculation equations to complete computer simulation model It is also compact with a minimum number of process dependant device parameters Written by its developers this book provides a comprehensive treatment of the EKV charge based model of the MOS transistor for the design and simulation of low power analog and RF ICs Clearly split into three parts the authors systematically examine the basic long channel intrinsic charge based model including all the fundamental aspects of the EKV MOST model such as the basic large signal static model the noise model and a discussion of temperature effects and matching properties the extended charge based model presenting important information for understanding the operation of deep submicron devices the high frequency model setting out a complete MOS transistor model required for designing RF CMOS integrated circuits Practising engineers and circuit designers in the semiconductor device and electronics systems industry will find this book a valuable guide to the modelling of MOS transistors for integrated circuits It is also a useful reference for advanced students in electrical and computer engineering

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