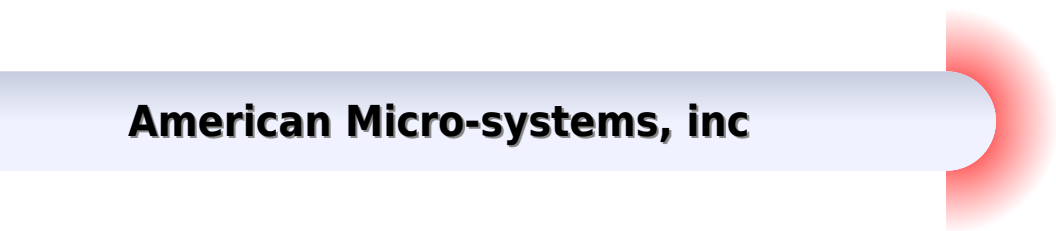


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Semiconductor Devices and Integrated Electronics A. G. Milnes, 2012-12-06 For some time there has been a need for a semiconductor device book that carries diode and transistor theory beyond an introductory level and yet has space to touch on a wider range of semiconductor device principles and applications. Such topics are covered in specialized monographs numbering many hundreds but the voluminous nature of this literature limits access for students. This book is the outcome of attempts to develop a broad course on devices and integrated electronics for university students at about senior year level. The educational prerequisites are an introductory course in semiconductor junction and transistor concepts and a course on analog and digital circuits that has introduced the concepts of rectification, amplification, oscillators, modulation and logic and Switching circuits. The book should also be of value to professional engineers and physicists because of both the information included and the detailed guide to the literature given by the references. The aim has been to bring some measure of order into the subject area examined and to provide a basic structure from which teachers may develop themes that are of most interest to students and themselves. Semiconductor devices and integrated circuits are reviewed and fundamental factors that control power levels, frequency, speed, size and cost are discussed. The text also briefly mentions how devices are used and presents circuits and comments on representative applications. Thus the book seeks a balance between the extremes of device physics and circuit design.

Applied Solid State Science Raymond Wolfe, 2013-10-22 Applied Solid State Science Advances in Materials and Device Research Volume 5 covers articles on devices made with Si, GaAs and CdS and on the photoconductive insulators of the xerographic process. The book discusses design ideas and fabrication techniques that have raised the efficiency of microwave generators as well as xerography and photoinduced discharge characteristics of photoconductive insulators. The text then describes a great variety of devices both unipolar and bipolar which make use of the advantages of ion implantation for introducing impurities into silicon in a most controllable manner. The progress made toward the goal of providing a low cost solar energy conversion device for terrestrial applications is also considered. The book further tackles high efficiency IMPATT diodes, the physics of xerographic photoreceptors, ion implantation in silicon and cadmium sulfide solar cells. Solid state physicists, materials scientists, electrical engineers and graduate students studying the subjects being discussed will find the book invaluable.

Computer-aided Design of Microelectronic Circuits and Systems: Digital-circuit aspects and state of the art A. F. Schwarz, 1987 **Dual Mode Logic** Itamar Levi, Alexander Fish, 2020-12-15 This book presents Dual Mode Logic (DML), a new design paradigm for digital integrated circuits. DML logic gates can operate in two modes, each optimized for a different metric. Its on-the-fly switching between these operational modes at the gate block and system levels provides maximal E/D optimization flexibility. Each highly detailed chapter has

multiple illustrations showing how the DML paradigm seamlessly implements digital circuits that dissipate less energy while simultaneously improving performance and reducing area without a significant compromise in reliability All the facets of the DML methodology are covered starting from basic concepts through single gate optimization general module optimization design trade offs and new ways DML can be integrated into standard design flows using standard EDA tools DML logic is compatible with numerous applications but is particularly advantageous for ultra low power reliable high performance systems and advanced scaled technologies Written in language accessible to students and design engineers each topic is oriented toward immediate application by all those interested in an alternative to CMOS logic Describes a novel promising alternative to conventional CMOS logic known as Dual Mode Logic DML with which a single gate can be operated selectively in two modes each optimized for a different metric e g energy consumption performance size Demonstrates several techniques at the architectural level which can result in high energy savings and improved system performance Focuses on the tradeoffs between power area and speed including optimizations at the transistor and gate level including alternatives to DML basic cells Illustrates DML efficiency for a variety of VLSI applications **Skew-Tolerant Circuit Design** David Harris,2001 Chapter 1 Introduction Chapter 2 Fundamental Concepts Chapter 3 IP Switching Chapter 4 Tag Switching Chapter 5 MPLS Core Protocols Chapter 6 Quality of Service Chapter 7 Constraint based routing Chapter 8 Virtual Private Networks NASA Technical Report ,1976

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