Glossary of IIT Terms



ACCESS POINT (AP)

A device that allows wireless computers and other devices to communicate with a wired



BANDWIDTH

A measurement of the amount of data that can be transmitted over a network at any given time. The higher the network's bandwidth, the greater the volume of data that can be transmitted.



CGI

A Common Gateway Interface is a mechanism used by most web servers to process data received from a client browser. Odi scripts contain the instructions that tell the web server what to do with the data.



BaaS



PING

Packet Internet Groper: a utility used to determine whether a particular computer is currently connected to the internet. It works by sending a packet to the specified IP address and waiting for a reply.



Naas

Network as a service (NaaS) is a business model for delivering enterprise-wide area network services wirtually on a



ACTIVE DIRECTORY



CACHE

A region of computer memory where frequently accessed data can be stored for rapid access.



CLOUD

A network of remote servers hosted on the internet and used to store, manage, and process data in place of local servers or personal computers.



IT ASSESSMENT

The process of gatheri information on part or shole then presented in a detail report. This report analyzes the cunrent spice analyzes the cunrent describes areas needs improvement.



MALWARE

Software programs designed to demage or 50 other unwanted actions on a computer common examples of maleure include viruses, worms, trojan horses, and spyware.



WPN

Virtual Private networking to a means of securely accessing resources on a metwork by connecting to a remote access server through the Internet or other networks.



Semiconductory Terminology Graphic Glossary Of Terms

Ramesh Bangia

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Semiconductor Terminology Beverly Griggs, Anne Miller, Peter Van Zant, Kerry Lopez, 1996-09 Semiconductor **Terminology** B. Griggs, Michael Heynes, A. Miller, P. Van Zant, 1989 Integrated Circuit Manufacturing - Non-Technical References Peter Van Zant, Daniel Gonneau, Beverly Griggs, Warren Landry, Carol Rose, 1990 Demystifying Chipmaking Richard F. Yanda, Michael Heynes, Anne Miller, 2005-06-06 This book takes the reader through the actual manufacturing process of making a typical chip from start to finish including a detailed discussion of each step in plain language The evolution of today s technology is added to the story as seen through the eyes of the engineers who solved some of the problems The authors are well suited to that discussion since they are three of those same engineers. They have a broad exposure to the industry and its technology that extends all the way back to Shockley Laboratories the first semiconductor manufacturer in Silicon Valley The CMOS Complementary Metal Oxide Semiconductor process flow is the focus of the discussion and is covered in ten chapters The vast majority of chips made today are fabricated using this general method In order to ensure that all readers are comfortable with the vocabulary the first chapter carefully and clearly introduces the science concepts found in later chapters A chapter is devoted to pointing out the differences in other manufacturing methods such as the gallium arsenide technology that produces chips for cell phones In addition a chapter describing the nature of the semiconductor industry from a business perspective is included The entire process of making a chip is surprisingly easy to understand The part of the story that defies belief is the tiny dimensions the conducting wires and other structures on a chip are more than a hundred times thinner than a hair and getting thinner with every new chip design Authors are actual engineers who have a broad range of exposure and experience with chip technology Contains a unique chapter describing the nature of the semiconductor industry from a business perspective Microchip Fabrication, 5th Ed. Peter Van Zant, 2004-05-19 The 1 book in the industry for more than 15 years Utilizing a straightforward math free pathology this is a novice friendly guide to the semiconductor fabrication process from raw materials through shipping the finished packaged device Challenging guizzes and review summaries make this the perfect learning guide for technicians in training NEW chapter on nanotechnology NEW sections on 300mm wafer processing Processes and devices and Green processing Every chapter updated to reflect the latest processing techniques **Dictionary of Technical Terms for Aerospace Use** United States. National Aeronautics and Space Administration. Scientific and Technical Information Division, 1965 Brief definitions intended to be as clear as possible to the non expert but accuracy has not been compromised for the sake of readability Mathematics has been used where necessary to avoid ambiguity Intro Published 1965 Semiconductor International ,1989

The Dictionary of Computer Graphics Technology and Applications Roy Latham, 2012-12-06 Superblack supercase supercomputer supersonic superimpose superquadric including superellipsoid superred and the supergreen and superblue superprimaries supersampling supershift superuser Super VGA Super VHS and superwhite are just a few of the words that

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