

A SAFETY LICENSABLE COMPUTING ARCHITECTURE

Application-Program PROM

Master Processor

Data
RAM

output
parameters
and
states

FB-Id
input
parameters
and
states

Function-Block-Firmware ROM
(verification only once)

Slave Processor

Data
RAM

Wolfgang A. Halang

Soon-Hey Jung

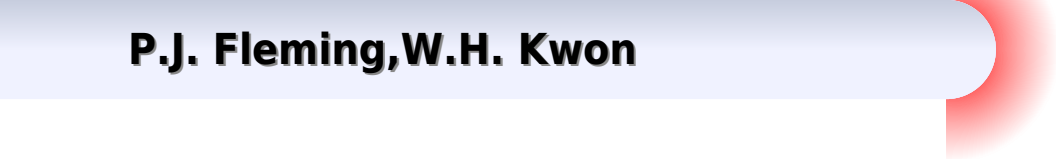
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Safety Licensable Computing Architecture

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