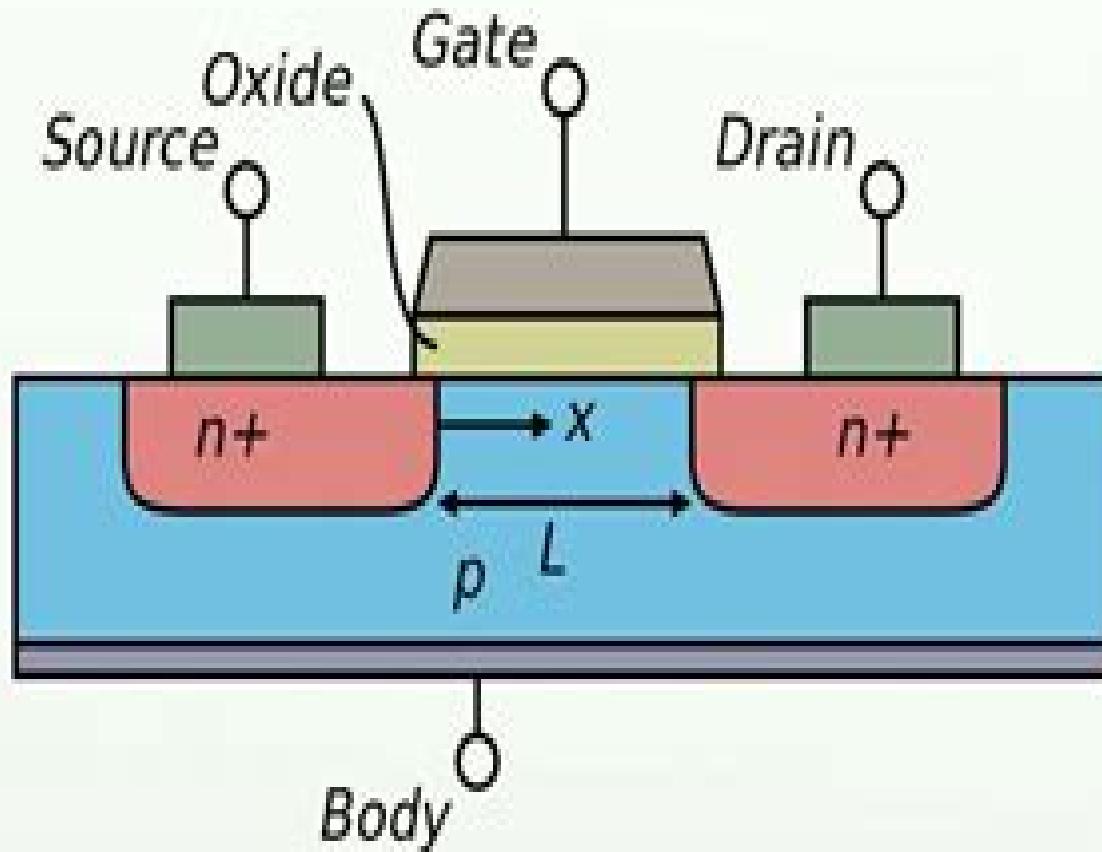


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Field Effect Transistor Kwyro Lee,1983 **Ultra-short Gate Lattice-mismatched Modulation-doped Field-effect Transistors** Guan-Wu Wang,1988 *Field Effect Transistors, A Comprehensive Overview* Pouya Valizadeh,2016-02-23 This book discusses modern day Metal Oxide Semiconductor Field Effect Transistors MOSFETs and future trends of transistor devices This book provides an overview of Field Effect Transistors FETs by discussing the basic principles of FETs and exploring the latest technological developments in the field It covers and connects a wide spectrum of topics related to semiconductor device physics physics of transistors and advanced transistor concepts This book contains six chapters Chapter 1 discusses electronic materials and charge Chapter 2 examines junctions discusses contacts under thermal equilibrium metal semiconductor contacts and metal insulator semiconductor systems Chapter 3 covers traditional planar Metal Oxide Semiconductor Field Effect Transistors MOSFETs Chapter 4 describes scaling driving technological variations and novel dimensions of MOSFETs Chapter 5 analyzes Heterojunction Field Effect Transistors FETs and also discusses the challenges and rewards of heteroepitaxy Finally Chapter 6 examines FETs at molecular scales Links the discussion of contemporary transistor devices to physical processes Material has been class tested in undergraduate and graduate courses on the design of integrated circuit components taught by the author Contains examples and end of chapter problems *Field Effect Transistors A Comprehensive Overview From Basic Concepts to Novel Technologies* is a reference for senior undergraduate graduate students and professional engineers needing insight into physics of operation of modern FETs Pouya Valizadeh is Associate Professor in the Department of Electrical and Computer Engineering at Concordia University in Quebec Canada He received B S and M S degrees with honors from the University of Tehran and Ph D degree from The University of Michigan Ann Arbor all in Electrical Engineering in 1997 1999 and 2005 respectively Over the past decade Dr Valizadeh has taught numerous sections of five different courses covering topics such as semiconductor process technology semiconductor materials and their properties advanced solid state devices transistor design for modern CMOS technology and high speed transistors *Fundamentals of III-V Semiconductor MOSFETs* Serge Oktyabrsky,Peide Ye,2010-03-16 Fundamentals of III V Semiconductor MOSFETs presents the fundamentals and current status of research of compound semiconductor metal oxide semiconductor field effect transistors MOSFETs that are envisioned as a future replacement of silicon in digital circuits The material covered begins with a review of specific properties of III V semiconductors and available technologies making them attractive to MOSFET technology such as band engineered heterostructures effect of strain nanoscale control during epitaxial growth Due to the lack of thermodynamically stable native oxides on III V s such as SiO₂ on Si high k oxides are the natural choice of dielectrics for III V MOSFETs The key challenge of the III V MOSFET technology is a high quality thermodynamically stable gate dielectric that passivates the interface states similar to SiO₂ on Si Several chapters give a detailed description of materials science and electronic behavior of various dielectrics and related interfaces as well as physics of fabricated devices and MOSFET fabrication technologies Topics also include recent progress

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