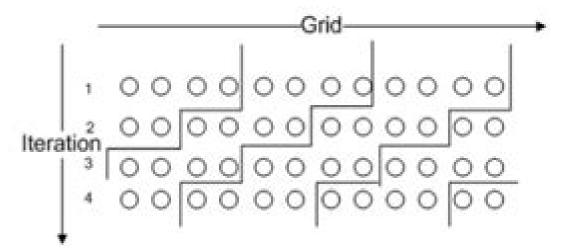
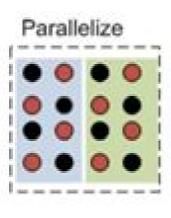
Loop tiling

- Tiling is skewed to satisfy data dependences
- After tiling, parallelism only exists within a tile due to data dependences between tiles







Loop Tiling For Parallelism

Jiyuan Zhang



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