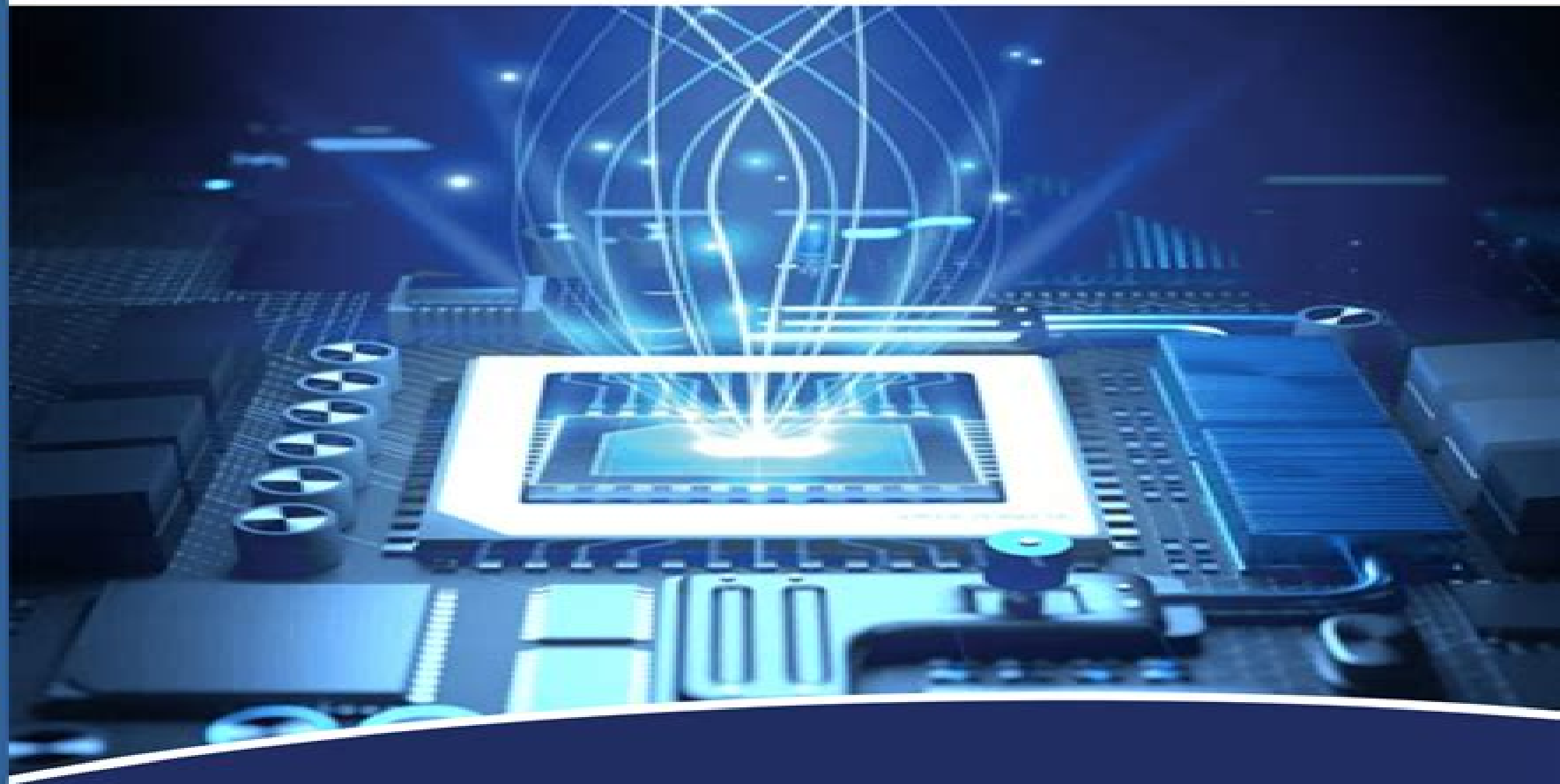


# LOW-POWER VLSI DESIGN TECHNIQUES AND APPLICATIONS

LOW-POWER VLSI DESIGN: TECHNIQUES AND APPLICATIONS



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# Low Power Vlsi Design And Technology

**Gary K. Yeap**



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