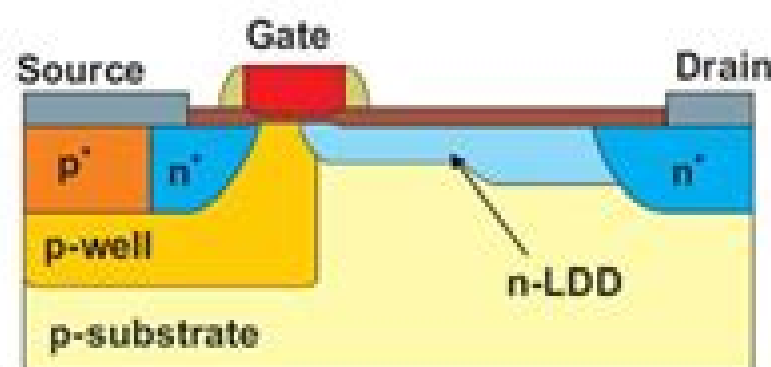


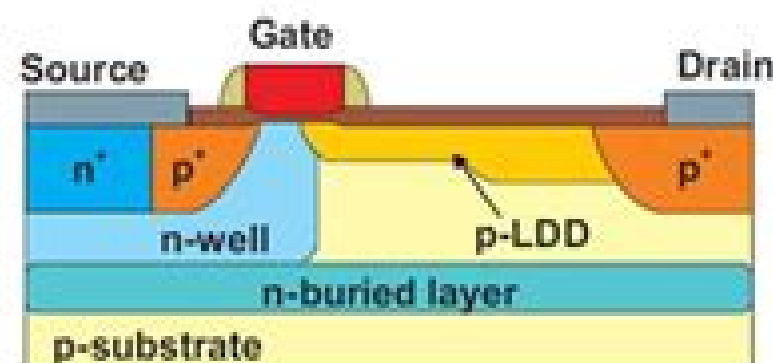
Ionisation Damage in LDMOS Transistors

- ❑ **LDMOS transistors will be used in DC-DC converters for the HL-LHC upgrade**



NLDMOS

$V_{DD} = 22 \text{ V}$, $f_T = 20 \text{ GHz}$



PLDMOS

$V_{DD} = -16 \text{ V}$, $f_T = 10 \text{ GHz}$

- ✓ Transistors from IHP Microelectronics (DE) (0.25 μm SGB25V GOD SiGe BiCMOS)
- ✓ Submitted to neutron irradiation (0.5, 5 and 10 Mrad)
- ❑ **First step:** Technological and electrical simulation of basic cell and fitting with experimental data
- ❑ **Second step:** Electric field distribution (oxides and semiconductors)

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Radiation Effects in Advanced Semiconductor Materials and Devices C. Claeys, E. Simoen, 2013-11-11 In the modern semiconductor industry there is a growing need to understand and combat potential radiation damage problems. Space applications are an obvious case but beyond that today's device and circuit fabrication rely on increasing numbers of processing steps that involve an aggressive environment where inadvertent radiation damage can occur. This book is both aimed at post graduate researchers seeking an overview of the field and will also be immensely useful for nuclear and space engineers and even process engineers. A background knowledge of semiconductor and device physics is assumed but the basic concepts are all briefly summarized. Finally the book outlines the shortcomings of present experimental and modeling techniques and gives an outlook on future developments.

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