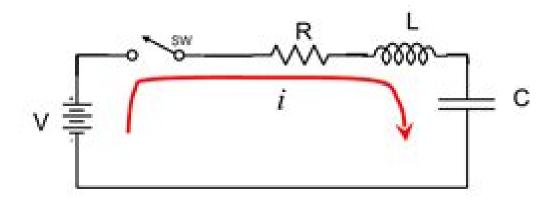
Transient Analysis: Series RLC Circuit

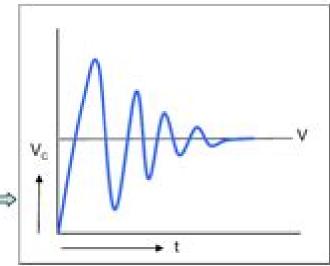


Current in an RLC circuit like shown Is governed by the equation $V = iR + L\frac{di}{dt} + \frac{1}{C} \equiv i dt$

We will analyse the situations with and without The source (V). The stored energy in C or L will force the current.

Once the switch (SW) is closed, after some oscillatory period, current

And voltage will settle. In steady state, Capacitor voltage (V_c) will approach V ===0



Rlc Circuit

James William Nilsson, Susan A. Riedel

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