Research Article

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Analysis of radiation effects of semiconductor devices based on numerical simulation Fermi-Dirac

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Abstract: To study the radiation effect of Fermi-Dirac (F–D) semiconductor devices based on numerical simulation, two methods are used. One is based on the combination of F-D statistical method and computer simulation. The method discusses the influence of temperature and light energy on the carrier number by starting from an intrinsic silicon semiconductor and carries out computer simulation on the carrier number in intrinsic silicon semiconductor. TID Sim, a three-dimensional parallel solver for ionizing radiation effects of semiconductor devices, is developed. The ionization radiation damage of typical metal oxide semiconductor (MOS) FET NMOS and bipolar transistor GLPNP is simulated. It was proved that the variation trend was close to a straight line in the temperature range (278-358 K) studied in this article. The results are consistent with those of the statistical distribution of semiconductor carriers. This method is suitable for calculating the number of semiconductor carriers, and it is an effective method to study the problems related to carrier distribution.

Keywords: F-D statistical method, semiconductor, radiation effect Metal oxide semiconductor (MOS) will degrade its performance parameters when exposed to high-energy ionizing radiation; the typical phenomena are threshold voltage drift and off-state leakage current increase. This is a longterm effect; that is, the phenomenon of effect cannot be eliminated after the device is powered off [1]. The cause of the long-term radiation effect of MOS devices is the formation of trapped charges that are difficult to move in the gate oxide or isolation oxide; this makes the channel surface form an inversion or a leakage path between the source and the drain when no bias is applied. Therefore, the generation of trapped charges, the transport, and final distribution have an important influence on the long-term effect of the device [2]. The Monte Carlo method and the coupling method of the finite volume method are used to simulate the long-term radiation effect caused by the oxide trap charge in the MOS tube. Using the Monte Carlo method to track the random transport process of incident particles, the ionization energy deposition distribution can be obtained, and this distribution is coupled to the drift-diffusion model equation as a source term [3]. To discretize the governing equation using the finite volume method and solve the Poisson equation in gate oxide and silicon together, the carrier continuity equation and the trapping rate equation of gate oxide, in this way, the threshold voltage drift and off-state leakage current of a typical MOS tube after being irradiated can be calculated. Gamma irradiation is a key environmental factor that causes the performance degradation and even permanent failure of satellite electronic systems. There are two basic effects of semiconductor device ionization radiation damage: Total ionizing dose effect, that is, the damage of the device, increases cumulatively with the increase in the total radiation dose, and the enhanced low dose rate effect (enhanced low dose rate sensitivity [ELDRS] effect) [3]. That is, under the condition of a fixed total radiation dose, the damage to

¹ Introduction

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Numerical Analysis For Semiconductor Devices

Joseph Worthy Parks (Jr.)

Numerical Analysis For Semiconductor Devices:

Numerical Analysis for Semiconductor Devices Mamoru Kurata,1982 Numerical Analysis of Semiconductor Devices Pascal Swei Lin Chen,1982 Numerical Analysis of Semiconductor Devices John Gary Shaw,1983 Analysis and Simulation of Semiconductor Devices S. Selberherr,2012-12-06 The invention of semiconductor devices is a fairly recent one considering classical time scales in human life The bipolar transistor was announced in 1947 and the MOS transistor in a practically usable manner was demonstrated in 1960 From these beginnings the semiconductor device field has grown rapidly The first integrated circuits which contained just a few devices became commercially available in the early 1960s Immediately thereafter an evolution has taken place so that today less than 25 years later the manufacture of integrated circuits with over 400 000 devices per single chip is possible Coincident with the growth in semiconductor device development the literature concerning semiconductor device and technology issues has literally exploded In the last decade about 50 000 papers have been published on these subjects The advent of so called Very Large Scale Integration VLSI has certainly revealed the need for a better understanding of basic device behavior The miniaturization of the single transistor which is the major prerequisite for VLSI nearly led to a breakdown of the classical models of semiconductor devices

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Numerical Analysis of Semiconductor Devices NASECODE Conference (1, 1979, Dublin),1981 Numerical analysis of semiconductor devices. Proceedings of the NASECODE Conference; 7,1991 Numerical analysis of semiconductor devices. Proceedings of the NASECODE Conference; 9,1993 Noise in Semiconductor Devices Fabrizio Bonani, Giovanni Ghione, 2013-03-09 The design and optimization of electronic systems often requires appraisal an of the electrical noise generated by active devices and at a technological level the ability to properly design active elements in order to minimize when possible their noise Examples of critical applications are of course receiver front ends in RF and optoelectronic transmission systems but also front end stages in sensors and in a completely different context nonlinear circuits such as oscillators mixers and frequency multipliers The rapid de velopment of silicon RF applications has recently fostered the interest toward low noise silicon devices for the lower microwave band such as low noise MOS transistors at the same time the RF and microwave ranges are be coming increasingly important in fast optical communication systems Thus high frequency noise modeling and simulation of both silicon and compound semiconductor based bipolar and field effect

transistors can be considered as an important and timely topic This does not exclude of course low frequency noise which is relevant also in the RF and microwave ranges when ever it is up converted within a nonlinear system either autonomous as an oscillator or non autonomous as a mixer or frequency multiplier The aim of the present book is to provide a thorough introduction to the physics based numerical modeling of semiconductor devices operating both in small signal and in large signal conditions In the latter instance only the non autonomous case was considered and thus the present treatment does not directly extend to oscillators **NASECODE**,1983 Nasecode IV John James Henry Miller,1985 Nasecode IX John J. H. Miller,1993-04 Numerical Analysis of Semiconductor Devices John Gary Shaw,1983 NASECODE X John J. H. Miller,1994 Microscopic Numerical Analysis of Semiconductor Devices with Application to Avalnache Photodiodes Joseph Worthy Parks (Jr.),1997

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Table of Contents Numerical Analysis For Semiconductor Devices

- 1. Understanding the eBook Numerical Analysis For Semiconductor Devices
 - The Rise of Digital Reading Numerical Analysis For Semiconductor Devices
 - Advantages of eBooks Over Traditional Books
- 2. Identifying Numerical Analysis For Semiconductor Devices
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
- 3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Numerical Analysis For Semiconductor Devices
 - User-Friendly Interface
- 4. Exploring eBook Recommendations from Numerical Analysis For Semiconductor Devices
 - Personalized Recommendations
 - Numerical Analysis For Semiconductor Devices User Reviews and Ratings
 - Numerical Analysis For Semiconductor Devices and Bestseller Lists
- 5. Accessing Numerical Analysis For Semiconductor Devices Free and Paid eBooks
 - Numerical Analysis For Semiconductor Devices Public Domain eBooks
 - Numerical Analysis For Semiconductor Devices eBook Subscription Services
 - Numerical Analysis For Semiconductor Devices Budget-Friendly Options

- 6. Navigating Numerical Analysis For Semiconductor Devices eBook Formats
 - o ePub, PDF, MOBI, and More
 - Numerical Analysis For Semiconductor Devices Compatibility with Devices
 - Numerical Analysis For Semiconductor Devices Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Numerical Analysis For Semiconductor Devices
 - Highlighting and Note-Taking Numerical Analysis For Semiconductor Devices
 - Interactive Elements Numerical Analysis For Semiconductor Devices
- 8. Staying Engaged with Numerical Analysis For Semiconductor Devices
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Numerical Analysis For Semiconductor Devices
- 9. Balancing eBooks and Physical Books Numerical Analysis For Semiconductor Devices
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Numerical Analysis For Semiconductor Devices
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Numerical Analysis For Semiconductor Devices
 - Setting Reading Goals Numerical Analysis For Semiconductor Devices
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Numerical Analysis For Semiconductor Devices
 - Fact-Checking eBook Content of Numerical Analysis For Semiconductor Devices
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements

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