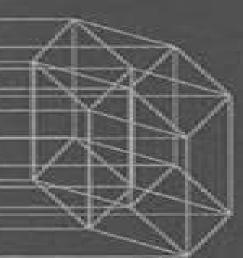
Mathematical DESIGN

Building Reliable Complex Computer Systems

J. PAUL ROTH



IEEE Press Series on Engineering of Complex Computer Systems Philip A. Lophanto and Alexander D. Skoyen, Series Editors

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Logic Design of NanoICS Svetlana N. Yanushkevich, Vlad P. Shmerko, Sergey Edward Lyshevski, 2017-12-19 Today s engineers will confront the challenge of a new computing paradigm relying on micro and nanoscale devices Logic Design of NanoICs builds a foundation for logic in nanodimensions and guides you in the design and analysis of nanoICs using CAD The authors present data structures developed toward applications rather than a purely theoretical treatment Requiring only basic logic and circuits background Logic Design of NanoICs draws connections between traditional approaches to design and modern design in nanodimensions The book begins with an introduction to the directions and basic methodology of logic design at the nanoscale then proceeds to nanotechnologies and CAD graphical representation of switching functions and networks word level and linear word level data structures 3 D topologies based on hypercubes multilevel circuit design and fault tolerant computation in hypercube like structures. The authors propose design solutions and techniques going beyond the underlying technology to provide more applied knowledge This design oriented reference is written for engineers interested in developing the next generation of integrated circuitry illustrating the discussion with approximately 250 figures and tables 100 equations 250 practical examples and 100 problems Each chapter concludes with a summary references and a suggested reading section Artificial Intelligence in Logic Design Svetlana N. Yanushkevich, 2013-03-19 There are three outstanding points of this book First for the first time a collective point of view on the role of artificial intelligence paradigm in logic design is introduced Second the book reveals new horizons of logic design tools on the technologies of the near future Finally the contributors of the book are twenty recognizable leaders in the field from the seven research centres The chapters of the book have been carefully reviewed by equally qualified experts All contributors are experienced in practical electronic design and in teaching engineering courses Thus the book s style is accessible to graduate students practical engineers and researchers Decision Diagram Techniques for Micro- and Nanoelectronic Design Handbook Svetlana N. Yanushkevich, D. Michael Miller, Vlad P. Shmerko, Radomir S. Stankovic, 2018-10-03 Decision diagram DD techniques are very popular in the electronic design automation EDA of integrated circuits and for good reason They can accurately simulate logic design can show where to make reductions in complexity and can be easily modified to model different scenarios Presenting DD techniques from an applied perspective Decision Diagram Techniques for Micro and Nanoelectronic Design Handbook provides a comprehensive up to date collection of DD techniques Experts with more than forty years of combined experience in both industrial and academic settings demonstrate how to apply the techniques to full advantage with more than 400 examples and illustrations Beginning with the fundamental theory data structures and logic underlying DD techniques they explore a breadth of topics from arithmetic and word level representations to spectral techniques and event driven analysis The book also includes abundant references to more detailed information and additional applications Decision Diagram Techniques for Micro and Nanoelectronic Design Handbook collects the theory methods and practical knowledge

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