

6. Specify the size of data, address, memory word and memory capacity of 8085 microprocessor.

S.No	Components of 8085	Size
1	Data	8-bit
2	Address	16-bit
3	Memory word	8-bit
4	Memory capacity	64 KB

7. List the five interrupt pins available in 8085

There are 6 interrupt pins in the microprocessor used as Hardware Interrupts given below:

- TRAP
- RST7.5
- RST6.5
- RST5.5
- INTR
- INTA is not an interrupt.

INTA is used by the Microprocessor for sending the acknowledgement. TRAP has highest priority and RST7.5 has second highest priority and so on.

8. What is ALE ?

ALE is a control signal of 8085 MPU. 8085 is a 8-bit microprocessor which have 16-bit address bus among them lower 8-bit are multiplexed. So while MPU executes read or write operation it needs to demultiplexed. ALE control signal used to demultiplexed the lower order address and data bus.

9. To obtain a 320 ns clock, what should be the input clock frequency? What is the frequency of clock signal at CLK OUT ?

Given : $T = 320 \text{ ns}$. We know that $f = 1/T$

$$f = 1/320 \times 10^{-9} = 10^9/320 \\ = 3.125 \text{ MHz}$$

The input clock frequency = $2 \times 3.125 \text{ MHz} = 6.25 \text{ MHz}$

The frequency of clock signal at CLK OUT = 3.125 MHz

10. What is meant by level triggered interrupt? Which of the interrupts in 8085 are level triggered?

An **interrupt** is a signal to the processor emitted by hardware or software indicating an event that needs immediate attention.

A *level-triggered interrupt* is an interrupt signaled by maintaining the interrupt line at a high or low level. A device wishing to signal a Level-triggered interrupt drives the interrupt request line to its active level (high or low), and then holds it at that level until it is serviced.

TRAP interrupt is edge and level triggered. The RST 6.5 and RST 5.5 both are level triggered. INTR also a level sensitive interrupt.

11. Why data bus is bi-directional?

The microprocessor has to fetch (read) the data from memory or input device for processing and after processing, it has to store (write) the data to memory or output device. Hence the data bus is bi-directional.

12. Distinguish between I/O mapped I/O and memory mapped I/O .

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Steve Money



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Semiconductor Devices and Integrated Electronics A. G. Milnes, 2012-12-06 For some time there has been a need for a semiconductor device book that carries diode and transistor theory beyond an introductory level and yet has space to touch on a wider range of semiconductor device principles and applications. Such topics are covered in specialized monographs numbering many hundreds but the voluminous nature of this literature limits access for students. This book is the outcome of attempts to develop a broad course on devices and integrated electronics for university students at about senior year level. The educational prerequisites are an introductory course in semiconductor junction and transistor concepts and a course on analog and digital circuits that has introduced the concepts of rectification, amplification, oscillators, modulation and logic and switching circuits. The book should also be of value to professional engineers and physicists because of both the information included and the detailed guide to the literature given by the references. The aim has been to bring some measure of order into the subject area examined and to provide a basic structure from which teachers may develop themes that are of most interest to students and themselves. Semiconductor devices and integrated circuits are reviewed and fundamental factors that control power levels, frequency, speed, size and cost are discussed. The text also briefly mentions how devices are used and presents circuits and comments on representative applications. Thus the book seeks a balance between the extremes of device physics and circuit design.

Newnes Interfacing Companion Tony Fischer-Cripps, 2002-08-05 Tony Fischer-Cripps is a Project Leader in the Division of Telecommunications and Industrial Physics of the CSIRO Commonwealth Scientific Industrial Research Organisation Australia. He was previously lecturer University of Technology Sydney UTS Australia and has also worked for the National Institute of Standards and Technology USA NIST formerly National Bureau of Standards NBS. The essential pocket reference for engineers and students. Interfacing in action: PCs, PLCs, transducers and instrumentation in one book. Develop systems and applications that work with Newnes Interfacing Companion. *The British National Bibliography* Arthur James Wells, 1992. Interfacing to Microprocessors and Microcomputers Owen Bishop, 1982.

Electronics & Wireless World, 1985. **Computer Publishers & Publications**, 1988. *Microprocessor Based Systems for the Higher Technician* R.E. Vears, 2016-01-29. Microprocessor Based Systems for the Higher Technician provides coverage of the BTEC level 4 unit in Microprocessor Based Systems syllabus U80 674. This book is composed of 10 chapters and concentrates on the development of 8 bit microcontrollers specifically constructed around the Z80 microprocessor. The design cycle for the development of such a microprocessor based system and the use of a disk based development system MDS as an aid to design are both described in detail. The book deals with the Control Program Monitor (CP/M) operating system and gives background information on file handling. Programming is given attention through a thorough explanation of software development tools and the use of macros. Choosing devices from the Z80 family of processors, the author explains hardware development including topics on basic circuits for each stage of development in resonance with the applicable data.

sheets When software and hardware are to be integrated and function efficiently a technique called emulation may prove useful hence it is also described The book ends with troubleshooting or fault location especially for computer systems that are still under development and riddled with bugs Troubleshooting or fault location which is considered an acquired skill is improved with discussions on basic techniques principles of operation and the equipment needed for a successful diagnosis and solution of the problem Software engineers computer technicians computer engineers teachers and instructors in the field of computing learning will find this book very instructive The book can also be read by computer enthusiasts who desire to have an advanced technical know how and understanding of computer hardware and systems **Bowker/Bantam ...**

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American Book Publishing Record ,1996 *Newnes Microprocessor Pocket Book* Steve Money,2014-05-12 Newnes Microprocessor Pocket Book explains the basic hardware operation of a microprocessor and describes the actions of the various types of instruction that can be executed A summary of the characteristics of many of the popular microprocessors is presented Apart from the popular 8 and 16 bit microprocessors some details are also given of the popular single chip microcomputers and of the reduced instruction set computer RISC type processors such as the Transputer Novix FORTH processor and Acorn ARM processor Comprised of 15 chapters this book discusses the principles involved in both parallel and serial input output interfaces and gives details of the common standards used for parallel and serial input output systems Although discrete logic can be used for input output interfaces most microprocessor based systems use specially developed integrated circuits for this purpose Examples of these special interface chips are described with details of their internal arrangement and the basic techniques for programming their modes of operation This covers parallel and serial input output chips counters and timers as well as one or two of the multifunction peripheral chips that are available Data formats instruction sets display systems and system development are also considered This monograph will be of interest to students and to anyone involved in designing servicing or just wishing to learn more about microprocessor based systems

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web feb 24 2014 blackberry ltd said it will make its messaging service bbm available to microsoft s windows phone and the

web on 24 february 2014 blackberry officially confirmed bbm for windows phone and nokia x would be released by q2 2014
nokia confirmed bbm would be preinstalled on nokia x devices as of june 2016 bbm was no longer offered on the windows
store