

Reduced Instruction Set Computer Architectures

Introduction

- To provide performance for programs written in HLLs: simpler implementation without the use of microcode
- IBM in 1975 : minicomputer known as the 801 (ECL)
- Berkeley in 1980 : VLSI processors RISC I, RISC II
- Stanford in 1981 : MIPS chip

The Effectiveness of a Computer Architecture

- How well it can be used to carry out an application and the performance attainable by implementations of that architecture in various technology
- The performance of an implementation running a particular benchmark

$$Performance = \frac{1}{\text{No of Instructions} \times \text{cycle time} \times \frac{\text{cycles}}{\text{Instruction}}}$$

Reduced Instruction Set Computer Architectures For Vlsi

ML Morrison



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