

SEMICONDUCTOR MEMORIES

A Handbook of Design,
Manufacture, and Application
SECOND EDITION

Betty Prince



Semiconductor Memories A Handbook Of Design Manufacture And Application

**Giuseppe Ferla, Luigi Fortuna, Antonio
Imbruglia**



Semiconductor Memories A Handbook Of Design Manufacture And Application:

Semiconductor Memories ,1992-04-01 This handbook gives a complete overview to the design manufacture and application of semiconductor memory technology The move towards application specific memories and the new developments in memory applications including smart memories computers and networks are explored The author also details the new trends in memory architecture and functionality such as multiplexing video memory architecture and error correction For embedded memories there is an explanation of the advantages and disadvantages testing methods and reliability Detailed consideration is given to future trends in memories VLSI technology commodity memories smart cards smart houses CIM speech synthesis and recognition RISC architecture neural processors and superconductors source 4 me de couv

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VLSI Test Principles and Architectures Laung-Terng Wang,Cheng-Wen Wu,Xiaoqing Wen,2006-08-14 This book is a comprehensive guide to new DFT methods that will show the readers how to design a testable and quality product drive down test cost improve product quality and yield and speed up time to market and time to volume Most up to date coverage of design for testability Coverage of industry practices commonly found in commercial DFT tools but not discussed in other books

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Technology Atlas uses examples and TEM Transmission Electron Microscopy micrographs to explain and illustrate ULSI process technologies and their associated problems. The first book available on the subject to be illustrated using TEM images. ULSI Semiconductor Technology Atlas is logically divided into four parts. Part I includes basic introductions to the ULSI process, device construction, analysis, and TEM sample preparation. Part II focuses on key ULSI modules: ion implantation and defects, dielectrics and isolation structures, silicides, salicides, and metallization. Part III examines integrated devices including complete planar DRAM, stacked cell DRAM, and trench cell DRAM, as well as SRAM, as examples for process integration and development. Part IV emphasizes special applications including TEM in advanced failure analysis, TEM in advanced packaging development, and UBM (Under Bump Metallization) studies and high resolution TEM in microelectronics. This innovative guide also provides engineers and managers in the microelectronics industry as well as graduate students with more than 1,100 TEM images to illustrate the science of ULSI. A historical introduction to the technology as well as coverage of the evolution of basic ULSI process problems and issues. Discussion of TEM in other advanced microelectronics devices and materials such as flash memories, SOI, SiGe devices, MEMS, and CD ROMs.

Digital Design and Fabrication
Vojin G. Oklobdzija, 2017-12-19

In response to tremendous growth and new technologies in the semiconductor industry, this volume is organized into five information-rich sections. Digital Design and Fabrication surveys the latest advances in computer architecture and design as well as the technologies used to manufacture and test them. Featuring contributions from leading experts, the book also includes a new section on memory and storage, in addition to a new chapter on nonvolatile memory technologies. Developing advanced concepts, this sharply focused book describes new technologies that have become driving factors for the electronic industry. Includes new information on semiconductor memory circuits whose development best illustrates the phenomenal progress encountered by the fabrication and technology sector. Contains a section dedicated to issues related to system power consumption. Describes reliability and testability of computer systems. Pinpoints trends and state of the art advances in fabrication and CMOS technologies. Describes performance evaluation measures which are the bottom line from the user's point of view. Discusses design techniques used to create modern computer systems including high speed computer arithmetic and high frequency design, timing and clocking, and PLL and DLL design.

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