

# SEMICONDUCTING DEVICES

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A Bibliography of  
Fabrication Technology,  
Properties, and Applications

# Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications

**Daniel F McAuley**



## **Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications:**

Semiconducting Devices A. H. Agajanian, 1976      **Semiconducting Devices** A. H. Agajanian, 2013-05-14 Since the advent of planar technology the literature on semiconductor devices has been growing exponentially This book of over 14000 references is intended to make the world literature available to workers in the field beginners as well as experienced to assist them in finding out what has been done by others in their fields of endeavor The literature on theory preparation properties characterization packaging instrumentation and applications of semiconductor devices is thoroughly covered the only aspect of preparation not covered is diffusion However several books and comprehensive review articles on this subject are given in Chapter A Radiation damage due to ion implantation is given in Chapter F Radiation damage due to all other types of radiations is given in Chapter J The chapters on processing technology radiation damage structural defects and analysis modeling and packaging are quite general and apply to most types of semiconductor devices Special attention is given to electrical properties and applications of FET Schottky and charge coupled devices in Chapters M N O and P

**Semiconductor Devices and Integrated Electronics** A. G. Milnes, 2012-12-06 For some time there has been a need for a semiconductor device book that carries diode and transistor theory beyond an introductory level and yet has space to touch on a wider range of semiconductor device principles and applications Such topics are covered in specialized monographs numbering many hundreds but the voluminous nature of this literature limits access for students This book is the outcome of attempts to develop a broad course on devices and integrated electronics for university students at about senior year level The educational prerequisites are an introductory course in semiconductor junction and transistor concepts and a course on analog and digital circuits that has introduced the concepts of rectification amplification oscillators modulation and logic and Switching circuits The book should also be of value to professional engineers and physicists because of both the information included and the detailed guide to the literature given by the references The aim has been to bring some measure of order into the subject area examined and to provide a basic structure from which teachers may develop themes that are of most interest to students and themselves Semiconductor devices and integrated circuits are reviewed and fundamental factors that control power levels frequency speed size and cost are discussed The text also briefly mentions how devices are used and presents circuits and comments on representative applications Thus the book seeks a balance between the extremes of device physics and circuit design      **Transport in Metal-Oxide-Semiconductor Structures** Hamid Bantarzi, 2011-01-12

This book focuses on the importance of mobile ions presented in oxide structures what significantly affects the metal oxide semiconductor MOS properties The reading starts with the definition of the MOS structure its various aspects and different types of charges presented in their structure A review on ionic transport mechanisms and techniques for measuring the mobile ions concentration in the oxides is given special attention being attempted to the Charge Pumping CP technique associated with the Bias Thermal Stress BTS method Theoretical approaches to determine the density of mobile ions as well

as their distribution along the oxide thickness are also discussed The content varies from general to very specific examples helping the reader to learn more about transport in MOS structures *Semiconductor Lithography* Wayne M.

Moreau,2012-12-06 Semiconductor lithography is one of the key steps in the manufacturing of integrated silicon based circuits In fabricating a semiconductor device such as a transistor a series of hot processes consisting of vacuum film deposition oxidations and dopant implantation are all patterned into microscopic circuits by the wet processes of lithography Lithography as adopted by the semiconductor industry is the process of drawing or printing the pattern of an integrated circuit in a resist material The pattern is formed and overlayed to a previous circuit layer as many as 30 times in the manufacture of logic and memory devices With the resist pattern acting as a mask a permanent device structure is formed by subtractive removal etching or by additive deposition of metals or insulators Each process step in lithography uses inorganic or organic materials to physically transform semiconductors of silicon insulators of oxides nitrides and organic polymers and metals into useful electronic devices All forms of electromagnetic radiation are used in the processing Lithography is a multidisciplinary science of materials processes and equipment interacting to produce three dimensional structures Many aspects of chemistry electrical engineering materials science and physics are involved The purpose of this book is to bring together the work of many scientists and engineers over the last 10 years and focus upon the basic resist materials the lithographic processes and the fundamental principles behind each lithographic process Proceedings of the Eighth

International Conference on Chemical Vapor Deposition J. M. Blocher, Guy E. Vuillard, Georg Wahl, 1981 **Thin Films by Chemical Vapour Deposition** C.E. Morosanu, 2016-06-22 The explosive growth in the semiconductor industry has caused a rapid evolution of thin film materials that lend themselves to the fabrication of state of the art semiconductor devices Early in the 1960s an old research technique named chemical vapour phase deposition CVD which has several unique advantages developed into the most widely used technique for thin film preparation in electronics technology In the last 25 years tremendous advances have been made in the science and technology of thin films prepared by means of CVD This book presents in a single volume an up to date overview of the important field of CVD processes which has never been completely reviewed previously Contents Part I 1 Evolution of CVD Films Introductory remarks Short history of CVD thin films II Fundamentals 2 Techniques of Preparing Thin Films Electrolytic deposition techniques Vacuum deposition techniques Plasma deposition techniques Liquid phase deposition techniques Solid phase deposition techniques Chemical vapour conversion of substrate Chemical vapour deposition Comparison between CVD and other thin film deposition techniques 3 Chemical Processes Used in CVD Introduction Description of chemical reactions used in CVD 4 Thermodynamics of CVD Feasibility of a CVD process Techniques for equilibrium calculations in CVD systems Examples of thermodynamic studies of CVD systems 5 Kinetics of CVD Steps and control type of a CVD heterogeneous reaction Influence of experimental parameters on thin film deposition rate Continuous measurement of the deposition rate Experimental methods for studying

CVD kinetics Role of homogeneous reactions in CVD Mechanism of CVD processes Kinetics and mechanism of dopant incorporation Transport phenomena in CVD Status of kinetic and mechanism investigations in CVD systems 6 Measurement of Thin Film Thickness Mechanical methods Mechanical optical methods Optical methods Electrical methods Miscellaneous methods 7 Nucleation and Growth of CVD Films Stages in the nucleation and growth mechanism Regimes of nucleation and growth Nucleation theory Dependence of nucleation on deposition parameters Heterogeneous nucleation and CVD film structural forms Homogeneous nucleation Experimental techniques Experimental results of CVD film nucleation 8 Thin Film Structure Techniques for studying thin film structure Structural defects in CVD thin films 9 Analysis of CVD Films Analysis techniques of thin film bulk Analysis techniques of thin film surfaces Film composition measurement Depth concentration profiling 10 Properties of CVD Films Mechanical properties Thermal properties Optical properties Photoelectric properties Electrical properties Magnetic properties Chemical properties Part III 11 Equipment and Substrates Equipment for CVD Safety in CVD Substrates 12 Preparation and Properties of Semiconducting Thin Films Homoepitaxial semiconducting films Heteroepitaxial semiconducting films 13 Preparation and Properties of Amorphous Insulating Thin Films Oxides Nitrides and Oxy-nitrides Polymeric thin films 14 Preparation and Properties of Conductive Thin Films Metals and metal alloys Resistor materials Transparent conducting films Miscellaneous materials 15 Preparation and Properties of Superconducting and Magnetic Thin Films Superconducting materials Magnetic materials 16 Uses of CVD Thin Films Applications in electronics and microelectronics Applications in the field of microwaves and optoelectronics Miscellaneous applications Artificial heterostructures Quantum wells superlattices monolayers two dimensional electron gases Part V 17 Present and Future Importance of CVD Films      *Walford's Guide to Reference Material: Science & technology* Albert John Walford, Anthony P. Harvey, H. Drubba, 1980      **VLSI System Design** Saburo Muroga, 1982-08-11 An overview of LSI VLSI systems that brings together all their engineering aspects with economical considerations such as production volume economy yield economy chip pricing and custom design methodology Offers clear concise explanations of how to design LSI VLSI chips and what advantages and disadvantages accompany their use The well illustrated text includes worked examples as well as extensive references for further study      Bibliographies of Interest to the Atomic Energy Program, 1962 Through 1966 U.S. Atomic Energy Commission, 1968      **Electrocomponent Science and Technology**, 1978      Reference Quarterly, 1978

**Microelectronic Packaging**, 1979 Good No Highlights No Markup all pages are intact Slight Shelfwear may have the corners slightly dented may have slight color changes slightly damaged spine      Nanoscale Science and Technology Robert Kelsall, Ian W. Hamley, Mark Geoghegan, 2005-11-01 Nanotechnology is a vital new area of research and development addressing the control modification and fabrication of materials structures and devices with nanometre precision and the synthesis of such structures into systems of micro and macroscopic dimensions Future applications of nanoscale science and technology include motors smaller than the diameter of a human hair and single celled organisms programmed to fabricate

materials with nanometer precision Miniaturisation has revolutionised the semiconductor industry by making possible inexpensive integrated electronic circuits comprised of devices and wires with sub micrometer dimensions These integrated circuits are now ubiquitous controlling everything from cars to toasters The next level of miniaturisation beyond sub micrometer dimensions into nanoscale dimensions invisible to the unaided human eye is a booming area of research and development This is a very hot area of research with large amounts of venture capital and government funding being invested worldwide as such Nanoscale Science and Technology has a broad appeal based upon an interdisciplinary approach covering aspects of physics chemistry biology materials science and electronic engineering Kelsall et al present a coherent approach to nanoscale sciences which will be invaluable to graduate level students and researchers and practising engineers and product designers

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## **Table of Contents Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications**

1. Understanding the eBook Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
  - The Rise of Digital Reading Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
  - Advantages of eBooks Over Traditional Books
2. Identifying Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
  - User-Friendly Interface
4. Exploring eBook Recommendations from Semiconducting Devices A Bibliography Of Fabrication Technology Properties

### And Applications

- Personalized Recommendations
  - Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications User Reviews and Ratings
  - Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications and Bestseller Lists
5. Accessing Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications Free and Paid eBooks
- Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications Public Domain eBooks
  - Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications eBook Subscription Services
  - Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications Budget-Friendly Options
6. Navigating Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications eBook Formats
- ePub, PDF, MOBI, and More
  - Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications Compatibility with Devices
  - Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications Enhanced eBook Features
7. Enhancing Your Reading Experience
- Adjustable Fonts and Text Sizes of Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
  - Highlighting and Note-Taking Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
  - Interactive Elements Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
8. Staying Engaged with Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
- Joining Online Reading Communities

- Participating in Virtual Book Clubs
- Following Authors and Publishers Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
- 9. Balancing eBooks and Physical Books Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
- 10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
- 11. Cultivating a Reading Routine Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
  - Setting Reading Goals Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
  - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
  - Fact-Checking eBook Content of Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications
  - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
  - Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
- 14. Embracing eBook Trends
  - Integration of Multimedia Elements
  - Interactive and Gamified eBooks

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