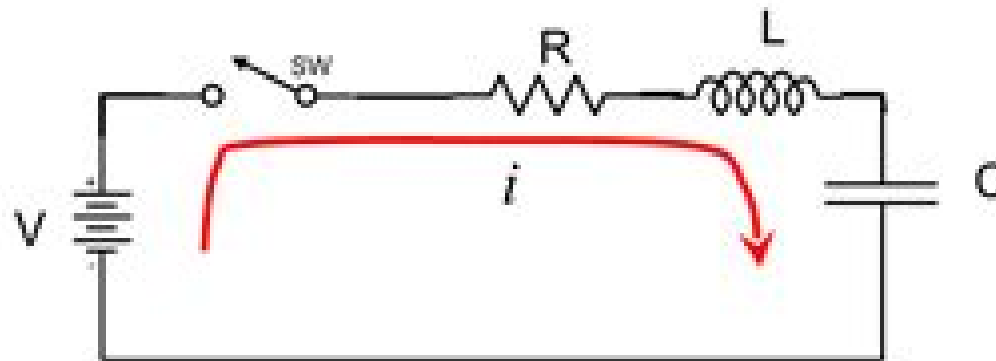


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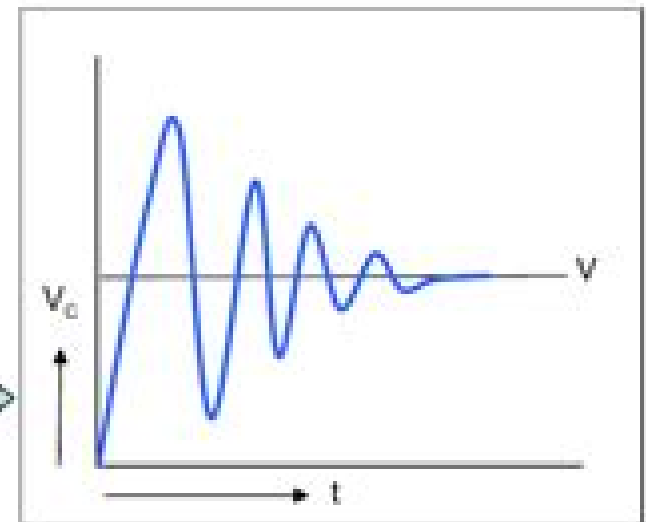


Current in an RLC circuit like shown is governed by the equation

$$\Rightarrow V = iR + L \frac{di}{dt} + \frac{1}{C} \int i dt$$

We will analyse the situations with and without The source (V). The stored energy in C or L will force the current

Once the switch (SW) is closed, after some oscillatory period, current And voltage will settle. In steady state, Capacitor voltage ( $V_C$ ) will approach V  $\Rightarrow$



# Rlc Circuit

**Djafar K. Mynbaev**



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