

ROADMAP TO

RELIABILITY EXCELLENCE

PART 4: DESIGN FOR RELIABILITY



Reliability For The Technologies

Paul Kales

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critical design aspects for analog and powerintegrated circuits are combined The first design aspect coversanalog circuit design techniques to achieve the desired circuitperformance. The second and main aspect presents the additional challenges associated with the design of adequate and effective ESDprotection elements and schemes A comprehensive list of practical application examples is used to demonstrate the successful combination of both techniques and any potential designtrade offs Chapter One looks at analog design discipline including layout and analog matching and analog layout design practices Chapter Twodiscusses analog design with circuits examining singletransistor amplifiers multi transistor amplifiers active loadsand more The third chapter covers analog design layout alsoMOSFET layout before Chapters Four and Five discuss analog designsynthesis The next chapters introduce the reader to analog digitalmixed signal design synthesis analog signal pin ESD networks and analog ESD power clamps Chapter Nine the last chapter covers ESD design in analog applications Clearly describes analog design fundamentals circuitfundamentals as well as outlining the various ESDimplications Covers a large breadth of subjects and technologies such asCMOS LDMOS BCD SOI and thick body SOI Establishes an ESD analog design discipline that distinguishes itself from the alternative ESD digital design focus Focuses on circuit and circuit design applications Assessible with the artwork and tutorial style of the ESD bookseries PowerPoint slides are available for university facultymembers Even in the world of digital circuits analog and power circuits are two very important but under addressed topics especially from the ESD aspect Dr Voldman's new book will serve as an essential and practical guide to the greater IC community Withhigh practical and academic values this book is a bible for professionals graduate students deviceand circuit designers for investigating the physics of ESD and forproduct designs and testing

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