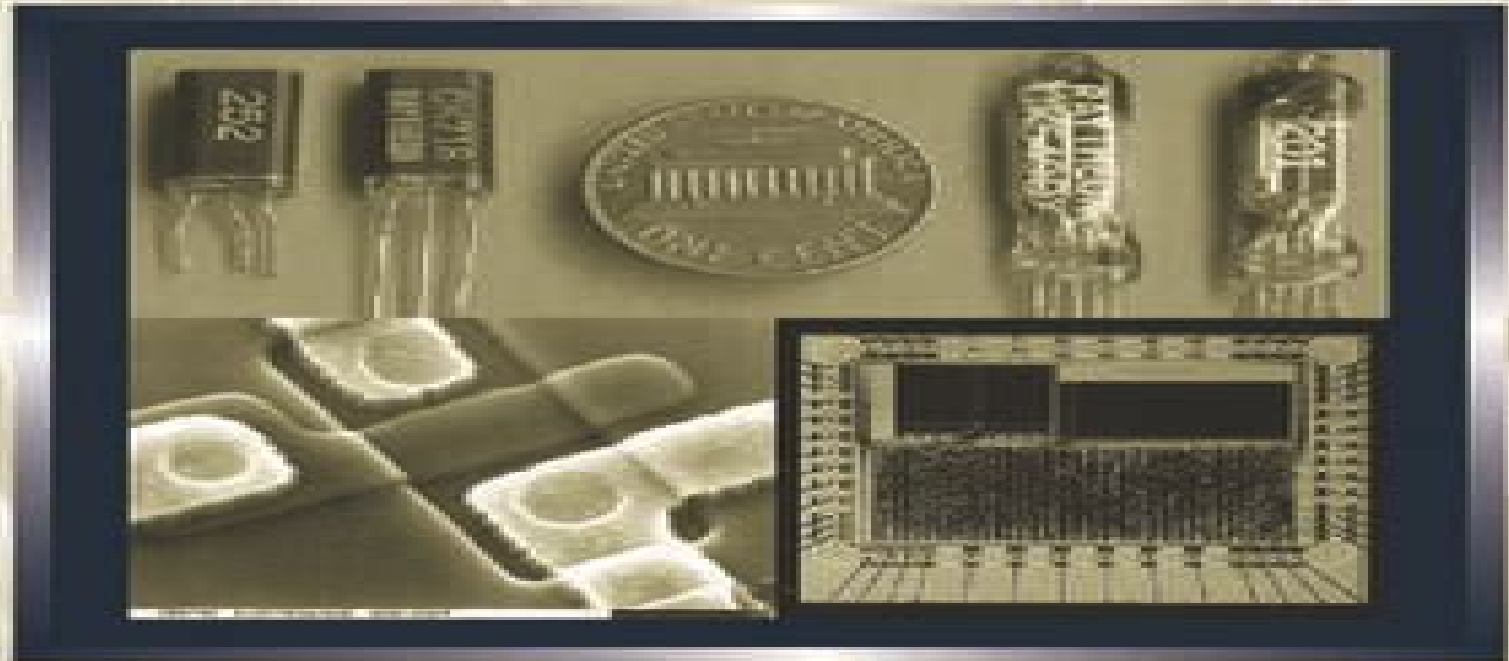


# Low-Power Electronics Design



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# Low Power Electronics Design

**Nihal Kularatna**



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*Power Electronics Design Handbook* Nihal Kularatna, 1998-09-09 Power Electronics Design Handbook covers the basics of power electronics theory and components while emphasizing modern low power components and applications Coverage includes power semiconductors converters power supplies batteries protection systems and power ICs One of the unique features of the Power Electronics Design Handbook is the integration of component and system theory with practical applications particularly energy saving low power applications Many chapters also include a section that looks forward to future developments in that area References for further information or more in depth technical reading are also included Nihal Kularatna is a principal research engineer with the Arthur C Clarke Foundation in Sri Lanka He is also the author of Modern Electronic Test and Measuring Instruments published by the Institute of Electrical Engineers Emphasizes low and medium power components Offers a unique mix of theory and practical application Provides a useful guide to further reading

*Ultra Low-Power Electronics and Design* E. Macii, 2007-05-08 Power consumption is a key limitation in many high speed and high data rate electronic systems today ranging from mobile telecom to portable and desktop computing systems especially when moving to nanometer technologies Ultra Low Power Electronics and Design offers to the reader the unique opportunity of accessing in an easy and integrated fashion a mix of tutorial material and advanced research results contributed by leading scientists from academia and industry covering the most hot and up to date issues in the field of the design of ultra low power devices systems and applications

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*Low Power Vlsi Design And Technology* Farid N Najm,Garey K-h Yeap,1996-08-30 Low power and low energy VLSI has become an important issue in today s consumer electronics This book is a collection of pioneering applied research papers in low power VLSI design and technology A comprehensive introductory chapter presents the current status of the industry and academic research in the area of low power VLSI design and technology Other topics cover logic synthesis floorplanning circuit design and analysis from the perspective of low power requirements The readers will have a sampling of some key problems in this area as the low power solutions span the entire spectrum of the design process The book also provides excellent references on up to date research and development issues with practical solution techniques

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*Low-Power Variation-Tolerant Design in Nanometer Silicon* Swarup Bhunia, Saibal Mukhopadhyay, 2010-11-10 Design considerations for low power operations and robustness with respect to variations typically impose contradictory requirements Low power design techniques such as voltage scaling dual threshold assignment and gate sizing can have large negative impact on parametric yield under process variations This book focuses on circuit architectural design techniques for achieving low power operation under parameter variations We consider both logic and memory design aspects and cover modeling and

analysis as well as design methodology to achieve simultaneously low power and variation tolerance while minimizing design overhead This book will discuss current industrial practices and emerging challenges at future technology nodes

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**Nano-CMOS Circuit and Physical Design** Ban Wong, Anurag Mittal, Yu Cao, Greg W. Starr, 2005-04-08 Based on the authors expansive collection of notes taken over the years Nano CMOS Circuit and Physical Design bridges the gap between physical and circuit design and fabrication processing manufacturability and yield This innovative book covers process technology including sub wavelength optical lithography impact of process scaling on circuit and physical implementation and low power with leaky transistors and DFM yield and the impact of physical implementation

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**International Symposium on Low Power Electronics and Design, 2003** International Symposium on Low Power Electronics and Design, 2003

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**Micro Electronic Circuit Design for High Performance Applications** Dr. S. Sathya, Dr. Priyanka Veeramosu, Dr. R. Boopathi, Dr. Bindu K V, Mr. Nishant S, 2025-01-28 Microelectronic Circuit Design for High Performance Applications is a comprehensive that explores advanced circuit design principles tailored for high speed low power and efficient electronic systems Topics such as semiconductor devices analog and digital circuit design signal integrity and power management the book provides in depth insights into optimizing performance in modern

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