RAPID PROTOTYPING OF DIGITAL SYSTEMS

A TUTORIAL APPROACH

SECOND EDITION

Extra Materials extraceptorism

JAMES O. HAMBLEN AND MICHAEL D. FURMAN

Rapid Prototyping Of Digital Systems

James O. Hamblen, Tyson S. Hall, Michael D. Furman

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overview of programmable logic and a design library with several easy to use input and output functions These features were developed to help students get started quickly Early design examples use schematic capture and library components VHDL is used for more complex designs after a short introduction to VHDL based synthesis The approach used in this text reflects contemporary practice in industry more accurately than the more traditional TTL protoboard based laboratory courses Designs containing up to twenty thousand gates are possible with the Altera Student Version CAD tools and the UP 1 board Rapid Prototyping of Digital Systems contains a number of interesting and challenging laboratory projects involving serial communications state machines with video output video games and graphics simple computers keyboard and mouse interfaces robotics and a RISC processor core These projects were all developed on the student version of the Altera CAD tools and can be implemented on the Altera UP 1 board **Rapid Prototyping of Digital Systems** James O. Hamblen, Tyson S. Hall, Michael D. Furman, 2006-01-16 Rapid Prototyping of Digital Systems Quartus II Edition provides an exciting and challenging laboratory component for undergraduate digital logic and computer design courses using FPGAs and CAD tools for simulation and hardware implementation The more advanced topics and exercises also make this text useful for upper level courses in digital logic programmable logic and embedded systems. This new version of the widely used Rapid Prototyping of Digital Systems Second Edition now uses Altera s new Quartus II CAD tool and includes laboratory projects for Altera s UP 2 and the new UP 3 FPGA board Rapid Prototyping of Digital Systems Quartus II Edition includes four tutorials on the Altera Quartus II and NIOS II tool environment an overview of programmable logic and IP cores with several easy to use input and output functions These features were developed to help students get started guickly Early design examples use schematic capture and IP cores developed for the Altera UP FPGA boards VHDL is used for more complex designs after a short introduction to VHDL based synthesis New to this edition is an overview of System on a Programmable Chip SOPC technology and SOPC design examples for the UP3 using Altera s new NIOS II Processor hardware and C software development tools Digital Systems Design and Prototyping Zoran Salcic, Asim Smailagic, 2000-10-31 A book CD ROM covering digital systems design using two important technologies field programmable logic devices FPLDs and hardware description languages HDLs These two technologies are combined to aid in the design prototyping and implementation of a range of digital systems Presents VHDL and Verilog widely used standard languages and the proprietary Altera HDL Chapters on these languages serve as tutorials and comparisons are made to highlight strengths and weaknesses of each language The CD ROM contains the Altera MAX PLUS II development environment Can be used as a reference or an advanced level text Salcic is affiliated with the University of Auckland Smailagic is affiliated with Carnegie Mellon University Annotation copyrighted by Book News Inc Portland OR Digital Systems Design and Prototyping Using Field Programmable Logic Zoran Salcic, Asim Smailagic, 2012-12-06 Field programmable logic has been available for a number of years The role of Field Programmable Logic Devices FPLDs has evolved from simply implementing

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capture features of digital systems in a form of behavioral dataflow or structural models providing a high degree of flexibility When augmented by a good simulator VHDL enables extensive verification of features of the system under design reducing uncertainties at the latter phases of design process As such it becomes an unavoidable modeling tool to model digital systems at various levels of abstraction Rapid Prototyping of Digital Systems Jamaludin Omar, 1997 The development and implementation of a framework for the rapid prototyping of digital systems using programmable hardware is presented The framework components developed consist of PC cards with programmable hardware components i e FPGAS PCB templates of the PC cards hardware modules for interfacing with PC systems software subroutines appropriate for hardware modules software utility programs for configuring the FPGAs on the PC cards and examples of designs which include hardware and software The framework allows a digital system to be developed rapidly and tested at realistic clock rates on the PC platform Hardware software and utilisation aspects of the rapid framework are described in detail The framework is shown to be very useful in rapid prototyping of a digital system A coprocessor card for computed tomography application was developed as an application using the rapid prototyping framework With limited capacity static RAM on board it was able to speed up implementation of the filtered backprojection algorithm up to 5 times **Digital Systems and Applications** Vojin G. Oklobdzija, 2017-12-19 New design architectures in computer systems have surpassed industry expectations Limits which were once thought of as fundamental have now been broken Digital Systems and Applications details these innovations in systems design as well as cutting edge applications that are emerging to take advantage of the fields increasingly sophisticated capabilities This book features new chapters on parallelizing iterative heuristics stream and wireless processors and lightweight embedded systems This fundamental text Provides a clear focus on computer systems architecture and applications Takes a top level view of system organization before moving on to architectural and organizational concepts such as superscalar and vector processor VLIW architecture as well as new trends in multithreading and multiprocessing includes an entire section dedicated to embedded systems and their applications Discusses topics such as digital signal processing applications circuit implementation aspects parallel I O algorithms and operating systems Concludes with a look at new and future directions in computing Features articles that describe diverse aspects of computer usage and potentials for use Details implementation and performance enhancing techniques such as branch prediction register renaming and virtual memory Includes a section on new directions in computing and their penetration into many new fields and aspects of our daily lives

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