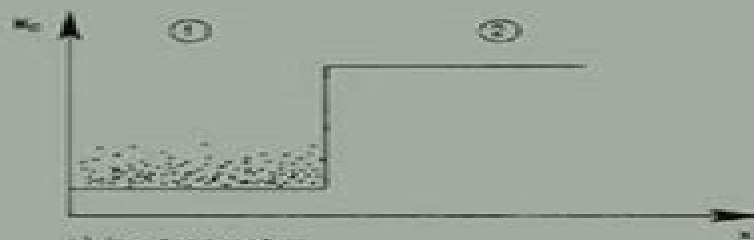
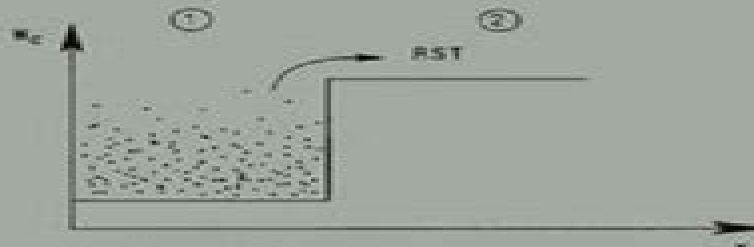


**Dietmar Schroeder**

# **Modelling of Interface Carrier Transport for Device Simulation**



a) low temperature



b) high temperature



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# Modelling Of Interface Carrier Transport For Device Simulation Computational Microelectronics

**Dimitris Tsoukalas**



## **Modelling Of Interface Carrier Transport For Device Simulation Computational Microelectronics:**

Modelling of Interface Carrier Transport for Device Simulation Dietmar Schroeder, 2012-11-01 This book contains a comprehensive review of the physics modelling and simulation of electron transport at interfaces in semiconductor devices. It combines a review of existing interface charge transport models with original developments and introduces a unified representation of charge transport at semiconductor interfaces.

*Modelling of Interface Carrier Transport for Device Simulation* Dietmar Schroeder, 2013-03-09 This book represents a comprehensive text devoted to charge transport at semiconductor interfaces and its consideration in device simulation by interface and boundary conditions. It contains a broad review of the physics modelling and simulation of electron transport at interfaces in semiconductor devices. Particular emphasis is put on the consistent derivation of interface or boundary conditions for semiconductor device simulation. The book is of interest with respect to a wide range of electronic engineering activities as process design, device design, process characterization, research in microelectronics, or device simulator development. It is also useful for students and lecturers in courses of electronic engineering and it supplements the library of technically oriented solid state physicists. The deepest roots of this book date back to the mid seventies. Being a student of electrical engineering who was exposed for the first time to the material of semiconductor device electronics, I was puzzled by noticing that much emphasis was put on a thorough introduction and understanding of the basic semiconductor equations while the boundary conditions for these equations received very much less attention. Until today on many occasions one could get the impression that boundary conditions are unimportant accessories; they do not stand on their own besides the bulk transport equations although it is clear that they are of course a necessary complement of these.

*Hierarchical Device Simulation* Christoph Jungemann, Bernd Meinerzhagen, 2003-06-05 This monograph is the first on physics based simulations of novel strained Si and SiGe devices. It provides an in depth description of the full band monte carlo method for SiGe and discusses the common theoretical background of the drift diffusion hydrodynamic and Monte Carlo models and their synergy.

**Simulation of Semiconductor Processes and Devices 2001** Dimitris Tsoukalas, 2001-08-21 This volume contains the Proceedings of the International Conference on Simulation of Semiconductor Devices and Processes SISPAD 01 held on September 5-7 2001 in Athens. The conference provided an open forum for the presentation of the latest results and trends in process and device simulation. The trend towards shrinking device dimensions and increasing complexity in process technology demands the continuous development of advanced models describing basic physical phenomena involved. New simulation tools are developed to complete the hierarchy in the Technology Computer Aided Design simulation chain between microscopic and macroscopic approaches. The conference program featured 8 invited papers, 60 papers for oral presentation and 34 papers for poster presentation selected from a total of 165 abstracts from 30 countries around the world. These papers disclose new and interesting concepts for simulating processes and devices.

**Advanced Physical Models for Silicon Device Simulation**

Andreas Schenk,2012-12-06 Device simulation has two main purposes to understand and depict the physical processes in the interior of a device and to make reliable predictions of the behavior of an anticipated new device generation Towards these goals the quality of the physical models is decisive The introductory chapter of this book contains a critical review on models for silicon device simulators which rely on moments of the Boltzmann equation With reference to fundamental experimental and theoretical work an extensive collection of widely used models is discussed in terms of physical accuracy and application results This review shows that the quality and efficiency of the physical models which have been developed for the purpose of numerical simulation over the last three decades is sufficient for many applications Nevertheless the basic understanding of the microscopic processes as well as the uniqueness and accuracy of the models are still unsatisfactory Hence the following chapters of the book deal with the derivation of physics based models from a microscopic level also using new approaches of tailored quantum mechanics Each model is compared with experimental data and applied to a number of simulation examples The problems when starting from first principles and making the models suitable for a device simulator will also be demonstrated We will show that demands for rapid computation and numerical robustness require a compromise between physical soundness and analytical simplicity and that the attainable accuracy is limited by the complexity of the problems

Analysis and Simulation of Heterostructure Devices Vassil Palankovski,Rüdiger Quay,2004 The topic of this monograph is the physical modeling of heterostructure devices A detailed discussion of physical models and parameters for compound semiconductors is presented including the relevant aspects of modern submicron heterostructure devices More than 25 simulation examples for different types of Si Ge based GaAs based InP based and GaN based heterostructure bipolar transistors HBTs and high electron mobility transistors HEMTs are given in comparison with experimental data from state of the art devices

Electrically Driven Quantum Dot Based Single-Photon Sources Markus Kantner,2020-01-25 Semiconductor quantum optics is on the verge of moving from the lab to real world applications When stepping from basic research to new technologies device engineers will need new simulation tools for the design and optimization of quantum light sources which combine classical device physics with cavity quantum electrodynamics This thesis aims to provide a holistic description of single photon emitting diodes by bridging the gap between microscopic and macroscopic modeling approaches The central result is a novel hybrid quantum classical model system that self consistently couples semi classical carrier transport theory with open quantum many body systems This allows for a comprehensive description of quantum light emitting diodes on multiple scales It enables the calculation of the quantum optical figures of merit together with the simulation of the spatially resolved current flow in complex multi dimensional semiconductor device geometries out of one box The hybrid system is shown to be consistent with fundamental laws of non equilibrium thermodynamics and is demonstrated by numerical simulations of realistic devices

Simulation of Semiconductor Processes and Devices 2004

Gerhard Wachutka,Gabriele Schrag,2004-08-23 This volume contains the proceedings of the 10th edition of the International

Conference on Simulation of Semiconductor Processes and Devices SISPAD 2004 held in Munich Germany on September 2-4 2004 The conference program included 7 invited plenary lectures and 82 contributed papers for oral or poster presentation which were carefully selected out of a total of 151 abstracts submitted from 14 countries around the world Like the previous meetings SISPAD 2004 provided a world wide forum for the presentation and discussion of recent advances and developments in the theoretical description physical modeling and numerical simulation and analysis of semiconductor fabrication processes device operation and system performance The variety of topics covered by the conference contributions reflects the physical effects and technological problems encountered in consequence of the progressively shrinking device dimensions and the ever growing complexity in device technology

*Semiconductor Nanophotonics* Michael Kneissl, Andreas Knorr, Stephan Reitzenstein, Axel Hoffmann, 2020-03-10 This book provides a comprehensive overview of the state of the art in the development of semiconductor nanostructures and nanophotonic devices It covers epitaxial growth processes for GaAs and GaN based quantum dots and quantum wells describes the fundamental optical electronic and vibronic properties of nanomaterials and addresses the design and realization of various nanophotonic devices These include energy efficient and high speed vertical cavity surface emitting lasers VCSELs and ultra small metal cavity nano lasers for applications in multi terabus systems silicon photonic I/O engines based on the hybrid integration of VCSELs for highly efficient chip to chip communication electrically driven quantum key systems based on q bit and entangled photon emitters and their implementation in real information networks and AlGaIn based deep UV laser diodes for applications in medical diagnostics gas sensing spectroscopy and 3D printing The experimental results are accompanied by reviews of theoretical models that describe nanophotonic devices and their base materials The book details how optical transitions in the active materials such as semiconductor quantum dots and quantum wells can be described using a quantum approach to the dynamics of solid state electrons under quantum confinement and their interaction with phonons as well as their external pumping by electrical currents With its broad and detailed scope this book is indeed a cutting edge resource for researchers engineers and graduate level students in the area of semiconductor materials optoelectronic devices and photonic systems

*Topics in Applied Analysis and Optimisation* Michael Hintermüller, José Francisco Rodrigues, 2019-11-27 This volume comprises selected revised papers from the Joint CIM WIAS Workshop TAAO 2017 held in Lisbon Portugal in December 2017 The workshop brought together experts from research groups at the Weierstrass Institute in Berlin and mathematics centres in Portugal to present and discuss current scientific topics and to promote existing and future collaborations The papers include the following topics PDEs with applications to material sciences thermodynamics and laser dynamics scientific computing nonlinear optimization and stochastic analysis

*Intrinsic Point Defects, Impurities, and Their Diffusion in Silicon* Peter Pichler, 2012-12-06 Basically all properties of semiconductor devices are influenced by the distribution of point defects in their active areas This book contains the first comprehensive review of the properties of intrinsic point defects

acceptor and donor impurities isovalent atoms chalcogens and halogens in silicon as well as of their complexes Special emphasis is placed on compiling the structures energetic properties identified electrical levels and spectroscopic signatures and the diffusion behavior from experimental and theoretical investigations In addition the book discusses the fundamental concepts of silicon and its defects the electron system diffusion thermodynamics and reaction kinetics which form the scientific basis needed for a thorough understanding of the text Therefore the book is able to provide an introduction to newcomers in this field up to a comprehensive reference for experts in process technology solid state physics and simulation of semiconductor processes

**Books in Print** ,1991      **Compact Models for Integrated Circuit Design** Samar K. Saha,2018-09-03 Compact Models for Integrated Circuit Design Conventional Transistors and Beyond provides a modern treatise on compact models for circuit computer aided design CAD Written by an author with more than 25 years of industry experience in semiconductor processes devices and circuit CAD and more than 10 years of academic experience in teaching compact modeling courses this first of its kind book on compact SPICE models for very large scale integrated VLSI chip design offers a balanced presentation of compact modeling crucial for addressing current modeling challenges and understanding new models for emerging devices Starting from basic semiconductor physics and covering state of the art device regimes from conventional micron to nanometer this text Presents industry standard models for bipolar junction transistors BJTs metal oxide semiconductor MOS field effect transistors FETs FinFETs and tunnel field effect transistors TFETs along with statistical MOS models Discusses the major issue of process variability which severely impacts device and circuit performance in advanced technologies and requires statistical compact models Promotes further research of the evolution and development of compact models for VLSI circuit design and analysis Supplies fundamental and practical knowledge necessary for efficient integrated circuit IC design using nanoscale devices Includes exercise problems at the end of each chapter and extensive references at the end of the book Compact Models for Integrated Circuit Design Conventional Transistors and Beyond is intended for senior undergraduate and graduate courses in electrical and electronics engineering as well as for researchers and practitioners working in the area of electron devices However even those unfamiliar with semiconductor physics gain a solid grasp of compact modeling concepts from this book

*Programmieren in C* Robert Klima,Siegfried Selberherr,2019-06-12 C ist eine der bedeutendsten Programmiersprachen und wird heute sehr häufig eingesetzt Die Autoren haben jahrelange Erfahrung in der Programmierung mit C der Vermittlung von Programmiersprachen und Programmiermethodik in das Lehrbuch einfließen lassen Der Leser soll nicht von der Komplexität und den vielen Möglichkeiten der Sprache C berfordert werden sondern das Wesentliche die Programmiermethodik lernen Was ist Programmieren Wie werden programmtechnische Probleme gelöst Wie beginnen Diese Fragen werden ebenso behandelt wie die Lösung komplexerer Aufgaben Schrittweise wird die Programmierung anhand der Sprache C erlernt und mit Beispielen und Aufgaben vertieft und wiederholt Der Sprachumfang von C wird vorgestellt und kritisch betrachtet um typische Fallen zu

vermeiden Nach grundlegenden Betrachtungen und fundierter Behandlung der Sprache C befassen sich die letzten Kapitel mit Verfahren Methoden und Problemen die in der Programmierung häufig anzutreffen sind *Computational Science - Iccs 2005* V.S. Sunderam, 2005-05-12 Annotation The three volume set LNCS 3514 3516 constitutes the refereed proceedings of the 5th International Conference on Computational Science ICCS 2005 held in Atlanta GA USA in May 2005 The 464 papers presented were carefully reviewed and selected from a total of 834 submissions for the main conference and its 21 topical workshops The papers span the whole range of computational science ranging from numerical methods algorithms and computational kernels to programming environments grids networking and tools These fundamental contributions dealing with computer science methodologies and techniques are complemented by papers discussing computational applications and needs in virtually all scientific disciplines applying advanced computational methods and tools to achieve new discoveries with greater accuracy and speed **Frontiers In Electronics (With Cd-rom) - Proceedings Of The Wofe-04** Michael S Shur, Yoshi Nishi, Hiroshi Iwai, Hei Wong, 2006-08-10 Frontiers in Electronics reports on the most recent developments and future trends in the electronics and photonics industry The issues address CMOS SOI and wide band gap semiconductor technology terahertz technology and bioelectronics providing a unique interdisciplinary overview of the key emerging issues This volume accurately reflects the recent research and development trends from pure research to research and development and its contributors are leading experts in microelectronics nanoelectronics and nanophotonics from academia industry and government agencies **Science Abstracts** ,1995 **2003 IEEE Conference on Electron Devices and Solid-State Circuits** ,2003 The proceedings from the 2003 IEEE Conference on Electron Devices and Solid State Circuits **International Conference on Simulation of Semiconductor Processes and Devices** ,2003 **Nano-Electronic Devices** Dragica Vasileska, Stephen M. Goodnick, 2011-06-10 This book surveys the advanced simulation methods needed for proper modeling of state of the art nanoscale devices It systematically describes theoretical approaches and the numerical solutions that are used in explaining the operation of both power devices as well as nano scale devices It clearly explains for what types of devices a particular method is suitable which is the most critical point that a researcher faces and has to decide upon when modeling semiconductor devices

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In an era characterized by interconnectedness and an insatiable thirst for knowledge, the captivating potential of verbal expression has emerged as a formidable force. Its ability to evoke sentiments, stimulate introspection, and incite profound transformations is genuinely awe-inspiring. Within the pages of "**Modelling Of Interface Carrier Transport For Device Simulation Computational Microelectronics**," a mesmerizing literary creation penned by a celebrated wordsmith, readers set about an enlightening odyssey, unraveling the intricate significance of language and its enduring effect on our lives. In this appraisal, we shall explore the book's central themes, evaluate its distinctive writing style, and gauge its pervasive influence on the hearts and minds of its readership.

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