LOW-VOLTAGE LOW-POWER ANALOG INTEGRATED CIRCUITS

edited by
Wouter A. Serdijn

Low Voltage Low Power Analog Integrated Circuits

Corrado Di Natale,Arnaldo D'amico,Giorgio Sberveglieri

Low Voltage Low Power Analog Integrated Circuits:

Low-voltage Low-power Analog Integrated Circuits ,1995 *Low-Voltage Low-Power Analog Integrated Circuits* Wouter A. Serdijn,2012-12-06 Low Voltage Low Power Analog Integrated Circuits brings together in one place important contributions and state of the art research results in this rapidly advancing area Low Voltage Low Power Analog Integrated Circuits serves as an excellent reference providing insight into some of the most important issues in the field Analog Circuit Design Rudy J. van de Plassche,Willy M.C. Sansen,Johan Huijsing,2013-06-29 The realization of signal sampling and quantization at high sample rates with low power dissipation is an important goal in many applications includ ing portable video devices such as camcorders personal communication devices such as wireless LAN transceivers in the read channels of magnetic storage devices using digital data detection and many others This paper describes architecture and circuit approaches for the design of high speed low power pipeline analog to digital converters in CMOS Here the term high speed is taken to imply sampling rates above 1 Mhz In the first section the dif ferent conversion techniques applicable in this range of sample rates is dis cussed Following that the particular problems associated with power minimization in video rate pipeline ADCs is discussed These include optimi zation of capacitor sizes design of low voltage transmission gates and opti mization of switched capacitor gain blocks and operational amplifiers for minimum power dissipation As an example of the application of these tech niques the design of a power optimized lO bit pipeline AID converter ADC that achieves 1 67 mW per MS s of sampling rate from 1 MS s to 20 MS s is described 2 Techniques for CMOS Video Rate AID Conversion Analog to digital conversion techniques can be categorized in many ways One convenient means of comparing techniques is to examine the number of analog clock cycles required to produce one effective output sample of the signal being quantized

Low-Voltage/Low-Power Integrated Circuits and Systems Edgar Sánchez-Sinencio,Andreas G. Andreou,1999-01-13 Electrical Engineering Low Voltage Low Power Integrated Circuits and Systems Low Voltage Mixed Signal Circuits Leading experts in the field present this collection of original contributions as a practical approach to low power analog and digital circuit theory and design illustrated with important applications and examples Low Voltage Low Power Integrated Circuits and Systems features comprehensive coverage of the latest techniques for the design modeling and characterization of low power analog and digital circuits Low Voltage Low Power Integrated Circuits and Systems will help you improve your understanding of the trade offs between analog and digital circuits and systems It is an invaluable resource for enhancing your designs This book is intended for senior and graduate students It is also intended as a key reference for designers in the semiconductor and communication industries Highlighted applications include Low voltage analog filters Low power multiplierless YUV to RGB based on human vision perception Micropower systems for implantable defibrillators and pacemakers Neuromorphic systems Low power design in telecom circuits **Analog Circuit Design** Willy M.C. Sansen,Johan Huijsing,Rudy J. van de Plassche,2012-12-06 This book contains the revised contributions of all the speakers of

the fifth AACD Workshop which was held in Lausanne on April 2 4 1996 It was organized by Dr Vlado Valence of the EPFL University and MEAD of Lausanne The program consisted of six tutorials per day during three days The tutorials were presented by experts in the field They were selected by a program committee consisting of Prof Willy Sansen of the Katholieke Universiteit Leuven Prof Rudy van de Plassche of Philips Research and the University of Technology Eindhoven and Prof 10han Huijsing of the Delft University of Technology The three topics mentioned above have been selected because of their importance in present days analog design The other topics that have been discussed before are in 1992 Operational amplifiers Analog to digital convereters Analog computer aided design in 1993 Mixed AID cicuit design Sensor interface circuits Communication circuits in 1994 Low power low voltage design Integrated filters Smart power circuits in 1995 Low noise low power low voltage design Mixed mode design with CAD tools Voltage current and time references Each AACD workhop has given rise to the publication of a book by Kluwer entitled Analog Circuit Design This is thus the fifth book This series of books provides a valuable overview of all analog circuit design techniques and achievements It is a reference for whoever is engaged in this discipline   Analog Circuit Design Michiel Steyaert,Arthur van Roermund,Andrea Baschirotto,2011-09-15 Analog Circuit Design contains the contribution of 18 tutorials of the 20th workshop on Advances in Analog Circuit Design Each part discusses a specific to date topic on new and valuable design ideas in the area of analog circuit design Each part is presented by six experts in that field and state of the art information is shared and overviewed This book is number 20 in this successful series of Analog Circuit Design providing valuable information and excellent overviews of Topic 1 Low Voltage Low Power chairman Andrea Baschirotto Topic 2 Short Range Wireless Front Ends chairman Arthur van Roermund Topic 3 Power Management and DC DC chairman Michiel Steyaert Analog Circuit Design is an essential reference source for analog circuit designers and researchers wishing to keep abreast with the latest development in the field The tutorial coverage also makes it suitable for use in an advanced design course   *The Design of Low-Voltage, Low-Power Sigma-Delta Modulators* Shahriar Rabii,Bruce A. Wooley,2012-12-06 Oversampling techniques based on sigma delta modulation are widely used to implement the analog digital interfaces in CMOS VLSI technologies This approach is relatively insensitive to imperfections in the manufacturing process and offers numerous advantages for the realization of high resolution analog to digital A D converters in the low voltage environment that is increasingly demanded by advanced VLSI technologies and by portable electronic systems In The Design of Low Voltage Low Power Sigma Delta Modulators an analysis of power dissipation in sigma delta modulators is presented and a low voltage implementation of a digital audio performance A D converter based on the results of this analysis is described Although significant power savings can typically be achieved in digital circuits by reducing the power supply voltage the power dissipation in analog circuits actually tends to increase with decreasing supply voltages Oversampling architectures are a potentially power efficient means of implementing high resolution A D converters because they reduce the number and complexity of the analog circuits

in comparison with Nyquist rate converters In fact it is shown that the power dissipation of a sigma delta modulator can approach that of a single integrator with the resolution and bandwidth required for a given application In this research the influence of various parameters on the power dissipation of the modulator has been evaluated and strategies for the design of a power efficient implementation have been identified The Design of Low Voltage Low Power Sigma Delta Modulators begins with an overview of A D conversion emphasizing sigma delta modulators It includes a detailed analysis of noise in sigma delta modulators analyzes power dissipation in integrator circuits and addresses practical issues in the circuit design and testing of a high resolution modulator The Design of Low Voltage Low Power Sigma Delta Modulators will be of interest to practicing engineers and researchers in the areas of mixed signal and analog integrated circuit design **The Design of Low-voltage Low-power Analog Integrated Circuits and Their Applications in Hearing Instruments** Wouter A. Serdijn,1994 **Low-Voltage Low-Power CMOS Current Conveyors** Giuseppe Ferri,Nicola C. Guerrini,2003-07-31 This concise and modern book on current conveyors considers first and second generation devices in a general environment and for low voltage low power applications It constitutes an excellent reference for analogue designers and researchers and is suitable as a textbook in an advanced course on microelectronics *Low Voltage Low Power Analog Integrated Circuit Analysis, Design and Modeling* Gonggui Xu,1999 **Analog Circuit Design** Johan Huijsing,Rudy J. van de Plassche,Willy M.C. Sansen,1995-12-31 This volume of Analog Circuit Design concentrates on three topics Low Noise Low Power Low Voltage Mixed Mode Design with CAD Tools Voltage Current and Time References The book contains six papers on each topic written by internationally recognised experts The papers are tutorial in nature and make a substantial contribution to improving the design of analog circuits The book is divided into three parts Part I Low Noise Low Power Low Voltage concentrates on the problems of the matching properties of high frequency MOS circuits caused by the continuous reduction in the size of integrated devices These problems are considered in light of maintaining the benefits of greater bandwidth and lower power consumption Part II Mixed Mode Design with CAD Tools looks at the practicalities of providing CAD tools for circuits containing both digital and analog elements The papers consider both the simulation and synthesis aspects of designing CAD tools suitable for such designs Part III Voltage Current and Time References contains much new and exciting material describing all aspects of these reference circuits Audience An essential reference source for analog design engineers and researchers wishing to keep abreast with the latest developments in the field The tutorial nature of the contributions also makes it suitable for use in an advanced course **Low-Voltage CMOS Log Companding Analog Design** Francisco Serra-Graells,Adoración Rueda,José L. Huertas,2006-04-18 Low Voltage CMOS Log Companding Analog Design presents in detail state of the art analog circuit techniques for the very low voltage and low power design of systems on chip in CMOS technologies The proposed strategy is mainly based on two bases the Instantaneous Log Companding Theory and the MOSFET operating in the subthreshold region The former allows inner compression of the voltage dynamic range for very

low voltage operation while the latter is compatible with CMOS technologies and suitable for low power circuits The required background on the specific modeling of the MOS transistor for Companding is supplied at the beginning Following this general approach a complete set of CMOS basic building blocks is proposed and analyzed for a wide variety of analog signal processing In particular the covered areas include amplification and AGC arbitrary filtering PTAT generation and pulse duration modulation PDM For each topic several case studies areconsidered to illustrate the design methodology Also integrated examples in 1 2um and 0 35um CMOS technologies are reported to verify the good agreement between design equations and experimental data The resulting analog circuit topologies exhibit very low voltage i e 1V and low power few tenths of uA capabilities Apart from these specific design examples a real industrial application in the field of hearing aids is also presented as the main demonstrator of all the proposed basic building blocks This system on chip exhibits true 1V operation high flexibility through digital programmability and very low power consumption about 300uA including the Class D amplifier As a result the reported ASIC can meet the specifications of a complete family of common hearing aid models In conclusion this book is addressed to both industry ASIC designers who can apply its contents to the synthesis of very low power systems on chip in standard CMOS technologies as wellas to the teachers of modern circuit design in electronic engineering   *Distortion Analysis of Analog Integrated Circuits* Piet Wambacq,Willy M.C. Sansen,2013-04-17 The analysis and prediction of nonlinear behavior in electronic circuits has long been a topic of concern for analog circuit designers The recent explosion of interest in portable electronics such as cellular telephones cordless telephones and other applications has served to reinforce the importance of these issues The need now often arises to predict and optimize the distortion performance of diverse electronic circuit configurations operating in the gigahertz frequency range where nonlinear reactive effects often dominate However there have historically been few sources available from which design engineers could obtain information on analysis tech niques suitable for tackling these important problems I am sure that the analog circuit design community will thus welcome this work by Dr Wambacq and Professor Sansen as a major contribution to the analog circuit design literature in the area of distortion analysis of electronic circuits I am personally looking forward to having a copy readily available for reference when designing integrated circuits for communication systems   **Integrated Circuits/Microchips** Kim Ho Yeap,Jonathan Sayago,2020-09 With the world marching inexorably towards the fourth industrial revolution IR 4 0 one is now embracing lives with artificial intelligence AI the Internet of Things IoTs virtual reality VR and 5G technology Wherever we are whatever we are doing there are electronic devices that we rely indispensably on While some of these technologies such as those fueled with smart autonomous systems are seemingly precocious others have existed for quite a while These devices range from simple home appliances entertainment media to complex aeronautical instruments Clearly the daily lives of mankind today are interwoven seamlessly with electronics Surprising as it may seem the cornerstone that empowers these electronic devices is nothing more than a mere diminutive semiconductor cube block

More colloquially referred to as the Very Large Scale Integration VLSI chip or an integrated circuit IC chip or simply a microchip this semiconductor cube block approximately the size of a grain of rice is composed of millions to billions of transistors The transistors are interconnected in such a way that allows electrical circuitries for certain applications to be realized Some of these chips serve specific permanent applications and are known as Application Specific Integrated Circuits ASICS while others are computing processors which could be programmed for diverse applications The computer processor together with its supporting hardware and user interfaces is known as an embedded system In this book a variety of topics related to microchips are extensively illustrated The topics encompass the physics of the microchip device as well as its design methods and applications   **CMOS Current Amplifiers** Giuseppe Palmisano,Gaetano Palumbo,Salvatore Pennisi,2012-12-06 CMOS Current Amplifiers presents design strategies for high performance current amplifiers based on CMOS technology After an introduction to various architectures of operational amplifiers the operating principles of the current amplifier are outlined This book provides the reader with simple and compact design equations for use in a pencil and paper design and the following simulation step Chapter 1 introduces the general aspects of current amplifiers After a preliminary classification of operational amplifiers ideal blocks and models are discussed for different architectures and a first high level comparison is made between traditional amplifiers and current amplifiers Analysis and examples of basic circuits as well as signal processing applications involving current amplifiers are also given Non idealities and second order effects causing limitations in performance are then discussed and evaluated Chapter 2 focuses on low drive current amplifiers Several design examples for current conveyors and class A current amplifiers are discussed in detail and design equations are presented for the main performance parameters which allows a good trade off between requirements High performance solutions for high bandwidth and low voltage capability are also considered and finally current comparators with progressively enhanced performance are reported and analyzed critically Chapter 3 deals with current amplifiers for off chip loads Several class AB current mode output stages are discussed and design strategies which improve performance are presented A detailed analysis of non ideal effect is carried out with particular emphasis on linearity Design examples are given and circuit arrangements for further developments are included CMOS Current Amplifiers serves as an excellent reference for researchers and professionals of analog IC design and may also be used as an advanced text on current amplifiers   **Neuromorphic Systems Engineering** Tor Sverre Lande,2007-08-26 Neuromorphic Systems Engineering Neural Networks in Silicon emphasizes three important aspects of this exciting new research field The term neuromorphic expresses relations to computational models found in biological neural systems which are used as inspiration for building large electronic systems in silicon By adequate engineering these silicon systems are made useful to mankind Neuromorphic Systems Engineering Neural Networks in Silicon provides the reader with a snapshot of neuromorphic engineering today It is organized into five parts viewing state of the art developments within neuromorphic engineering from different perspectives

Neuromorphic Systems Engineering Neural Networks in Silicon provides the first collection of neuromorphic systems descriptions with firm foundations in silicon Topics presented include large scale analog systems in silicon neuromorphic silicon auditory ear and vision eye systems in silicon learning and adaptation in silicon merging biology and technology micropower analog circuit design analog memory analog interchipcommunication on digital buses LIST Neuromorphic Systems Engineering Neural Networks in Silicon serves as an excellent resource for scientists researchers and engineers in this emerging field and may also be used as a text for advanced courses on the subject   Electronic Engineering and Information Science Jing Hua Yin,Bo Su,Dong Xing Wang,2014-07-08 Selected peer reviewed papers from the 2014 International Conference on Electronic Engineering and Information Science ICEEIS 2014 June 21 22 2014 Harbin China

Sensors And Microsystems: Proceedings Of The 3rd Italian Conference Corrado Di Natale,Arnaldo D'amico,Giorgio Sberveglieri,1998-12-31 This book is testimony to the degree of advancement in the research and development of sensors in Italy It covers the typical areas of sensors and microsystems such as chemical and biological sensors physical sensors optical sensors and micromechanics As in previous proceedings of the Italian Conference on Sensors and Microsystems a section of this book is devoted to advanced sensor applications The book focuses on the development of sensors for the human body and the relationship between human bodies and sensor systems   **Recent Trends in Electronics and Communication** Amit Dhawan,Vijay Shanker Tripathi,Karm Veer Arya,Kshirasagar Naik,2021-12-13 This book comprises select proceedings of the International Conference on VLSI Communication and Signal processing VCAS 2020 The contents are broadly divided into three topics VLSI Communication and Signal Processing The book focuses on the latest innovations trends and challenges encountered in the different areas of electronics and communication especially in the area of microelectronics and VLSI design communication systems and networks and image and signal processing It also offers potential solutions and provides an insight into various emerging areas such as Internet of Things IoT System on a Chip SoC Sensor Networks underwater and underground communication networks etc This book will be useful for academicians and professionals alike   Mixed-Signal Circuits Thomas Noulis,2018-09-03 Mixed Signal Circuits offers a thoroughly modern treatment of integrated circuit design in the context of mixed signal applications Featuring chapters authored by leading experts from industry and academia this book Discusses signal integrity and large scale simulation verification and testing Demonstrates advanced design techniques that enable digital circuits and sensitive analog circuits to coexist without any compromise Describes the process technology needed to address the performance challenges associated with developing complex mixed signal circuits Deals with modeling topics such as reliability variability and crosstalk that define pre silicon design methodology and trends and are the focus of companies involved in wireless applications Develops methods to move analog into the digital domain quickly minimizing and eliminating common trade offs between performance power consumption simulation time verification size and cost Details approaches for very low power

performances high speed interfaces phase locked loops PLLs voltage controlled oscillators VCOs analog to digital converters ADCs and biomedical filters Delineates the respective parts of a full system on chip SoC from the digital parts to the baseband blocks radio frequency RF circuitries electrostatic discharge ESD structures and built in self test BIST architectures Mixed Signal Circuits explores exciting opportunities in wireless communications and beyond The book is a must for anyone involved in mixed signal circuit design for future technologies

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