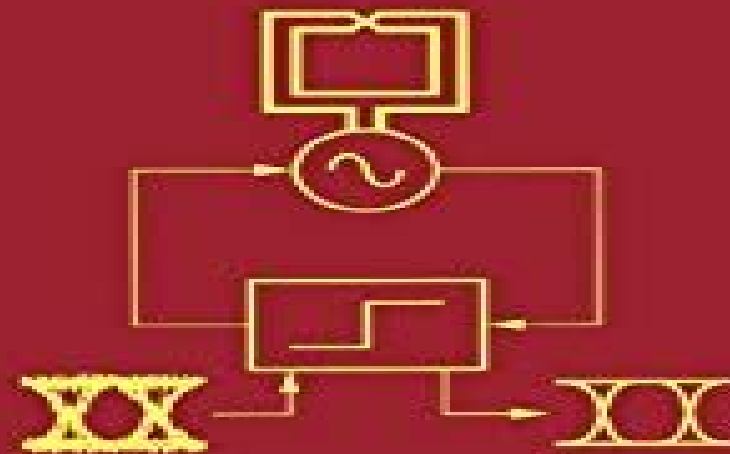


Phase-Locking in High-Performance Systems

From Devices to Architectures



EDITED BY
BEHZAD RAZAVI

Phase Locking In High Performance Systems From Devices To Architectures

Jian-Jia Chen



Phase Locking In High Performance Systems From Devices To Architectures:

Phase-Locking in High-Performance Systems Behzad Razavi, 2003-02-27 Comprehensive coverage of recent developments in phase locked loop technology The rapid growth of high speed semiconductor and communication technologies has helped make phase locked loops PLLs an essential part of memories microprocessors radio frequency RF transceivers broadband data communication systems and other burgeoning fields Complementing his 1996 Monolithic Phase Locked Loops and Clock Recovery Circuits Wiley IEEE Press Behzad Razavi now has collected the most important recent writing on PLL into a comprehensive self contained look at PLL devices circuits and architectures Phase Locking in High Performance Systems From Devices to Architectures five original tutorials and eighty three key papers provide an eminently readable foundation in phase locked systems Analog and digital circuit designers will glean a wide range of practical information from the book s Tutorials dealing with devices delay locked loops DLLs fractional N synthesizers bang bang PLLs and simulation of phase noise and jitter In depth discussions of passive devices such as inductors transformers and varactors Papers on the analysis of phase noise and jitter in various types of oscillators Concentrated examinations of building blocks including the design of oscillators frequency dividers and phase frequency detectors Articles addressing the problem of clock generation by phase locking for timing and digital applications RF synthesis and the application of phase locking to clock and data recovery circuits In tandem with its companion volume Phase Locking in High Performance Systems From Devices to Architectures is a superb reference for anyone working on or seeking to better understand this rapidly developing and increasingly central technology

Automation and Control Aleksandar Rodic, 2009-12-01 The present edited book is a collection of 18 chapters written by internationally recognized experts and well known professionals of the field Chapters contribute to diverse facets of automation and control The volume is organized in four parts according to the main subjects regarding the recent advances in this field of engineering The first thematic part of the book is devoted to automation This includes solving of assembly line balancing problem and design of software architecture for cognitive assembling in production systems The second part of the book concerns different aspects of modelling and control This includes a study on modelling pollutant emission of diesel engine development of a PLC program obtained from DEVS model control networks for digital home automatic control of temperature and flow in heat exchanger and non linear analysis and design of phase locked loops The third part addresses issues of parameter estimation and filter design including methods for parameters estimation control and design of the wave digital filters The fourth part presents new results in the intelligent control This includes building a neural PDF strategy for hydroelectric saturation simulator intelligent network system for process control neural generalized predictive control for industrial processes intelligent system for forecasting diagnosis and decision making based on neural networks and self organizing maps development of a smart semantic middleware for the Internet development of appropriate AI methods in fault tolerant control building expert system in rotary railcar dumpers expert system for plant asset

management and building of a image retrieval system in heterogeneous database The content of this thematic book admirably reflects the complementary aspects of theory and practice which have taken place in the last years Certainly the content of this book will serve as a valuable overview of theoretical and practical methods in control and automation to those who deal with engineering and research in this field of activities

Clocking in Modern VLSI Systems Thucydides Xanthopoulos, 2009-08-19 THUCYDIDIS HISTORIAE IV 108 C Hude ed Teubner Lipsiae MCMXIII 108 It being the fashion of men what they wish to be true to admit even upon an ungrounded hope and what they wish not with a magistral kind of arguing to reject Thucydides the Peloponnesian War Part I IV 108 Thomas Hobbes Trans Sir W Molesworth ed In The English Works of Thomas Hobbes of Malmesbury Vol VIII I have been introduced to clock design very early in my professional career when I was tapped right out of school to design and implement the clock generation and distribution of the Alpha 21364 microprocessor Traditionally Alpha processors hibited highly innovative clocking systems always worthy of ISSCC JSSC publi tions and for a while Alpha processors were leading the industry in terms of clock performance I had huge shoes to ll Obviously I was overwhelmed confused and highly con dent that I would drag the entire project down

The Turn of Moore's Law from Space to Time Liming Xiu, 2022-05-24 This book states that a space induced crisis is recognized as the cause of trouble that Moore s Law is currently facing The contemporary practice of this empirical law can be considered as happening within a space dominant paradigm An alternative of exploiting potential in the dimension of time is identified as an emerging paradigm in microelectronics The new practice is termed a time oriented paradigm It is justified as the turn of Moore s Law from space to time The resultant Time Moore strategy is envisioned as the next generation enabler for continuing Moore s Law s pursuit of everhigher information processing power and efficiency It also serves as the perpetuation of the spirit that Moore s law is nothing but a collective storied history of innovations In the first part of this book by following Thomas Kuhn s seminal work around the concepts of paradigm and scientific revolution the argument for the Time Moore strategy Time Moore to use time more and the paradigm shift from space to time is carried out heavily through philosophical persuasion rather than technical proof due to the difficult challenge of change of mindset The second part of the book provides solid technical materials for supporting this transition from the old paradigm to the new one In short the goal of this book is to reevaluate the contemporary practice of microelectronics identify the cause of the current crisis advocate a change of mindset to circumvent the crisis and ultimately point out a new route for advancing After achieving so many unprecedented accomplishments through several decades of relentless endeavor it s time for the big ship of Moore s Law i e the art of microelectronic system design to make a turn

Smart Structures, Devices, and Systems , 2004 *Fast Hopping Frequency Generation in Digital CMOS* Mohammad Farazian, Lawrence E. Larson, Prasad S. Gudem, 2012-10-12 Overcoming the agility limitations of conventional frequency synthesizers in multi band OFDM ultra wideband is a key research goal in digital technology This volume outlines a frequency plan that can generate all the

required frequencies from a single fixed frequency able to implement center frequencies with no more than two levels of SSB mixing It recognizes the need for future synthesizers to bypass on chip inductors and operate at low voltages to enable the increased integration and efficiency of networked appliances The author examines in depth the architecture of the dividers that generate the necessary frequencies from a single base frequency and are capable of establishing a fractional division ratio Presenting the first CMOS inductorless single PLL 14 band frequency synthesizer for MB OFDMUWB makes this volume a key addition to the literature and with the synthesizer capable of arbitrary band hopping in less than two nanoseconds it operates well within the desired range on a 1.2 volt power supply The author's close analysis of the operation stability and phase noise of injection locked regenerative frequency dividers will provide researchers and technicians with much food for developmental thought

Three-Dimensional Integrated Circuit Design Vasilis F. Pavlidis, Ioannis Savidis, Eby G. Friedman, 2017-07-04 Three Dimensional Integrated Circuit Design Second Edition expands the original with more than twice as much new content adding the latest developments in circuit models temperature considerations power management memory issues and heterogeneous integration 3 D IC experts Pavlidis Savidis and Friedman cover the full product development cycle throughout the book emphasizing not only physical design but also algorithms and system level considerations to increase speed while conserving energy A handy comprehensive reference or a practical design guide this book provides effective solutions to specific challenging problems concerning the design of three dimensional integrated circuits Expanded with new chapters and updates throughout based on the latest research in 3 D integration Manufacturing techniques for 3 D ICs with TSVs Electrical modeling and closed form expressions of through silicon vias Substrate noise coupling in heterogeneous 3 D ICs Design of 3 D ICs with inductive links Synchronization in 3 D ICs Variation effects on 3 D ICs Correlation of WID variations for intra tier buffers and wires Offers practical guidance on designing 3 D heterogeneous systems Provides power delivery of 3 D ICs Demonstrates the use of 3 D ICs within heterogeneous systems that include a variety of materials devices processors GPU CPU integration and more Provides experimental case studies in power delivery synchronization and thermal characterization

From Frequency to Time-Average-Frequency Liming Xiu, 2015-04-20 Written in a simple easy to understand style this book will teach PLL users how to use new clock technology in their work in order to create innovative applications Investigates the clock frequency concept from a different perspective at an application level Teaches engineers to use this new clocking technology to create innovations in chip system level through real examples extracted from commercial products

Advanced VLSI Design and Testability Issues Suman Lata Tripathi, Sobhit Saxena, Sushanta Kumar Mohapatra, 2020-08-18 This book facilitates the VLSI interested individuals with not only in depth knowledge but also the broad aspects of it by explaining its applications in different fields including image processing and biomedical The deep understanding of basic concepts gives you the power to develop a new application aspect which is very well taken care of in this book by using simple language in explaining the concepts In the VLSI world the

importance of hardware description languages cannot be ignored as the designing of such dense and complex circuits is not possible without them Both Verilog and VHDL languages are used here for designing The current needs of high performance integrated circuits ICs including low power devices and new emerging materials which can play a very important role in achieving new functionalities are the most interesting part of the book The testing of VLSI circuits becomes more crucial than the designing of the circuits in this nanometer technology era The role of fault simulation algorithms is very well explained and its implementation using Verilog is the key aspect of this book This book is well organized into 20 chapters Chapter 1 emphasizes on uses of FPGA on various image processing and biomedical applications Then the descriptions enlighten the basic understanding of digital design from the perspective of HDL in Chapters 2 5 The performance enhancement with alternate material or geometry for silicon based FET designs is focused in Chapters 6 and 7 Chapters 8 and 9 describe the study of bimolecular interactions with biosensing FETs Chapters 10 13 deal with advanced FET structures available in various shapes materials such as nanowire HFET and their comparison in terms of device performance metrics calculation Chapters 14 18 describe different application specific VLSI design techniques and challenges for analog and digital circuit designs Chapter 19 explains the VLSI testability issues with the description of simulation and its categorization into logic and fault simulation for test pattern generation using Verilog HDL Chapter 20 deals with a secured VLSI design with hardware obfuscation by hiding the IC s structure and function which makes it much more difficult to reverse engineer

RF Circuits for 5G Applications Sangeeta Singh,Rajeev Kumar Arya,B. C. Sahana,Ajay Kumar Vyas,2023-03-10 RF CIRCUITS FOR 5G APPLICATIONS This book addresses FinFET based analog IC designing for fifth generation 5G communication networks and highlights the latest advances problems and challenges while presenting the latest research results in the field of mmwave integrated circuits designing The wireless communication sector is experiencing exponential expansion particularly in the areas of mobile data and the 5G mobile network creating fresh market possibilities for designing the integrated circuits ICs needed in the industry Drawing from scientific literature and practical realization this book explores FinFET based analog IC designing for 5G communication networks and considers the latest breakthroughs and obstacles It also presents the recent research trends and future roadmaps for the 5G communication circuits RF Circuits for 5G Applications includes design guidelines to be considered when designing these circuits and detrimental scaling effects of the same In addition to enhance the usability of this book the editors have included real time problems in RFIC designing and case studies from experimental results as well as clearly demarcated design guidelines for the 5G communication ICs designing Audience The primary target audience includes researchers postgraduate students and industry professionals pursuing specializations in RF engineering electronics engineering electrical engineering information and communication technology Indoor Location Technologies Subrata Goswami,2012-07-25 Focusing on the special challenges posed by accurately pinpointing a location indoors this volume reflects the distance we have come in the handful of decades since the

germination of GPS technology Not only can we locate a signal to within a meter s accuracy but we now have this technology in the most basic mobile phone Tracing recent practical developments in positioning technology and in the market it supplies the author examines the contributions of the varied research in silicon signal and image processing radio communications and software to a fast evolving field The book looks forward to a time when in addition to directing your road journey positioning systems can peer indoors and guide you to an available photocopier in your office building Featuring standalone chapters each dealing with a specific aspect of the subject including treatments of systems such as Zebra Awarepoint Aeroscout IEEE 802 11 etc This study has all the detail needed to get up to speed on a key modern technology

Handbook of Multimedia Information Security: Techniques and Applications Amit Kumar Singh, Anand Mohan, 2019-07-19 This handbook is organized under three major parts The first part of this handbook deals with multimedia security for emerging applications The chapters include basic concepts of multimedia tools and applications biological and behavioral biometrics effective multimedia encryption and secure watermarking techniques for emerging applications an adaptive face identification approach for android mobile devices and multimedia using chaotic and perceptual hashing function The second part of this handbook focuses on multimedia processing for various potential applications The chapter includes a detail survey of image processing based automated glaucoma detection techniques and role of de noising recent study of dictionary learning based image reconstruction techniques for analyzing the big medical data brief introduction of quantum image processing and its applications a segmentation less efficient Alzheimer detection approach object recognition image enhancements and de noising techniques for emerging applications improved performance of image compression approach and automated detection of eye related diseases using digital image processing The third part of this handbook introduces multimedia applications The chapter includes the extensive survey on the role of multimedia in medicine and multimedia forensics classification a finger based authentication system for e health security analysis of recently developed deep learning techniques for emotion and activity recognition Further the book introduces a case study on change of ECG according to time for user identification role of multimedia in big data cloud computing the Internet of things IoT and blockchain environment in detail for real life applications This handbook targets researchers policy makers programmers and industry professionals in creating new knowledge for developing efficient techniques framework for multimedia applications Advanced level students studying computer science specifically security and multimedia will find this book useful as a reference

Radiation Tolerant Electronics Paul Leroux, 2019-08-26 Research on radiation tolerant electronics has increased rapidly over the past few years resulting in many interesting approaches to modeling radiation effects and designing radiation hardened integrated circuits and embedded systems This research is strongly driven by the growing need for radiation hardened electronics for space applications high energy physics experiments such as those on the Large Hadron Collider at CERN and many terrestrial nuclear applications including nuclear energy and nuclear safety With the progressive

scaling of integrated circuit technologies and the growing complexity of electronic systems their susceptibility to ionizing radiation has raised many exciting challenges which are expected to drive research in the coming decade In this book we highlight recent breakthroughs in the study of radiation effects in advanced semiconductor devices as well as in high performance analog mixed signal RF and digital integrated circuits We also focus on advances in embedded radiation hardening in both FPGA and microcontroller systems and apply radiation hardened embedded systems for cryptography and image processing targeting space applications

Microwave and Millimetre-Wave Design for Wireless Communications Ian Robertson, Nutapong Somjit, Mitchai Chongcheawchamnan, 2016-06-29 This book describes a full range of contemporary techniques for the design of transmitters and receivers for communications systems operating in the range from 1 through to 300 GHz In this frequency range there is a wide range of technologies that need to be employed with silicon ICs at the core but compared with other electronics systems a much greater use of more specialist devices and components for high performance for example high Q factor low loss and good power efficiency Many text books do of course cover these topics but what makes this book timely is the rapid adoption of millimetre waves frequencies from 30 to 300 GHz for a wide range of consumer applications such as wireless high definition TV 5G Gigabit mobile internet systems and automotive radars It has taken many years to develop low cost technologies for suitable transmitters and receivers so previously these frequencies have been employed only in expensive military and space applications The book will cover these modern technologies with the follow topics covered transmitters and receivers lumped element filters transmission lines and S parameters RF MEMS RFICs and MMICs and many others In addition the book includes extensive line diagrams to illustrate circuit diagrams and block diagrams of systems including diagrams and photographs showing how circuits are implemented practically Furthermore case studies are also included to explain the salient features of a range of important wireless communications systems The book is accompanied with suitable design examples and exercises based on the Advanced Design System the industry leading CAD tool for wireless design More importantly the authors have been working with Keysight Technologies on a learning teaching initiative which is designed to promote access to industry standard EDA tools such as ADS Through its University Educational Support Program Keysight offers students the opportunity to request a student license backed up with extensive classroom materials and support resources This culminates with students having the chance to demonstrate their RF MW design and measurement expertise through the Keysight RF Microwave Industry Ready Student Certification Program www.keysight.com/find/eesof/university www.keysight.com/find/eesof/student/certification

Enabling Techniques for Wide Bandwidth Fractional-N Phase Locked Loops Sudhakar Pamarti, 2003

Digitally-Assisted Analog and Analog-Assisted Digital IC Design Xicheng Jiang, 2015-07-23 Discover cutting edge techniques for next generation integrated circuit design and learn how to deliver improved speed density power and cost

Digital Phase Lock Loops Saleh R. Al-Araji, Zahir M. Hussain, Mahmoud A. Al-Qutayri, 2007-04-29 This exciting new book

covers various types of digital phase lock loops It presents a comprehensive coverage of a new class of digital phase lock loops called the time delay tanlock loop TDTL It also details a number of architectures that improve the performance of the TDTL through adaptive techniques that overcome the conflicting requirements of the locking range and speed of acquisition

Design of Monolithic Emitter Degenerated Voltage-controlled Oscillators Jing-Hong Conan Zhan,2004 **Wireless**

Receiver Architectures and Design Tony J. Roupael,2014-06-17 Wireless Receiver Architectures and Design presents the various designs and architectures of wireless receivers in the context of modern multi mode and multi standard devices This one stop reference and guide to designing low cost low power multi mode multi standard receivers treats analog and digital signal processing simultaneously with equal detail given to the chosen architecture and modulating waveform It provides a complete understanding of the receiver s analog front end and the digital backend and how each affects the other The book explains the design process in great detail starting from an analysis of requirements to the choice of architecture and finally to the design and algorithm development The advantages and disadvantages of each wireless architecture and the suitability to a standard are given enabling a better choice of design methodology receiver lineup analog block and digital algorithm for a particular architecture Whether you are a communications engineer working in system architecture and waveform design an RF engineer working on noise and linearity budget and line up analysis a DSP engineer working on algorithm development or an analog or digital design engineer designing circuits for wireless transceivers this book is your one stop reference and guide to designing low cost low power multi mode multi standard receivers The material in this book is organized and presented to lead you from applied theory to practical design with plenty of examples and case studies drawn from modern wireless standards Provides a complete description of receiver architectures together with their pros and cons enabling a better choice of design methodology Covers the design trade offs and algorithms between the analog front end and the digital modem enabling an end to end design approach Addresses multi mode multi standard low cost low power radio design critical for producing the applications for Smart phones and portable internet devices **A Journey of**

Embedded and Cyber-Physical Systems Jian-Jia Chen,2020-07-30 This Open Access book celebrates Professor Peter Marwedel s outstanding achievements in compilers embedded systems and cyber physical systems The contributions in the book summarize the content of invited lectures given at the workshop Embedded Systems held at the Technical University Dortmund in early July 2019 in honor of Professor Marwedel s seventieth birthday Provides a comprehensive view from leading researchers with respect to the past present and future of the design of embedded and cyber physical systems Discusses challenges and potential solutions from theoreticians and practitioners on modeling design analysis and optimization for embedded and cyber physical systems Includes coverage of model verification communication software runtime systems operating systems and real time computing

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