

Loop Parallelization - Example



- Iterations of loop i must be executed sequentially, but the iterations of loop j may be executed in parallel.

Why?

- Inner loop parallelism.

Loop Parallelization

Utpal Banerjee



Loop Parallelization:

Loop Parallelization Utpal Banerjee, 2013-06-29 Automatic transformation of a sequential program into a parallel form is a subject that presents a great intellectual challenge and promises a great practical award. There is a tremendous investment in existing sequential programs and scientists and engineers continue to write their application programs in sequential languages primarily in Fortran. The demand for higher speedups increases. The job of a restructuring compiler is to discover the dependence structure and the characteristics of the given machine. Much attention has been focused on the Fortran do loop. This is where one expects to find major chunks of computation that need to be performed repeatedly for different values of the index variable. Many loop transformations have been designed over the years and several of them can be found in any parallelizing compiler currently in use in industry or at a university research facility. The book series on KappaLoop Transformations for Restructuring Compilers provides a rigorous theory of loop transformations and dependence analysis. We want to develop the transformations in a consistent mathematical framework using objects like directed graphs, matrices, and linear equations. Then the algorithms that implement the transformations can be precisely described in terms of certain abstract mathematical algorithms. The first volume, Loop Transformations for Restructuring Compilers: The Foundations, provided the general mathematical background needed for loop transformations, including those basic mathematical algorithms discussed, data dependence, and introduced the major transformations. The current volume, Loop Parallelization, builds a detailed theory of iteration level loop transformations based on the material developed in the previous book.

Automatic Parallelization Samuel Midkiff, 2022-06-01 Compiling for parallelism is a longstanding topic of compiler research. This book describes the fundamental principles of compiling regular numerical programs for parallelism. We begin with an explanation of analyses that allow a compiler to understand the interaction of data reads and writes in different statements and loop iterations during program execution. These analyses include dependence analysis, use-def analysis, and pointer analysis. Next, we describe how the results of these analyses are used to enable transformations that make loops more amenable to parallelization and discuss transformations that expose parallelism to target shared memory, multicore, and vector processors. We then discuss some problems that arise when parallelizing programs for execution on distributed memory machines. Finally, we conclude with an overview of solving Diophantine equations and suggestions for further readings in the topics of this book to enable the interested reader to delve deeper into the field. Table of Contents: Introduction and overview; Dependence analysis: dependence graphs and alias analysis; Program parallelization: Transformations to modify and eliminate dependences; Transformation of iterative and recursive constructs; Compiling for distributed memory machines; Solving Diophantine equations; A guide to further reading.

Symbolic Parallelization of Nested Loop Programs Alexandru-Petru Tanase, Frank Hannig, Jürgen Teich, 2018-02-22 This book introduces new compilation techniques using the polyhedron model for the resource adaptive parallel execution of loop programs on massively parallel

processor arrays The authors show how to compute optimal symbolic assignments and parallel schedules of loop iterations at compile time for cases where the number of available cores becomes known only at runtime The compile runtime symbolic parallelization approach the authors describe reduces significantly the runtime overhead compared to dynamic or just in time compilation The new on demand fault tolerant loop processing approach described in this book protects loop nests for parallel execution against soft errors

OpenMP Shared Memory Parallel Programming Matthias S. Müller, 2008-05-21

This book constitutes the thoroughly refereed post workshop proceedings of the First and the Second International Workshop on OpenMP IWOMP 2005 and IWOMP 2006 held in Eugene OR USA and in Reims France in June 2005 and 2006 respectively The first part of the book presents 16 revised full papers carefully reviewed and selected from the IWOMP 2005 program and organized in topical sections on performance tools compiler technology run time environment applications as well as the OpenMP language and its evaluation In the second part there are 19 papers of IWOMP 2006 fully revised and grouped thematically in sections on advanced performance tuning aspects of code development applications and proposed extensions to OpenMP

Parallel Processing and Applied Mathematics Roman Wyrzykowski, Jack Dongarra, Ewa Deelman, Konrad Karczewski, 2018-03-22 The two volume set LNCS 10777 and 10778 constitutes revised selected papers from the 12th International Conference on Parallel Processing and Applied Mathematics PPAM 2017 held in Lublin Poland in September 2017 The 49 regular papers presented in this volume were selected from 98 submissions For the workshops and special sessions that were held as integral parts of the PPAM 2017 conference a total of 51 papers was accepted from 75 submissions The papers were organized in topical sections named as follows Part I numerical algorithms and parallel scientific computing particle methods in simulations task based paradigm of parallel computing GPU computing parallel non numerical algorithms performance evaluation of parallel algorithms and applications environments and frameworks for parallel distributed cloud computing applications of parallel computing soft computing with applications and special session on parallel matrix factorizations Part II workshop on models algorithms and methodologies for hybrid parallelism in new HPC systems workshop power and energy aspects of computations PEAC 2017 workshop on scheduling for parallel computing SPC 2017 workshop on language based parallel programming models WLPP 2017 workshop on PGAS programming minisymposium on HPC applications in physical sciences minisymposium on high performance computing interval methods workshop on complex collective systems

Languages and Compilers for Parallel Computing Utpal

Banerjee, 1993-12-08 The articles in this volume are revised versions of the best papers presented at the Fifth Workshop on Languages and Compilers for Parallel Computing held at Yale University August 1992 The previous workshops in this series were held in Santa Clara 1991 Irvine 1990 Urbana 1989 and Ithaca 1988 As in previous years a reasonable cross section of some of the best work in the field is presented The volume contains 35 papers mostly by authors working in the U S or Canada but also by authors from Austria Denmark Israel Italy Japan and the U K

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Parallel Computing James Brodman, Peng Tu, 2015-04-30 This book constitutes the thoroughly refereed post conference proceedings of the 27th International Workshop on Languages and Compilers for Parallel Computing LCPC 2014 held in Hillsboro OR USA in September 2014 The 25 revised full papers were carefully reviewed and selected from 39 submissions The papers are organized in topical sections on accelerator programming algorithms for parallelism compilers debugging vectorization

Software Engineering for Parallel and Distributed Systems Innes Jelly, Ian Gorton, Peter Croll, 2016-01-09 A wide range of modern computer applications require the performance and flexibility of parallel and distributed systems Better software support is required if the technical advances in these systems are to be fully exploited by commerce and industry This involves the provision of specialised techniques and tools as well as the integration of standard software engineering methods This book will reflect current advances in this area and will address issues of theory and practice with contributions from academia and industry It is the aim of the book to provide a focus for information on this developing which will be of use to both researchers and practitioners

Compiler Optimizations for Scalable Parallel Systems Santosh Pande, Dharma P. Agrawal, 2003-06-29 Scalable parallel systems or more generally distributed memory systems offer a challenging model of computing and pose fascinating problems regarding compiler optimization ranging from language design to run time systems Research in this area is foundational to many challenges from memory hierarchy optimizations to communication optimization This unique handbook like monograph assesses the state of the art in the area in a systematic and comprehensive way The 21 coherent chapters by leading researchers provide complete and competent coverage of all relevant aspects of compiler optimization for scalable parallel systems The book is divided into five parts on languages analysis communication optimizations code generation and run time systems This book will serve as a landmark source for education information and reference to students practitioners professionals and researchers interested in updating their knowledge about or active in parallel computing

Exploitation of Fine-Grain Parallelism Günter Böckle, 1995-07-18 Many parallel computer architectures are especially suited for particular classes of applications However there are only a few parallel architectures equally well suited for standard programs Much effort is invested into research in compiler techniques to make programming parallel machines easier This book presents methods for automatic parallelization so that programs need not to be tailored for specific architectures here the focus is on fine grain parallelism offered by most new microprocessor architectures The book addresses compiler writers computer architects and students by demonstrating the manifold complex relationships between architecture and compiler technology

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