Raise Specification Language

A Gutmann

Raise Specification Language:

The RAISE Specification Language RAISE Language Group,1992 Written with a tutorial section meant for self study as well as a reference section this book is designed for those who use or who plan to use RAISE as well as for those who would like to explore formal methods and specification It includes numerous examples of the use of RSL and an index

Specification Case Studies in RAISE Hung Dang Van,2002-02-08 This volume presents twelve case studies that use RAISE Rigorous Approach to Industrial Software Engineering to construct analyse develop and apply formal specifications The case studies cover a wide range of application areas including government finance case based reasoning multi language text processing object oriented design patterns component based software design and natural resource management By illustrating the variety of uses of formal specifications the case studies also raise questions about the creation purpose and scope of formal models before they are built Additional resources and complete specifications for all of the case studies and the RAISE tools used to process them are available on the World Wide Web This book will be of particular interest to software engineers especially those responsible for the initial stages of requirements engineering and software architecture and design It will also be of interest to academics and students on advanced formal methods courses **Logics of Specification Languages** Dines Bjørner,Martin C. Henson,2007-12-05 This book presents comprehensive studies on nine specification languages and their logics of reasoning The editors and authors are authorities on these specification languages and their application In a unique feature the book closes with short commentaries on the specification languages written by researchers closely associated with their original development The book contains extensive references and pointers to future developments **VDM '91. Formal Software Development Methods. 4th International Symposium of VDM Europe, Noordwijkerhout, The Netherlands, October 21-25, 1991. Proceedings** Soren Prehn,Hans Toetenel,1991-10-14 The proceedings of the fourth Vienna Development Method Symposium VDM 91 are published here in two volumes Previous VDM symposia were held in 1987 LNCS 252 1988 LNCS 328 and 1990 LNCS 428 The VDM symposia have been organized by the VDM Europe formed in 1985 as an advisory board sponsored by the Commission of the European Communities The VDM Europe working group consisted of reasearchers software engineers and programmers all interested in prommoting the industrial usage of formal methods for software development The fourth VDM symposium presented not only VDM but also a large number of other methods for formal software development Volume 1 contains the conference contributions It has four parts contributions of invited speakers papers project reports and tools demonstration abstracts The emphasis is on methods and calculi for development verification and verification tools support experiences from doing developments and the associated theoretical problems Volume2 contains four introductory tutorials on LARCH Refinement Calculus VDM and RAISE and four advanced tutorials on ABEL PROSPECTRA THE B Method and The Stack They present a comprehensive account of the state of theart *The RAISE Specification Language* Dines Bjørner (et al.),1991 **Semantics of**

Specification Languages (SoSL) Derek J. Andrews,Jan F. Groote,C.A. Middelburg,2012-12-06 SoSL was the first International Workshop on Semantics of Specification Languages held from 25 27 October 1993 in Utrecht the Netherlands The workshop was organized by the Department of Philosophy of Utrecht University with financial support from the Nationale Faciliteit Informatica of the Nederlandse Organisatie voor Wetenschappelijk Onderzoek NWO and under the auspices of the British Computer Society S specialist group in Formal Aspects of Computing Science BCS FACS The concern of the workshop was the semantics of specification languages and the issues closely related to this area such as type checking and the justification of proof rules and proof obligations Its aim was the exchange of problems and ideas in this field of formal methods and the identification of common programs of work for further investigation The program of SoSL consisted of 3 invited lectures presenting the developments of the semantics of 3 major specification languages Furthermore there were 16 presentations of submitted papers This volume provides a direct account of the workshop It contains 3 papers that match the invited lectures and the 16 selected papers The editors want to thank all those who have contributed to the workshop the Program Committee and the referees for selecting the contributed papers the invited speakers for their interesting talks the Organizing Committee for all their efforts and of course the participants We have the feeling that the workshop was worthwhile and should be repeated   **VDM '91. Formal Software Development Methods. 4th International Symposium of VDM Europe, Noordwijkerhout, The Netherlands, October 21-25, 1991. Proceedings** Søren Prehn,W. J. Toetenel,1991 The proceedings of the fourth Vienna Development Method Symposium VDM 91 are published here in two volumes Previous VDM symposia were held in 1987 LNCS 252 1988 LNCS 328 and 1990 LNCS 428 The VDM symposia have been organized by the VDM Europe formed in 1985 as an advisory board sponsored by the Commission of the European Communities The VDM Europe working group consisted of reasearchers software engineers and programmers all interested in prommoting the industrial usage of formal methods for software development The fourth VDM symposium presented not only VDM but also a large number of other methods for formal software development Volume 1 contains the conference contributions It has four parts contributions of invited speakers papers project reports and tools demonstration abstracts The emphasis is on methods and calculi for development verification and verification tools support experiences from doing developments and the associated theoretical problems Volume2 contains four introductory tutorials on LARCH Refinement Calculus VDM and RAISE and four advanced tutorials on ABEL PROSPECTRA The B Method and The Stack They present a comprehensive account of the state of the art PUBLISHER S WEBSITE   Integrated Formal Methods Wolfgang Grieskamp,Thomas Santen,Bill Stoddart,2007-10-23   A Brief History of Computing Gerard O'Regan,2008-02-01 Overview The objective of this book is to provide an introduction into some of the key topics in the history of computing The computing eld is a vast area and a truly comp hensive account of its history would require several volumes The aims of this book are more modest and its goals are to give the reader a avour of some of the key topics and events in the history of

computing It is hoped that this will stimulate the interested reader to study the more advanced books and articles available The history of computing has its origins in the dawn of civilization Early hunter gatherer societies needed to be able to perform elementary calculations such as counting and arithmetic As societies evolved into towns and communities there was a need for more sophisticated calculations This included primitive accounting to determine the appropriate taxation to be levied as well as the development of geometry to enable buildings templates and bridges to be constructed Our account commenceswith the contributions of the Egyptians and Babylonians It moves on to the foundationalwork done by Boole and Babbage in the nineteenth century and to the importantwork on Boolean Logicand circuit design doneby Claude Shannon in the 1930s The theoretical work done by Turing on computability is considered as well as work done by von Neumann and others on the fundamental architecture for computers **Industrial Applications of Formal Methods to Model, Design and Analyze Computer Systems** Dan Craigen,Susan Gerhart,2012-12-02 Formal methods are mathematically based techniques often supported by reasoning tools that can offer a rigorous and effective way to model design and analyze computer systems The purpose of this study is to evaluate international industrial experience in using formal methods The cases selected are representative of industrial grade projects and span a variety of application domains The study had three main objectives To better inform deliberations within industry and government on standards and regulations To provide an authoritative record on the practical experience of formal methods to date and To suggest areas where future research and technology development are needed This study was undertaken by three experts in formal methods and software engineering Dan Craigen of ORA Canada Susan Gerhart of Applied Formal Methods and Ted Ralston of Ralston Research Associates Robin Bloomfield of Adelard was involved with the Darlington Nuclear Generating Station Shutdown System case Support for this study was provided by organizations in Canada and the United States The Atomic Energy Control Board of Canada AECB provided support for Dan Craigen and for the technical editing provided by Karen Summerskill The U S Naval Research Laboratories NRL Washington DC provided support for all three authors The U S National Institute of Standards and Technology NIST provided support for Ted Ralston **Algebraic Methodology and Software Technology** Charles Rattray,Savitri Maharaj,2004-06-24 This book constitutes the refereed proceedings of the 10th International Conference on Algebraic Methodology and Software Technology AMAST 2004 held in Stirling Scotland UK in July 2004 The 35 revised full papers presented together with abstracts of 5 invited talks and an invited paper were carefully reviewed and selected from 63 submissions Among the topics covered are all current issues in formal methods related to algebraic approaches to software engineering including abstract data types process algebras algebraic specification model checking abstraction refinement model checking state machines rewriting Kleene algebra programming logic etc **Formal Methods and Software Engineering** Jim Davies,Wolfram Schulte,Mike Barnett,2004-10-28 Formal engineering methods are changing the way that software systems are veloped Withlanguageandtoolsupport theyarebeingusedforautomaticcode generation and for

the automatic abstraction and checking of implementations In the future they will be used at every stage of development requirements speci cation design implementation testing and documentation The ICFEM series of conferences aims to bring together those interested in the application of formal engineering methods to computer systems Researchers and practitioners from industry academia and government are encouraged to attend andtohelpadvancethestateoftheart Authorsarestronglyencouraged to make their ideas as accessible as possible and there is a clear emphasis upon work that promises to bring practical tangible bene t reports of case studies should have a conceptual message theory papers should have a clear link to application and papers describing tools should have an account of results ICFEM 2004 was the sixth conference in the series and the rst to be held in North America Previous conferences were held in Singapore China UK A tralia and Japan The Programme Committee received 110 papers and selected 30forpresentation The nalversionsofthosepapersareincludedhere together with 2 page abstracts for the 5 accepted tutorials and shorter abstracts for the 4 invited talks   Encyclopedia of Information Science and Technology Mehdi Khosrow-Pour,Mehdi Khosrowpour,2009 This set of books represents a detailed compendium of authoritative research based entries that define the contemporary state of knowledge on technology Provided by publisher   **The B Language and Method** Kevin Lano,2012-12-06 B is one of the few formal methods which has robust commercially available tool support for the entire development lifecycle from specification through to code generation This volume provides a comprehensive introduction to the B Abstract Machine Notation and to how it can be used to support formal specification and development of high integrity systems A strong emphasis is placed on the use of B in the context of existing software development methods including object oriented analysis and design The text includes a large number of worked examples graduated exercises in B AMN specification and development all of which have been class tested two extended case studies of the development process and an appendix of proof techniques suitable for B Based on material which has been used to teach B at postgraduate and undergraduate level this volume will provide invaluable reading a wide range of people including students project technical managers and workers and researchers with an interest in methods integration and B semantics   Encyclopedia of Information Science and Technology, Third Edition Khosrow-Pour, D.B.A., Mehdi,2014-07-31 This 10 volume compilation of authoritative research based articles contributed by thousands of researchers and experts from all over the world emphasized modern issues and the presentation of potential opportunities prospective solutions and future directions in the field of information science and technology Provided by publisher   *A Practical Approach to Software Quality* Gerard O'Regan,2012-11-03 Overview The aim of this book is to provide a practical introduction to software quality in an industrial environment and is based on the author s experience in working in software engineering and software quality improvement with leading indus trial companies The book is written from a practitioner s viewpoint and the objective is to include both theory and practice The reader will gain a grasp of the fundamentals as well as guidance on the practical application of the

theory The principles of software quality management and software process im provement are discussed and guidance on the implementation of maturity mod els such as the CMM SPICE or the ISO 9000 2000 standard is included Organization and Features The first chapter provides an introduction to the fundamentals of quality man agement Later chapters consider software inspections and testing ISO 9000 the CMM the evolving SPICE standard metrics and problem solving and the final chapter on formal methods and design considers some advanced topics includ ing configuration management UML software usability and formal methods The reader may find the material heavy going in places especially in the section on formal methods and this section may be skipped The book includes a chapter on software inspections and testing and this in cludes material on Fagan inspections to build quality into the software product **Formal Methods for Open Object-Based Distributed Systems V** Bart Jacobs,Arend Rensink,2013-04-17 Formal Methods for Open Object Based Distributed Systems V brings together research in three important and related fields Formal methods Distributed systems Object based technology Such a convergence is representative of recent advances in the field of distributed systems and provides links between several scientific and technological communities The wide scope of topics covered in this volume range in subject from UML to object based languages and calculi and security and in approach from specification to case studies and verification This volume comprises the proceedings of the Fifth International Conference on Formal Methods for Open Object Based Distributed Systems FMOODS 2002 which was sponsored by the International Federation for Information Processing IFIP and held in Enschede The Netherlands in March 2002 *Theoretical Aspects of Computing - ICTAC 2004* Zhiming Liu,Keijiro Araki,2005-02-22 This book constitutes the thoroughly refereed postproceedings of the First International Colloquium on Theoretical Aspects of Computing ICTAC 2004 The 34 revised full papers presented together with 4 invited contributions were carefully selected from 111 submissions during two rounds of reviewing and improvement The papers are organized in topical sections on concurrent and distributed systems model integration and theory unification program reasoning and testing verification theories of programming and programming languages real time and co design and automata theory and logics VDM '87. VDM - A Formal Method at Work VDM-Europe,1987-03-11 Erste Untersuchungen der Halswirbels ule werden stets mit Hilfe von R ntgenaufnahmen durchgef hrt und in den meisten F llen gen gen diese als Grundlage f r die Diagnose Mi bildungen Tumoren und noch fter Traumata Rheuma und sogar ganz gew hnliche Nackenschmerzen erfordern eine radiologische Untersuchung der Wirbels ule Die Auswertung jedoch ist schwierig Nimmt man einen Halswirbel in die Hand so stellt man fest um welch komplexes Gebilde es sich hierbei handelt Bei radiologischen Aufnahmen wird die Auswertung noch durch die sich berlappenden Knochenteile Anh ufungen und die verschiedenen Blickwinkel erschwert Das Buch von J F Bonneville und F Cattin stellt eine originelle Interpretationsmethode von R ntgenaufnahmen vor die die Auswertung wesentlich erleichtert Dieses Buch zeigt da zwei bzw dreidimensionale Computertomogramme eine ausgezeichnete Hilfe zum Verst ndnis von konventionellen R ntgenbildern sein k nnen Der Leser

bekommt gleichsam Zugang zu jedem Einzelteil des Knochens und von da an wird alles einfach berlappungen verschwinden die in der R ntgenaufnahme verborgenen T cken werden sichtbar die Anatomie triumphiert das Bild lebt Die Halswirbels ule von J F Bonneville und F Cattin ist unentbehrlich f r jeden Radiologen in seiner t glichen Praxis aber ebenso auch f r Chirurgen Rheumatologen und Physiotherapeuten die sich f r die Halswirbels ule interessieren *ZUM'97: The Z Formal Specification Notation* Jonathan P. Bowen,Michael G. Hinchey,David Till,1997-03-20 This book constitutes the refereed proceedings of the 10th International Conference of Z Users ZUM 97 held in Reading UK in April 1997 The volume presents 18 revised full papers together with three invited presentations by internationally leading experts The papers are organized into topical sections on real time systems tools logic system development reactive systems refinement and applications Also a select Z bibliography by Jonathan Bowen is added All in all the book competently reports the state of the art in research and advanced applications of the Z notation

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web the nccih categorizes complementary approaches into three categories natural products e g vitamins probiotics and herb supplements mind and body e g yoga and

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web aug 30 2018   the national center for complementary and integrative health nccih reports that many americans 30 of adults use cih approaches citing general

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web jun 21 2022   different types of vhdl modelling styles the architecture of vhdl code is written in three different coding styles dataflow modelling behavioral modelling structural modelling 1 logic development for and gate the and logic gate can be realized as follows the truth table for and gate is a

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web mar 22 2022   in this chapter the general background and coding techniques of vhdl are highlighted basic concept to programming can be summarized as follow three main units constitute a vhdl code a working library an entity that describes

the external view of the system and an architecture that describes the behaviour or internal structure of the system

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web vhdl stands for very high speed integration circuit hdl hardware description language it is an ieee institute of electrical and electronics engineers standard hardware description language that is used to describe and simulate the behavior of complex digital circuits

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web as an alternative you can also write your simulation stimulus using generic vhdl code called a testbench the testbench codes gets compiled along with the design so that the stimulus is also in the compiled simulation model ieee came along and created additional libaries that are supported by almost all vhdl compilers specific silicon

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web for sample syntax and a list of vhdl statements supported by the vhdl synthesizer see appendix a quick reference for a list of exceptions and constraints on the vhdl synthesizer s support of vhdl see appendix b limitations this chapter shows you the structure of a vhdl design and then

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web nov 8 2018   in this post we will take a look at implementing the vhdl code for all logic gates using dataflow architecture first we will take a look at the logic equations of all the gates and then the syntax we will also test our logic by writing a testbench

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web from wikipedia the free encyclopedia vhdl source for a signed vhsic hardware description languagevhdl is a hardware description language hdl that can model the behavior and structure of at multiple levels of abstraction ranging from the system level down to that of logic gates for design entry documentation and verification purposes

from vhdl code to real hardware designing an 8 bit alu - Feb 10 2022

web oct 22 2023   alu project overview in this project i will walk through the creation of the 8 bit alu circuit with input dip switches and output leds demonstrated in operation in figure 1 i used the vhdl language to code the alu and ran it on a complex programmable logic device cpld development board

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web vhdl tool vhdl tool provides a language server for the vhdl hardware description language this plugin makes vhdl tool s language server interface available from within vscode this is beta quality code report bugs here

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web apr 7 2014   the vhdl code for full adder circuit adds three one bit binary numbers a b cin and outputs two one bit binary numbers a sum s and a carry cout truth table describes the functionality of full adder sum s output is high when odd number of inputs are high cout is high when two or more inputs are high

<u>and all elements of an n bit array in vhdl stack overflow</u> - Apr 14 2022

web nov 30 2013   lets say i have an n bit array i want to and all elements in the array similar to wiring each element to an n bit and gate how do i achieve this in vhdl note i am trying to use re usable vhdl code so i want to avoid hard coding something like result array 0 and array 1 and array 2 and array n thanks oshara

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web this tutorial describes language features that are common to all versions of the language they are expressed using the syntax of vhdl 93 and subsequent versions there are some aspects of syntax that are incompatible with the original vhdl 87 version however most tools now support at least vhdl 93 so syntactic differences

using the work library in vhdl xilinx support - Sep 19 2022

web feb 16 2023   by using the coding style above there is no question about which version of bottom is wanted the version in the library my lib1 is the correct one care should be taken when choosing library names for vhdl files while a library called work is a common library name for many projects the tool will handle it a little differently than

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web high level synthesisable vhdl hvhdl is a set of coding patterns for standard vhdl which are designed to greatly increase abstraction level of vhdl source code using standard synthesis tools the coding patterns are designed to support incremental design testing and development of the vhdl source code

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web overview visual studio code is an open source editor from microsoft vhdl tool makes its services available to vs code through the language server protocol vscode plugin find the plugin by searching for vhdl in the extensions sidebar or go to the online marketplace follow the instructions in the readme

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web dec 29 2017   vhdl uses a similar description however it has its own syntax for example it uses the following lines of code to describe the input and output ports of this circuit 1 entity circuit 1 is 2 port a in std logic 3 b in std logic 4 out1 out std logic 5 end circuit 1

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web apr 29 2016   vhdl was created in the 1980s to develop a high speed integrated circuit it has since become an industry standard language for describing digital systems at the structural or behavioral levels in a text based format the vhdl file can be used to manufacture a physical circuit such as a motherboard processor or memory module

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web basic logic gates esd chapter 2 figure 2 3 every vhdl design description consists of at least one entity architecture pair or one entity with multiple architectures the entity section of the hdl design is used to declare the i o ports of the circuit while the description code resides within architecture portion

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web quartus and vhdl code in this section you should learn how to implement a vhdl code for single cycle data path before we go any further let s define some of the terms that we use throughout the book entity all designs are expressed in terms of entities an entity is the most basic building block in a design