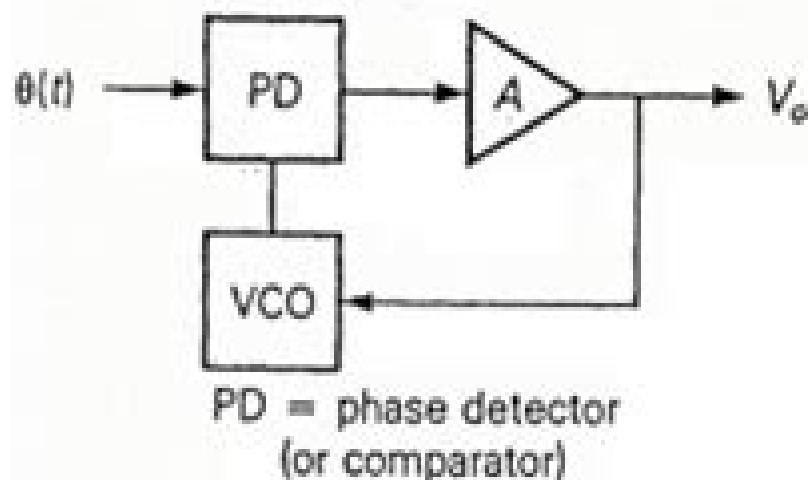


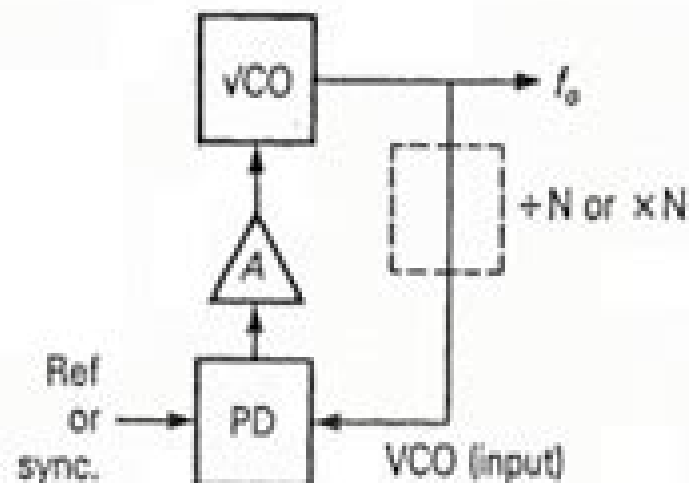
Chapter 10.

Phase-Locked Loops

- Different Applications of PLLs:



(a) Coherent phase detector
or FM demodulator



(b) Stabilized VCO (voltage-controlled oscillator).
Applications: frequency synthesizer;
TV horizontal AFC.

Phase Locked Loops

Donald R. Stephens



Phase Locked Loops:

Phase-locked Loops Roland E. Best, 1984 **Phase Locked Loops 6/e** Roland E. Best, 2007-08-13 The Definitive Introduction to Phase Locked Loops Complete with Software for Designing Wireless Circuits The Sixth Edition of Roland Best's classic Phase Locked Loops has been updated to equip you with today's definitive introduction to PLL design complete with powerful PLL design and simulation software written by the author Filled with all the latest PLL advances this celebrated sourcebook now includes new chapters on frequency synthesis CAD for PLLs mixed signal PLLs all digital PLLs and software PLLs plus a new collection of sample communications applications An essential tool for achieving cutting edge PLL design the Sixth Edition of Phase Locked Loops features A wealth of easy to use methods for designing phase locked loops Over 200 detailed illustrations New to this edition new chapters on frequency synthesis including fractional N PLL frequency synthesizers using sigma delta modulators CAD for PLLs mixed signal PLLs all digital PLLs and software PLLs new PLL communications applications including an overview on digital modulation techniques Inside this Updated PLL Design Guide Introduction to PLLs Mixed Signal PLL Components Mixed Signal PLL Analysis PLL Performance in the Presence of Noise Design Procedure for Mixed Signal PLLs Mixed Signal PLL Applications Higher Order Loops CAD and Simulation of Mixed Signal PLLs All Digital PLLs ADPLLs CAD and Simulation of ADPLLs The Software PLL SPLL The PLL in Communications State of the Art Commercial PLL Integrated Circuits Appendices The Pull In Process The Laplace Transform Digital Filter Basics Measuring PLL Parameters **Phase-Locked Loops** Roland Best, 2003-07-11 Phase Locked Loops PLLs are electronic circuits used for frequency control Anything using radio waves from simple radios and cell phones to sophisticated military communications gear uses PLLs The communications industry's big move into wireless in the past two years has made this mature topic red hot again The fifth edition of this classic circuit reference comes complete with extremely valuable PLL design software written by Dr Best The software alone is worth many times the price of the book The new edition also includes new chapters on frequency synthesis CAD for PLLs mixed signal PLLs and a completely new collection of sample communications applications **Phase Locked Loops** J. Encinas, 2012-09-24 This book is devoted to a detailed and comprehensive study of phase locked loops aimed at preparing the reader to design them and to understand their applications It is written at a level corresponding to a final year electronics undergraduate or a postgraduate student Linear and semidigital phase locked loops are studied in nine chapters Most of this book is concerned with analogue PLLs but there are chapters on semidigital PLLs and on applications The mathematical tools and background required are described at the end of the book Important symbols A Amplifier gain Mixer gain V_1 A Filter bandwidth Hz B_i Low pass filter bandwidth Hz B_L Unilateral equivalent noise bandwidth Hz B_n D s Polynomial of variable s Peak amplitude of signal voltage V E_e Peak amplitude of reference signal voltage V E_r Carrier frequency Hz I_e Intermediate frequency Hz I_i Intermediate frequency Hz IIF Local oscillator frequency Hz i_t Reference frequency Hz I_r F s Transfer function of loop filter G Amplifier voltage gain k

FM modulator sensitivity rad/s/V $\frac{1}{mK}$ Motor coefficient rad/s Back electromotive force coefficient Vs/rad $\frac{1}{K_1}$ Reverse back electromotive force coefficient rad/V $\frac{1}{S_1}$ Ke PC conversion gain V/rad/s $\frac{1}{K_d}$ Motor torque coefficient Nm/A $\frac{1}{KM}$ $\frac{1}{1}$ VCO conversion gain rads/V K_o Conversion gain of PLL $\frac{S_2}{K_v}$ m Modulation factor m Integer n Integer n Loop order N N Integers representing division $\frac{1}{2}$ $\frac{1}{1}$

Phase-Lock Basics William F. Egan, 2007-11-02 Broad based and hands on Phase Lock Basics Second Edition is both easy to understand and easy to customize The text can be used as a theoretical introduction for graduate students or when used with MATLAB simulation software the book becomes a virtual laboratory for working professionals who want to improve their understanding of the design process and apply it to the demands of specific situations This second edition features a large body of new statistical data obtained from simulations and uses available experimental data for confirmation of the simulation results

Phase-Locked Loops Woogeun Rhee, Zhiping Yu, 2023-12-19 Phase Locked Loops Discover the essential materials for phase locked loop circuit design from fundamentals to practical design aspects A phase locked loop PLL is a type of circuit with a range of important applications in telecommunications and computing It generates an output signal with a controlled relationship to an input signal such as an oscillator which matches the phases of input and output signals This is a critical function in coherent communication systems with the result that the theory and design of these circuits are essential to electronic communications of all kinds Phase Locked Loops System Perspectives and Circuit Design Aspects provides a concise accessible introduction to PLL design It introduces readers to the role of PLLs in modern communication systems the fundamental techniques of phase lock circuitry and the possible applications of PLLs in a wide variety of electronic communications contexts The first book of its kind to incorporate modern architectures and to balance theoretical fundamentals with detailed design insights this promises to be a must own text for students and industry professionals The book also features Coverage of PLL basics with insightful analysis and examples tailored for circuit designers Applications of PLLs for both wireless and wireline systems Practical circuit design aspects for modern frequency generation frequency modulation and clock recovery systems Phase Locked Loops is essential for graduate students and advanced undergraduates in integrated circuit design as well researchers and engineers in electrical and computing subjects

Phase-Locked Loop Synthesizer Simulation Giovanni Bianchi, 2005-03-30 Phase Locked Loop frequency synthesis is a key component of all wireless systems This is a complete toolkit for PLL synthesizer design with MathCAD SIMetrix files included on CD allowing readers to perform sophisticated calculation and simulation exercises Describes how to calculate PLL performance by using standard mathematical or circuit analysis programs

Phaselock Techniques Floyd M. Gardner, 2005-07-25 A greatly revised and expanded account of phaselock technology The Third Edition of this landmark book presents new developments in the field of phaselock loops some of which have never been published until now Established concepts are reviewed critically and recommendations are offered for improved formulations The work reflects the author's own research and many years of hands on experience with phaselock loops Reflecting the

myriad of phaselock loops that are now found in electronic devices such as televisions computers radios and cell phones the book offers readers much new material including Revised and expanded coverage of transfer functions Two chapters on phase noise Two chapters examining digital phaselock loops A chapter on charge pump phaselock loops Expanded discussion of phase detectors and of oscillators A chapter on anomalous phaselocking A chapter on graphical aids including Bode plots root locus plots and Nichols charts As in the previous editions the focus of the book is on underlying principles which remain valid despite technological advances Extensive references guide readers to additional information to help them explore particular topics in greater depth Phaselock Techniques Third Edition is intended for practicing engineers researchers and graduate students This critically acclaimed book has been thoroughly updated with new information and expanded for greater depth

PHASELOCK TECHNIQUES. 1966(REPR.1967) Floyd Martin Gardner,1966 *Phase-Locked Loops for Wireless Communications* Donald R. Stephens,2002 A tutorial of phase locked loops from analogue implementations to digital and optical designs This text establishes a foundation of continuous time analysis techniques and maintains a consistent notation as discrete time and non uniform sampling are presented It examines charge pumps and the complementary sequential phase detector Frequency synthesizers and digital divider analysis techniques are also included in this edition Starting with a historical overview presenting analogue digital and optical PLLs discussing phase noise analysis and including circuits algorithms for data synchronization this volume illustrates the techniques being used in this field The subjects covered include development of phase locked loops from analogue to digital and optical with notation throughout expanded coverage of the loop filters used to design second and third order PLLs design examples on delay locked loops used to synchronize circuits on CPUs and ASICs new material on digital dividers that dominate a frequency synthesizer's noise floor techniques to analytically estimate the phase noise of a divider presentation of optical phase locked loops with primers on the optical components and fundamentals of optical mixing a section on automatic frequency control to provide frequency locking of the lasers instead of phase locking and a presentation of charge pumps counters and delay locked loops This volume includes the topics that should be of interest to wireless optics and the traditional phase locked loop specialist to design circuits and software algorithms

Phase-locked Loops & Their Application William C. Lindsey,Marvin Kenneth Simon,1978 *Design of Phase-locked Loop Circuits with Experiments* Howard M. Berlin,1978

Theory of the Non-linear Analog Phase Locked Loop Nikolaos I. Margaris,2004-05-18 This book develops for the first time a complete and connected nonlinear theory for the analog Phase Locked Loop PLL which clarifies the obscure points of its complex non linear behaviour The book suggests new non linear models for the PLL components and applies the averaging method to analyse PLL The book presents the physical interpretation of the PLL operation locates the difficulties presented by its operation and suggests solutions to overcome these problems Finally it provides closed form expressions for all the important measures of the PLL and proposes new design criteria

Frequency Acquisition Techniques for Phase Locked Loops

Daniel B. Talbot, 2012-10-09 How to acquire the input frequency from an unlocked state A phase locked loop PLL by itself cannot become useful until it has acquired the applied signal's frequency Often a PLL will never reach frequency acquisition capture without explicit assistive circuits Curiously few books on PLLs treat the topic of frequency acquisition in any depth or detail *Frequency Acquisition Techniques for Phase Locked Loops* offers a no nonsense treatment that is equally useful for engineers technicians and managers Since mathematical rigor for its own sake can degenerate into intellectual rigor mortis the author introduces readers to the basics and delivers useful information with clear language and minimal mathematics With most of the approaches having been developed through years of experience this completely practical guide explores methods for achieving the locked state in a variety of conditions as it examines Performance limitations of phase frequency detector based phase locked loops The quadrature correlator method for both continuous and sampled modes Sawtooth ramp and sample phase detector and how its waveform contains frequency error information that can be extracted The benefits of a self sweeping self extinguishing topology Sweep methods using quadrature mixer based lock detection The use of digital implementations versus analog *Frequency Acquisition Techniques for Phase Locked Loops* is an important resource for RF microwave engineers in particular circuit designers practicing electronics engineers involved in frequency synthesis phase locked loops carrier or clock recovery loops radio frequency integrated circuit design and aerospace electronics and managers wanting to understand the technology of phase locked loops and frequency acquisition assistance techniques or jitter attenuating loops Errata can be found by visiting the Book Support Site at <http://booksupport.wiley.com>

Phase-Locked Loops John L. Stensby, 1997-06-19 Applications of phase locked loops play an increasingly important role in modern electronic systems and the last 25 years have seen new developments in the underlying theories as well *Phase Locked Loops* presents the latest information on the basic theory and applications of PLLs Organized in a logical format it first introduces the subject in a qualitative manner and discusses key applications Next it develops basic models for components of a PLL and these are used to develop a basic PLL model The text then discusses both linear and nonlinear methods that are used to analyze the basic PLL model This book includes extensive coverage of the nonlinear behavior of phase locked loops an important area of this field and one where exciting new research is being performed No other book available covers this critical area in such careful detail Improvements brought about by the advent of the personal computer especially in the use of numerical results are integrated into the text This book also focuses on PLL component technologies used in system implementation

Phase-locked Loops William C. Lindsey, C. M. Chie, 1986 Good No Highlights No Markup all pages are intact Slight Shelfwear may have the corners slightly dented may have slight color changes slightly damaged spine

Phase-Locked Loops for Wireless Communications Donald R. Stephens, 2012-12-06 This book is intended for the graduate or advanced undergraduate engineer The primary motivation for writing the text was to present a complete tutorial of phase locked loops with a consistent notation As such it can serve as a textbook in formal classroom instruction or

as a self study guide for the practicing engineer A former colleague Kevin Kreitzer had suggested that I write a text with an emphasis on digital phase locked loops As modern designers we were continually receiving requests from other engineers asking for a definitive reference on digital phase locked loops There are several good papers in the literature but there was not a good textbook for either classroom or self paced study From my own experience in designing low phase noise synthesizers I also knew that third order analog loop design was omitted from most texts With those requirements the material in the text seemed to flow naturally Chapter 1 is the early history of phase locked loops I believe that historical knowledge can provide insight to the development and progress of a field and phase locked loops are no exception As discussed in Chapter 1 consumer electronics color television prompted a rapid growth in phase locked loop theory and applications much like the wireless communications growth today xiv Preface Although all analog phase locked loops are becoming rare the continuous time nature of analog loops allows a good introduction to phase locked loop theory

Design of CMOS Phase-Locked Loops Behzad Razavi, 2020-01-30 Using a modern pedagogical approach this textbook gives students and engineers a comprehensive and rigorous knowledge of CMOS phase locked loop PLL design for a wide range of applications It features intuitive presentation of theoretical concepts built up gradually from their simplest form to more practical systems broad coverage of key topics including oscillators phase noise analog PLLs digital PLLs RF synthesizers delay locked loops clock and data recovery circuits and frequency dividers tutorial chapters on high performance oscillator design covering fundamentals to advanced topologies and extensive use of circuit simulations to teach design mentality highlight design flaws and connect theory with practice Including over 200 thought provoking examples highlighting best practices and common pitfalls 250 end of chapter homework problems to test and enhance the readers understanding and solutions and lecture slides for instructors this is the perfect text for senior undergraduate and graduate level students and professional engineers who want an in depth understanding of PLL design

Phase-locked Loop Engineering Handbook for Integrated Circuits Stanley J. Goldman, 2007-01-01 Phased locked loops PLLs are control systems that have become indispensable in today's electronic circuitry This highly accessible handbook is a practical resource that electronics engineers and circuit designers will find invaluable when developing these systems PLLs are highly complex to design and are just as difficult to test To speed development and ensure effective testing engineers can turn to this collection of practical solutions SPICE listings simulation techniques and testing set ups The book offers in depth coverage of monolithic phase locked loops and the latest generation of PLLs showing how to meet the demand for high powered low cost electronics Moreover this cutting edge volume examines the complexities and new technologies for integrating monolithic PLLs on a single chip

Phase-Locked Loops Shambhu N. Sharma, 2020-09-02 The historic account of the Phase Locked Loops can be traced back from the idea of designing an electromechanical system with the objective of controlling the oscillation of the pendulum of the bell Great George The method is to contrast the phase of pendulum and the incoming telegraph signal phase

using the electromechanical system That generates the correction signal varying the pendulum oscillation The idea was conceived as well as implemented by David Robertson Professor of Electrical Engineering at the University of Bristol The term Phase Locked Loop was coined to this technique by later Researchers in 1932 Professor David Robertson is credited to the Phase Locked Loop for pioneering the technique In general setting the Phase Locked Loops are for synchronization purposes The phase locked loops perspective hinges on the analysis functions and applications

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