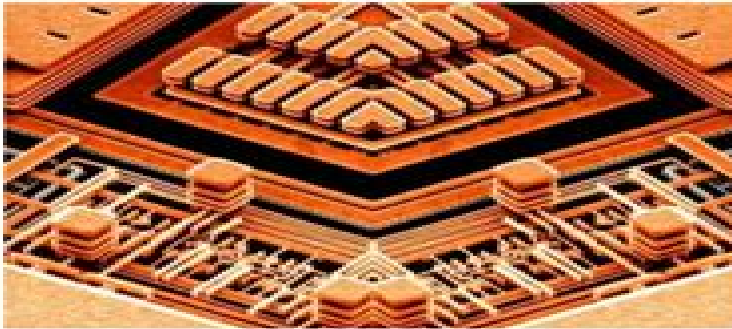
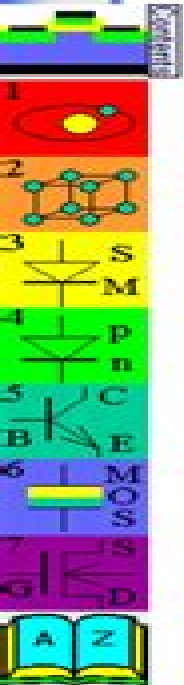


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Soviet Physics, 1989 **Physical Limitations of Semiconductor Devices** Vladislav A. Vashchenko, V. F. Sinkevitch, 2008-03-22 Since the beginning of semiconductor era in microelectronics the methodology of reliability assessment became a well established area In most cases the reliability assessment involves statistical methods for safe operating area and long term reliability parameters at the development of semiconductor processes components and systems At the same time in case of catastrophic failures at any development phase the major practical method is failure analysis FA However FA is mainly dealing with detection of consequences of some irreversible event that already happened This book is focused on the most important and the less summarized reliability aspects Among them catastrophic failures impact of local structural inhomogeneities major principles of physical limitation of safe operating area SOA physical mechanisms of the current instability filamentation and conductivity modulation in particular device types and architectures Specifically the similar principles and regularities are discussed for electrostatic

discharge ESD protection devices treating them as a particular case of pulsed power devices Thus both the most intriguing applications and reliability problems in case of the discrete and the integrated components are covered in this book

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