

# **2022 IEEE 28th International Symposium on On-Line Testing and Robust System Design (IOLTS 2022)**

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# On Line Testing Symposium Iolts 2005 Proceedings

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## **On Line Testing Symposium Iolts 2005 Proceedings:**

*On-Line Testing Symposium, 2003. IOLTS 2003. 9th IEEE C. Metra, 2003*      **Progress in Cryptology - INDOCRYPT 2008** Dipanwita Roy Chowdhury, Vincent Rijmen, Abhijit Das, 2008-12 This book constitutes the refereed proceedings of the 9th International Conference on Cryptology in India INDOCRYPT 2008 held in Kharagpur India in December 2008 The 33 revised full papers were carefully reviewed and selected from 111 submissions The papers are organized in topical sections on stream ciphers cryptographic hash functions public key cryptography security protocols hardware attacks block ciphers cryptographic hardware elliptic curve cryptography and threshold cryptography      **Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation** Jose L. Ayala, Braulio Garcia-Camara, Manuel Prieto, Martino Ruggiero, Gilles Sicard, 2011-09-15 This book constitutes the refereed proceedings of the 21st International Conference on Integrated Circuit and System Design PATMOS 2011 held in Madrid Spain in September 2011 The 34 revised full papers presented were carefully reviewed and selected from numerous submissions The paper feature emerging challenges in methodologies and tools for the design of upcoming generations of integrated circuits and systems and focus especially on timing performance and power consumption as well as architectural aspects with particular emphasis on modeling design characterization analysis and optimization      **Proceedings ,2007      Rugged Embedded Systems** Augusto Vega, Pradip Bose, Alper Buyuktosunoglu, 2016-12-02 Rugged Embedded Systems Computing in Harsh Environments describes how to design reliable embedded systems for harsh environments including architectural approaches cross stack hardware software techniques and emerging challenges and opportunities A harsh environment presents inherent characteristics such as extreme temperature and radiation levels very low power and energy budgets strict fault tolerance and security constraints etc that challenge the computer system in its design and operation To guarantee proper execution correct safe and low power in such scenarios this contributed work discusses multiple layers that involve firmware operating systems and applications as well as power management units and communication interfaces This book also incorporates use cases in the domains of unmanned vehicles advanced cars and micro aerial robots and space exploration as examples of computing designs for harsh environments Provides a deep understanding of embedded systems for harsh environments by experts involved in state of the art autonomous vehicle related projects Covers the most important challenges fault tolerance power efficiency and cost effectiveness faced when developing rugged embedded systems Includes case studies exploring embedded computing for autonomous vehicle systems advanced cars and micro aerial robots and space exploration

**Resilience Assessment and Evaluation of Computing Systems** Katinka Wolter, Alberto Avritzer, Marco Vieira, Aad van Moorsel, 2012-11-02 The resilience of computing systems includes their dependability as well as their fault tolerance and security It defines the ability of a computing system to perform properly in the presence of various kinds of disturbances and to recover from any service degradation These properties are immensely important in a world where many aspects of our

daily life depend on the correct reliable and secure operation of often large scale distributed computing systems Wolter and her co editors grouped the 20 chapters from leading researchers into seven parts an introduction and motivating examples modeling techniques model driven prediction measurement and metrics testing techniques case studies and conclusions The core is formed by 12 technical papers which are framed by motivating real world examples and case studies thus illustrating the necessity and the application of the presented methods While the technical chapters are independent of each other and can be read in any order the reader will benefit more from the case studies if he or she reads them together with the related techniques The papers combine topics like modeling benchmarking testing performance evaluation and dependability and aim at academic and industrial researchers in these areas as well as graduate students and lecturers in related fields In this volume they will find a comprehensive overview of the state of the art in a field of continuously growing practical importance

*Soft Errors in Modern Electronic Systems* Michael Nicolaidis, 2010-09-24 This book provides a comprehensive presentation of the most advanced research results and technological developments enabling understanding qualifying and mitigating the soft errors effect in advanced electronics including the fundamental physical mechanisms of radiation induced soft errors the various steps that lead to a system failure the modelling and simulation of soft error at various levels including physical electrical netlist event driven RTL and system level modelling and simulation hardware fault injection accelerated radiation testing and natural environment testing soft error oriented test structures process level device level cell level circuit level architectural level software level and system level soft error mitigation techniques The book contains a comprehensive presentation of most recent advances on understanding qualifying and mitigating the soft error effect in advanced electronic systems presented by academia and industry experts in reliability fault tolerance EDA processor SoC and system design and in particular experts from industries that have faced the soft error impact in terms of product reliability and related business issues and were in the forefront of the countermeasures taken by these companies at multiple levels in order to mitigate the soft error effects at a cost acceptable for commercial products In a fast moving field where the impact on ground level electronics is very recent and its severity is steadily increasing at each new process node impacting one after another various industry sectors as an example the Automotive Electronics Council comes to publish qualification requirements on soft errors research and technology developments and industrial practices have evolve very fast outdated the most recent books edited at 2004

*Robust Intelligent Systems* Alfons Schuster, 2008-08-06 Our time recognizes robustness as an important all pervading feature in the world around us Despite its omnipresence robustness is not entirely understood rather dif cult to de ne and despite its obvious value in many situations rather dif cult to achieve One of the goals of this edited book is to report on the topic of robustness from a variety and diverse range of elds and perspectives We are interested for instance in fundamental strategies nature applies to make systems robust and arguably intelligent and how these strategies may hold as general design principles in modern technology A particular focus is on computer based systems

and applications This in mind the book has four main sections Part I has a look at robustness in terms of underlying technologies and infrastructures upon which many computer based intelligent systems reside and investigates robustness on the hardware and software level but also in larger environments such as the Internet and self managing systems The contributions in Part II target robustness in research areas that are inspired by biology including brain computer interfaces biological networks and biological immune systems for example Part III involves the exciting field of artificial intelligence The chapters here discuss the value of robustness as a general design principle for artificial intelligence stressing its potential in areas such as humanoid robotics and image processing

**Hardware IP Security and Trust** Prabhat Mishra, Swarup Bhunia, Mark Tehranipoor, 2017-01-02 This book provides an overview of current Intellectual Property IP based System on Chip SoC design methodology and highlights how security of IP can be compromised at various stages in the overall SoC design fabrication deployment cycle Readers will gain a comprehensive understanding of the security vulnerabilities of different types of IPs This book would enable readers to overcome these vulnerabilities through an efficient combination of proactive countermeasures and design for security solutions as well as a wide variety of IP security and trust assessment and validation techniques This book serves as a single source of reference for system designers and practitioners for designing secure reliable and trustworthy SoCs

Energy-Efficient Fault-Tolerant Systems Jimson Mathew, Rishad A. Shafik, Dhiraj K. Pradhan, 2013-09-07 This book describes the state of the art in energy efficient fault tolerant embedded systems It covers the entire product lifecycle of electronic systems design analysis and testing and includes discussion of both circuit and system level approaches Readers will be enabled to meet the conflicting design objectives of energy efficiency and fault tolerance for reliability given the up to date techniques presented

System Dependability and Analytics Long Wang, Karthik Pattabiraman, Catello Di Martino, Arjun Athreya, Saurabh Bagchi, 2022-07-25 This book comprises chapters authored by experts who are professors and researchers in internationally recognized universities and research institutions The book presents the results of research and descriptions of real world systems services and technologies Reading this book researchers professional practitioners and graduate students will gain a clear vision on the state of the art of the research and real world practice on system dependability and analytics The book is published in honor of Professor Ravishankar K Iyer the George and Ann Fisher Distinguished Professor in the Department of Electrical and Computer Engineering at the University of Illinois at Urbana Champaign UIUC Urbana Illinois Professor Iyer is ACM Fellow IEEE Fellow AAAS Fellow and served as Interim Vice Chancellor of UIUC for research during 2008 2011 The book contains chapters written by many of his former students

**Fault Analysis in Cryptography** Marc Joye, Michael Tunstall, 2012-06-21 In the 1970s researchers noticed that radioactive particles produced by elements naturally present in packaging material could cause bits to flip in sensitive areas of electronic chips Research into the effect of cosmic rays on semiconductors an area of particular interest in the aerospace industry led to methods of hardening electronic devices designed for harsh environments Ultimately various

mechanisms for fault creation and propagation were discovered and in particular it was noted that many cryptographic algorithms succumb to so called fault attacks Preventing fault attacks without sacrificing performance is nontrivial and this is the subject of this book Part I deals with side channel analysis and its relevance to fault attacks The chapters in Part II cover fault analysis in secret key cryptography with chapters on block ciphers fault analysis of DES and AES countermeasures for symmetric key ciphers and countermeasures against attacks on AES Part III deals with fault analysis in public key cryptography with chapters dedicated to classical RSA and RSA CRT implementations elliptic curve cryptosystems and countermeasures using fault detection devices resilient to fault injection attacks lattice based fault attacks on signatures and fault attacks on pairing based cryptography Part IV examines fault attacks on stream ciphers and how faults interact with countermeasures used to prevent power analysis attacks Finally Part V contains chapters that explain how fault attacks are implemented with chapters on fault injection technologies for microprocessors and fault injection and key retrieval experiments on a widely used evaluation board This is the first book on this topic and will be of interest to researchers and practitioners engaged with cryptographic engineering

Fundamentals of IP and SoC Security Swarup Bhunia, Sandip Ray, Susmita Sur-Kolay, 2017-01-24 This book is about security in embedded systems and it provides an authoritative reference to all aspects of security in system on chip SoC designs The authors discuss issues ranging from security requirements in SoC designs definition of architectures and design choices to enforce and validate security policies and trade offs and conflicts involving security functionality and debug requirements Coverage also includes case studies from the trenches of current industrial practice in design implementation and validation of security critical embedded systems Provides an authoritative reference and summary of the current state of the art in security for embedded systems hardware IPs and SoC designs Takes a cross cutting view of security that interacts with different design and validation components such as architecture implementation verification and debug each enforcing unique trade offs Includes high level overview detailed analysis on implementation and relevant case studies on design verification debug issues related to IP SoC security

**Design and Testing of Reversible Logic** Ashutosh Kumar Singh, Masahiro Fujita, Anand Mohan, 2019-07-29 The book compiles efficient design and test methodologies for the implementation of reversible logic circuits The methodologies covered in the book are design approaches test approaches fault tolerance in reversible circuits and physical implementation techniques The book also covers the challenges and the reversible logic circuits to meet these challenges stimulated during each stage of work cycle The novel computing paradigms are being explored to serve as a basis for fast and low power computation

Information Security and Cryptology - ICISC 2008 Pil Joong Lee, Jung Hee Cheon, 2009-03-18 This book constitutes the refereed proceedings of the 11th International Conference on Information Security and Cryptology ICISC 2008 held in Seoul Korea during December 3 5 2008 The 26 revised full papers presented have gone through two rounds of reviewing and improvement and were carefully selected from 131 submissions The papers are organized in topical sections

on public key encryption key management and secret sharing privacy and digital rights digital signature and voting side channel attack hash and mac primitives and foundations as well as block and stream ciphers

**Intelligent Algorithms in Software Engineering** Radek Silhavy, 2020-08-08 This book gathers the refereed proceedings of the Intelligent Algorithms in Software Engineering Section of the 9th Computer Science On line Conference 2020 CSOC 2020 held on line in April 2020 Software engineering research and its applications to intelligent algorithms have now assumed an essential role in computer science research In this book modern research methods together with applications of machine and statistical learning in software engineering research are presented

Design and Test Technology for Dependable Systems-on-Chip Ubar, Raimund, Raik, Jaan, Vierhaus, Heinrich Theodor, 2010-12-31 This book covers aspects of system design and efficient modelling and also introduces various fault models and fault mechanisms associated with digital circuits integrated into System on Chip SoC Multi Processor System on Chip MPSoC or Network on Chip NoC

**Systems Engineering for Microscale and Nanoscale Technologies** M. Ann Garrison Darrin, Janet L. Barth, 2016-04-19 To realize the full potential of micro and nanoscale devices in system building it is critical to develop systems engineering methodologies that successfully integrate stand alone small scale technologies that can effectively interface with the macro world So how do we accomplish this Systems Engineering for Microscale and Nanoscale Technologies

*Hardware Security* Debdeep Mukhopadhyay, Rajat Subhra Chakraborty, 2014-10-29 Beginning with an introduction to cryptography Hardware Security Design Threats and Safeguards explains the underlying mathematical principles needed to design complex cryptographic algorithms It then presents efficient cryptographic algorithm implementation methods along with state of the art research and strategies for the design of very large scale integrated VLSI circuits and symmetric cryptosystems complete with examples of Advanced Encryption Standard AES ciphers asymmetric ciphers and elliptic curve cryptography ECC Gain a Comprehensive Understanding of Hardware Security from Fundamentals to Practical Applications Since most implementations of standard cryptographic algorithms leak information that can be exploited by adversaries to gather knowledge about secret encryption keys Hardware Security Design Threats and Safeguards Details algorithmic and circuit level countermeasures for attacks based on power timing fault cache and scan chain analysis Describes hardware intellectual property piracy and protection techniques at different levels of abstraction based on watermarking Discusses hardware obfuscation and physically unclonable functions PUFs as well as Trojan modeling taxonomy detection and prevention Design for Security and Meet Real Time Requirements If you consider security as critical a metric for integrated circuits ICs as power area and performance you ll embrace the design for security methodology of Hardware Security Design Threats and Safeguards

**Topics in Cryptology - CT-RSA 2020** Stanislaw Jarecki, 2020-02-14 This book constitutes the refereed proceedings of the Cryptographer s Track at the RSA Conference 2020 CT RSA 2020 held in San Francisco CA USA in February 2020 The 28 papers presented in this volume were carefully reviewed and selected from 95 submissions CT RSA is the track devoted to

scientific papers on cryptography public key to symmetric key cryptography and from cryptographic protocols to primitives and their implementation security



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