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Semiconductor on-Insulator and Thin Film Transistor Technology

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Semiconductor-on-Insulator and Thin Film Transistor Technology Held in Boston, Massachusetts on 3-6 December 1985 A. Chiang, M. W. Geis, L. Pfeiffer, MATERIALS RESEARCH SOCIETY PITTSBURGH PA., 1986 The papers in this book have been arranged into five parts The first three parts Zone Melting and Recrystallization Heteroepitaxy and Porous Silicon and Buried Oxide and Nitride by Implantation deal with specific approaches for fabricating Semiconductor on Insulator SOL material and structures The last two parts Characterization of SOL Thin Films and Device and Circuit Applications are more general in scope Keywords Symposia Silicon Oxides Nitrides Semiconductors Insulators Thin Film Transistors Zone Melting Recrystallization Proceedings of the Second Symposium on Thin Film Transistor Technologies Yue Kuo, 1995

Silicon-on-insulator and Buried Metals in Semiconductors ,1988 Proceedings of the ... Symposium on Thin Film Transistor Technologies ,1992 Low-temperature-processed Thin-film Transistors ,2011

Semiconductor-on-Insulator and Thin Film Transistor Technology: A. Chiang, M. W. Geis, L. Pfeiffer, 2014-06-05 The MRS Symposium Proceeding series is an internationally recognised reference suitable for researchers and practitioners This book was first published in 2006 Rapid Thermal Processing, 1986 Comparison of Thin Film Transistor and SOI Technologies H. W. Lam, M. J. Thompson, 1984

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