

# SEMICONDUCTING DEVICES

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A Bibliography of  
Fabrication Technology,  
Properties, and Applications

# Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications

**United States. National Bureau of  
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## **Semiconducting Devices A Bibliography Of Fabrication Technology Properties And Applications:**

**Semiconducting Devices** A. H. Agajanian, 1976      *Semiconducting Devices* A. H. Agajanian, 2013-05-14 Since the advent of planar technology the literature on semiconductor devices has been growing exponentially This book of over 14000 references is intended to make the world literature available to workers in the field beginners as well as experienced to assist them in finding out what has been done by others in their fields of endeavor The literature on theory preparation properties characterization packaging instrumentation and applications of semiconductor devices is thoroughly covered the only aspect of preparation not covered is diffusion However several books and comprehensive review articles on this subject are given in Chapter A Radiation damage due to ion implantation is given in Chapter F Radiation damage due to all other types of radiations is given in Chapter J The chapters on processing technology radiation damage structural defects and analysis modeling and packaging are quite general and apply to most types of semiconductor devices Special attention is given to electrical properties and applications of FET Schottky and charge coupled devices in Chapters M N O and P

Semiconductor Devices and Integrated Electronics A. G. Milnes, 2012-12-06 For some time there has been a need for a semiconductor device book that carries diode and transistor theory beyond an introductory level and yet has space to touch on a wider range of semiconductor device principles and applications Such topics are covered in specialized monographs numbering many hundreds but the voluminous nature of this literature limits access for students This book is the outcome of attempts to develop a broad course on devices and integrated electronics for university students at about senior year level The educational prerequisites are an introductory course in semiconductor junction and transistor concepts and a course on analog and digital circuits that has introduced the concepts of rectification amplification oscillators modulation and logic and Switching circuits The book should also be of value to professional engineers and physicists because of both the information included and the detailed guide to the literature given by the references The aim has been to bring some measure of order into the subject area examined and to provide a basic structure from which teachers may develop themes that are of most interest to students and themselves Semiconductor devices and integrated circuits are reviewed and fundamental factors that control power levels frequency speed size and cost are discussed The text also briefly mentions how devices are used and presents circuits and comments on representative applications Thus the book seeks a balance between the extremes of device physics and circuit design      Semiconductor Lithography Wayne M. Moreau, 2012-12-06 Semiconductor lithography is one of the key steps in the manufacturing of integrated silicon based circuits In fabricating a semiconductor device such as a transistor a series of hot processes consisting of vacuum film deposition oxidations and dopant implantation are all patterned into microscopic circuits by the wet processes of lithography Lithography as adopted by the semiconductor industry is the process of drawing or printing the pattern of an integrated circuit in a resist material The pattern is formed and overlayed to a previous circuit layer as many as 30 times in the manufacture of logic and memory devices With the resist

pattern acting as a mask a permanent device structure is formed by subtractive removal etching or by additive deposition of metals or insulators Each process step in lithography uses inorganic or organic materials to physically transform semiconductors of silicon insulators of oxides nitrides and organic polymers and metals into useful electronic devices All forms of electromagnetic radiation are used in the processing Lithography is a multidisciplinary science of materials processes and equipment interacting to produce three dimensional structures Many aspects of chemistry electrical engineering materials science and physics are involved The purpose of this book is to bring together the work of many scientists and engineers over the last 10 years and focus upon the basic resist materials the lithographic processes and the fundamental principles behind each lithographic process

*Proceedings of the Eighth International Conference on Chemical Vapor Deposition* J. M. Blocher, Guy E. Vuillard, Georg Wahl, 1981

### **Thin Films by Chemical Vapour**

**Deposition** C.E. Morosanu, 2016-06-22 The explosive growth in the semiconductor industry has caused a rapid evolution of thin film materials that lend themselves to the fabrication of state of the art semiconductor devices Early in the 1960s an old research technique named chemical vapour phase deposition CVD which has several unique advantages developed into the most widely used technique for thin film preparation in electronics technology In the last 25 years tremendous advances have been made in the science and technology of thin films prepared by means of CVD This book presents in a single volume an up to date overview of the important field of CVD processes which has never been completely reviewed previously

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**Microelectronic Packaging**, 1979 Good No Highlights No Markup all pages are intact Slight Shelfwear may have the corners slightly dented may have slight color changes slightly damaged spine **Bibliographies of Interest to the Atomic Energy Program**, 1962 **Nanoscale Science and Technology** Robert Kelsall, Ian W. Hamley, Mark Geoghegan, 2005-11-01 Nanotechnology is a vital new area of research and development addressing the control modification and fabrication of materials structures and devices with nanometre precision and the synthesis of such structures into systems of micro and macroscopic dimensions Future applications of nanoscale science and technology include motors smaller than the diameter of a human hair and single celled organisms programmed to fabricate materials with nanometer precision Miniaturisation has revolutionised the semiconductor industry by making possible inexpensive integrated electronic circuits comprised of devices and wires with sub micrometer dimensions These integrated circuits are now ubiquitous controlling everything from cars to toasters The next level of miniaturisation beyond sub micrometer dimensions into nanoscale dimensions invisible to the unaided human eye is a booming area of research and development This is a very hot area of research with large amounts of venture capital and government funding being invested worldwide as such Nanoscale Science and Technology has a broad appeal based upon an interdisciplinary approach covering aspects of physics chemistry

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