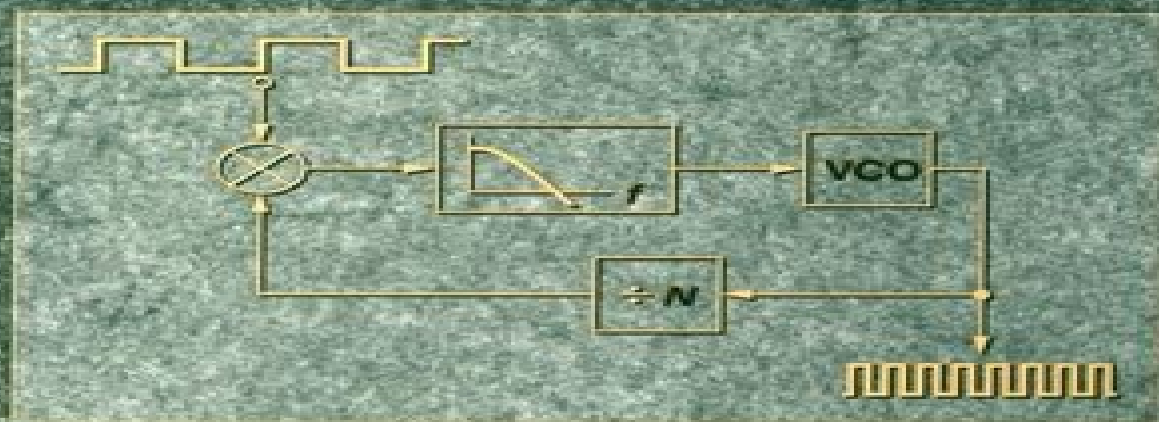


MONOLITHIC PHASE-LOCKED LOOPS AND CLOCK RECOVERY CIRCUITS

Theory and Design

edited by
BEHZAD RAZAVI



Monolithic Phase Locked Loops And Clock Recovery Circuits Theory And Design

Martin Clara



Monolithic Phase Locked Loops And Clock Recovery Circuits Theory And Design:

Monolithic Phase-Locked Loops and Clock Recovery Circuits Behzad Razavi, 1996-04-18 Featuring an extensive 40 page tutorial introduction this carefully compiled anthology of 65 of the most important papers on phase locked loops and clock recovery circuits brings you comprehensive coverage of the field all in one self contained volume You ll gain an understanding of the analysis design simulation and implementation of phase locked loops and clock recovery circuits in CMOS and bipolar technologies along with valuable insights into the issues and trade offs associated with phase locked systems for high speed low power and low noise

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CMOS Multichannel Single-Chip Receivers for Multi-Gigabit Optical Data Communications Paul Muller, Yusuf Leblebici, 2007-10-08 The intention of this book is to address a number of timely performance critical issues within the field of short distance optical communications from a circuit designer s perspective It discusses the major trade offs the designer has to deal with in the development of monolithically integrated receivers in CMOS technologies As such it is based on Dr Muller s doctoral dissertation entitled A Standard CMOS Multi Channel Single Chip Receiver for Multi Gigabit Optical Data Communications subm ted to the School of Engineering of the cole Polytechnique F d rale de Lausanne EPFL in May 2006 The dissertation material has been enhanced by the presentation of a number of alternative design approaches and circuit topologies providing exhaustive coverage of the state of the art in optical sho distance receiver circuit design The need for a new processor input output I O interface paradigm is dictated by ongoing te nology scaling and the advent of multi core systems Indeed each new generation of microprocessors and digital signal processors provides higher computing power and data throughput whereas the available bandwidth of the I O interfaces is subject to much slower growth Moving beyond coming serial links to an optical data link paradigm for very short distance board to board and chip chip communications allows for considerable I O interface bandwidth enhancement Fully integrated silicon CMOS receivers are considered to be the technology of choice to lead this solution to economic success because monolithic integration results in lower volume manufacturing cost improved yield and reduced assembly and test expenses

Phase Lock Loops and Frequency Synthesis Venceslav F. Kroupa, 2003-06-02 Phase lock loop frequency synthesis finds uses in a myriad of wireless applications from local oscillators for receivers and transmitters to high performance RF test equipment As the security and reliability of mobile communication transmissions have gained importance PLL and frequency synthesisers have become increasingly topical subjects Phase Lock Loops Frequency Synthesis examines the various components that make up the phase lock loop design including oscillators crystal voltage controlled dividers and phase detectors Interaction amongst the various components are also discussed Real world problems such as power supply noise shielding grounding and isolation are given comprehensive coverage and solved examples with MATHCAD programs are presented throughout Presents a

comprehensive study of phase lock loops and frequency synthesis in communication systems Written by an internationally recognised expert in the field Details the problem of spurious signals in PLL frequency synthesizers a topic neglected by available competing titles Provides detailed theoretical background coupled with practical examples of state of the art device design MATHCAD programs and simulation software to accompany the design exercises and examples This combination of thorough theoretical treatment and guidance on practical applications will appeal to mobile communication circuit designers and advanced electrical engineering students

Proceedings of the Estonian Academy of Sciences, Engineering ,2001-12 *High-speed Circuits for Lightwave Communications* Keh-Chung Wang,1999 High speed circuits are crucial for increasing the bandwidth of transmission and switching of voice video data over optical fiber networks The ever increasing demand for bit rates higher than those available due to the explosion of Internet traffic has driven engineers to develop integrated circuits of performance approaching 100 Gb/s Commercial lightwave products using high speed circuits of 10 Gb/s and beyond are readily available High Speed Circuits for Lightwave Communications presents the latest information on circuit design measured results applications and product development It covers electronic and optoelectronic circuits for transmission receiving and cross point switching These circuits were implemented with various state of the art IC technologies including Si BJT GaAs MESFET HEMT HBT as well as InP HEMT and HBT The book written by more than 50 experts will benefit graduate students researchers and engineers who are interested in or work in this exciting and challenging field of optical communications

High Speed Circuits For Lightwave Communications, Selected Topics In Electronics And Systems, Vol 1 Keh-chung Wang,1999-01-25 High speed circuits are crucial for increasing the bandwidth of transmission and switching of voice video data over optical fiber networks The ever increasing demand for bit rates higher than those available due to the explosion of Internet traffic has driven engineers to develop integrated circuits of performance approaching 100 Gb/s Commercial lightwave products using high speed circuits of 10 Gb/s and beyond are readily available High Speed Circuits for Lightwave Communications presents the latest information on circuit design measured results applications and product development It covers electronic and optoelectronic circuits for transmission receiving and cross point switching These circuits were implemented with various state of the art IC technologies including Si BJT GaAs MESFET HEMT HBT as well as InP HEMT and HBT The book written by more than 50 experts will benefit graduate students researchers and engineers who are interested in or work in this exciting and challenging field of optical communications

High-Speed Clock Network Design Qing K. Zhu,2013-03-14 High Speed Clock Network Design is a collection of design concepts techniques and research works from the author for clock distribution in microprocessors and high performance chips It is organized in 11 chapters

Radio Frequency Integrated Circuit Design John W. M. Rogers,Calvin Plett,2010 This newly revised and expanded edition of the 2003 Artech House classic Radio Frequency Integrated Circuit Design serves as an up to date practical reference for complete RFIC know how The second edition

includes numerous updates including greater coverage of CMOS PA design RFIC design with on chip components and more worked examples with simulation results By emphasizing working designs this book practically transports you into the authors own RFIC lab so you can fully understand the function of each design detailed in this book Among the RFIC designs examined are RF integrated LC based filters VCO automatic amplitude control loops and fully integrated transformer based circuits as well as image reject mixers and power amplifiers If you are new to RFIC design you can benefit from the introduction to basic theory so you can quickly come up to speed on how RFICs perform and work together in a communications device A thorough examination of RFIC technology guides you in knowing when RFICs are the right choice for designing a communication device This leading edge resource is packed with over 1 000 equations and more than 435 illustrations that support key topics

Phase-Locked Loops Woogeun Rhee,Zhiping Yu,2024-01-18 Phase Locked Loops Discover the essential materials for phase locked loop circuit design from fundamentals to practical design aspects A phase locked loop PLL is a type of circuit with a range of important applications in telecommunications and computing It generates an output signal with a controlled relationship to an input signal such as an oscillator which matches the phases of input and output signals This is a critical function in coherent communication systems with the result that the theory and design of these circuits are essential to electronic communications of all kinds Phase Locked Loops System Perspectives and Circuit Design Aspects provides a concise accessible introduction to PLL design It introduces readers to the role of PLLs in modern communication systems the fundamental techniques of phase lock circuitry and the possible applications of PLLs in a wide variety of electronic communications contexts The first book of its kind to incorporate modern architectures and to balance theoretical fundamentals with detailed design insights this promises to be a must own text for students and industry professionals The book also features Coverage of PLL basics with insightful analysis and examples tailored for circuit designers Applications of PLLs for both wireless and wireline systems Practical circuit design aspects for modern frequency generation frequency modulation and clock recovery systems Phase Locked Loops is essential for graduate students and advanced undergraduates in integrated circuit design as well researchers and engineers in electrical and computing subjects

Phase-Lock Basics William F. Egan,2007-10-16 Broad based and hands on Phase Lock Basics Second Edition is both easy to understand and easy to customize The text can be used as a theoretical introduction for graduate students or when used with MATLAB simulation software the book becomes a virtual laboratory for working professionals who want to improve their understanding of the design process and apply it to the demands of specific situations This second edition features a large body of new statistical data obtained from simulations and uses available experimental data for confirmation of the simulation results

Millimeter-Wave Digitally Intensive Frequency Generation in CMOS Wanghua Wu,Robert Bogdan Staszewski,John R. Long,2015-09-23 This book describes the digitally intensive time domain architectures and techniques applied to millimeter wave frequency synthesis with the objective of improving performance and reducing the cost of

implementation Coverage includes system architecture system level modeling critical building block design and digital calibration techniques making it highly suitable for those who want to learn about mm wave frequency generation for communication and radar applications integrated circuit implementation and time domain circuit and system techniques Highlights the challenges of frequency synthesis at mm wave band using CMOS technology Compares the various approaches for mm wave frequency generation pros and cons Introduces the digitally intensive synthesizer approach and its advantages Discusses the proper partitioning of the digitally intensive mm wave frequency synthesizer into mm wave RF analog digital and software components Provides detailed design techniques from system level to circuit level Addresses system modeling simulation techniques design for test and layout issues Demonstrates the use of time domain techniques for high performance mm wave frequency synthesis *Radio Frequency System Architecture and Design* John W. M.

Rogers, Calvin Plett, Ian Marsland, 2013-10-01 Communication devices such as smart phones GPS systems and Bluetooth are now part of our daily lives more than ever before As our communication equipment becomes more sophisticated so do the radios and other hardware required to enable that technology Common radio architectures are required to make this technology work seamlessly This resource describes practical aspects of radio frequency communications systems design bridging the gap between system level design considerations and circuit level design specifications Industry experts not only provide detailed calculations and theory to determine block level specifications but also discuss basic theory and operational concepts This resource also includes extensive up to date application examples **Circuits at the Nanoscale** Krzysztof Iniewski, 2018-10-08 Circuits for Emerging Technologies Beyond CMOS New exciting opportunities are abounding in the field of body area networks wireless communications data networking and optical imaging In response to these developments top notch international experts in industry and academia present Circuits at the Nanoscale Communications Imaging and Sensing This volume unique in both its scope and its focus addresses the state of the art in integrated circuit design in the context of emerging systems A must for anyone serious about circuit design for future technologies this book discusses emerging materials that can take system performance beyond standard CMOS These include Silicon on Insulator SOI Silicon Germanium SiGe and Indium Phosphide InP Three dimensional CMOS integration and co integration with Microelectromechanical MEMS technology and radiation sensors are described as well Topics in the book are divided into comprehensive sections on emerging design techniques mixed signal CMOS circuits circuits for communications and circuits for imaging and sensing Dr Krzysztof Iniewski is a director at CMOS Emerging Technologies Inc a consulting company in Vancouver British Columbia His current research interests are in VLSI circuits for medical applications He has published over 100 research papers in international journals and conferences and he holds 18 international patents granted in the United States Canada France Germany and Japan In this volume he has assembled the contributions of over 60 world renowned experts who are at the top of their field in the world of circuit design advancing the bank of knowledge for all who work in

this exciting and burgeoning area **Advanced Frequency Synthesis by Phase Lock** William F. Egan, 2011-08-09 The latest frequency synthesis techniques including sigma delta Diophantine and all digital Sigma delta is a frequency synthesis technique that has risen in popularity over the past decade due to its intensely digital nature and its ability to promote miniaturization A continuation of the popular Frequency Synthesis by Phase Lock Second Edition this timely resource provides a broad introduction to sigma delta by pairing practical simulation results with cutting edge research Advanced Frequency Synthesis by Phase Lock discusses both sigma delta and fractional n the still in use forerunner to sigma delta employing Simulink models and detailed simulations of results to promote a deeper understanding After a brief introduction the book shows how spurs are produced at the synthesizer output by the basic process and different methods for overcoming them It investigates how various defects in sigma delta synthesis contribute to spurs or noise in the synthesized signal Synthesizer configurations are analyzed and it is revealed how to trade off the various noise sources by choosing loop parameters Other sigma delta synthesis architectures are then reviewed The Simulink simulation models that provided data for the preceding discussions are described providing guidance in making use of such models for further exploration Next another method for achieving wide loop bandwidth simultaneously with fine resolution the Diophantine Frequency Synthesizer is introduced Operation at extreme bandwidths is also covered further describing the analysis of synthesizers that push their bandwidths close to the sampling frequency limit Lastly the book reviews a newly important technology that is poised to become widely used in high production consumer electronics all digital frequency synthesis Detailed appendices provide in depth discussion on various stages of development and many related resources are available for download including Simulink models MATLAB scripts spreadsheets and executable programs All these features make this authoritative reference ideal for electrical engineers who want to achieve an understanding of sigma delta frequency synthesis and an awareness of the latest developments in the field **CMOS Receiver Front-ends for Gigabit Short-Range Optical**

Communications Francisco Aznar, Santiago Celma Pueyo, Belén Calvo Lopez, 2012-08-09 This book describes optical receiver solutions integrated in standard CMOS technology attaining high speed short range transmission within cost effective constraints These techniques support short reach applications such as local area networks fiber to the home and multimedia systems in cars and homes The authors show how to implement the optical front end in the same technology as the subsequent digital circuitry leading to integration of the entire receiver system in the same chip The presentation focuses on CMOS receiver design targeting gigabit transmission along a low cost standardized plastic optical fiber up to 50m in length This book includes a detailed study of CMOS optical receiver design from building blocks to the system level

High-Speed Devices and Circuits with THz Applications Jung Han Choi, 2017-09-19 Presenting the cutting edge results of new device developments and circuit implementations High Speed Devices and Circuits with THz Applications covers the recent advancements of nano devices for terahertz THz applications and the latest high speed data rate

connectivity technologies from system design to integrated circuit IC design providing relevant standard activities and technical specifications Featuring the contributions of leading experts from industry and academia this pivotal work Discusses THz sensing and imaging devices based on nano devices and materials Describes silicon on insulator SOI multigate nanowire field effect transistors FETs Explains the theory underpinning nanoscale nanowire metal oxide semiconductor field effect transistors MOSFETs simulation methods and their results Explores the physics of the silicon germanium SiGe heterojunction bipolar transistor HBT as well as commercially available SiGe HBT devices and their applications Details aspects of THz IC design using standard silicon Si complementary metal oxide semiconductor CMOS devices including experimental setups for measurements detection methods and more An essential text for the future of high frequency engineering High Speed Devices and Circuits with THz Applications offers valuable insight into emerging technologies and product possibilities that are attractive in terms of mass production and compatibility with current manufacturing facilities

DRAM Circuit Design Brent Keeth,R. Jacob Baker,Brian Johnson,Feng Lin,2007-12-04 A modern comprehensive introduction to DRAM for students and practicing chip designers Dynamic Random Access Memory DRAM technology has been one of the greatest driving forces in the advancement of solid state technology With its ability to produce high product volumes and low pricing it forces solid state memory manufacturers to work aggressively to cut costs while maintaining if not increasing their market share As a result the state of the art continues to advance owing to the tremendous pressure to get more memory chips from each silicon wafer primarily through process scaling and clever design From a team of engineers working in memory circuit design DRAM Circuit Design gives students and practicing chip designers an easy to follow yet thorough introductory treatment of the subject Focusing on the chip designer rather than the end user this volume offers expanded up to date coverage of DRAM circuit design by presenting both standard and high speed implementations Additionally it explores a range of topics the DRAM array peripheral circuitry global circuitry and considerations voltage converters synchronization in DRAMs data path design and power delivery Additionally this up to date and comprehensive book features topics in high speed design and architecture and the ever increasing speed requirements of memory circuits The only book that covers the breadth and scope of the subject under one cover DRAM Circuit Design is an invaluable introduction for students in courses on memory circuit design or advanced digital courses in VLSI or CMOS circuit design It also serves as an essential one stop resource for academics researchers and practicing engineers

High-Performance D/A-Converters Martin Clara,2012-12-22 This book deals with modeling and implementation of high performance current steering D A converters for digital transceivers in nanometer CMOS technology In the first part the fundamental performance limitations of current steering DACs are discussed Based on simplified models closed form expressions for a number of basic non ideal effects are derived and tested With the knowledge of basic performance limits the converter and system architecture can be optimized in an early design phase trading off circuit complexity silicon area and power

dissipation for static and dynamic performance The second part describes four different current steering DAC designs in standard 130 nm CMOS The converters have a resolution in the range of 12–14 bits for an analog bandwidth between 2–20 MHz and 50 MHz and sampling rates from 100 MHz to 350 MHz Dynamic Element Matching DEM and advanced dynamic current calibration techniques are employed to minimize the required silicon area

Unveiling the Energy of Verbal Artistry: An Mental Sojourn through **Monolithic Phase Locked Loops And Clock Recovery Circuits Theory And Design**

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