SDL

Formal Object-oriented Language for
Communicating Systems

jan ellsberger
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Sdl Formal Object Oriented Language For Communicating Systems

Daniel Hardy,Guy Malleus,Jean-Noel Mereur

Sdl Formal Object Oriented Language For Communicating Systems:

   SDL Jan Ellsberger,Dieter Hogrefe,Amardeo Sarma,1997 SDL 92 is the recognized international standard language for designing and specifying telecommunications systems and is increasingly also used for RAD real time interactive applications This is the first complete introduction and reference to SDL for both novices and experienced system engineers The book begins with an overview of basic SDL concepts structure and syntax It introduces SDL abstract data types the SDL interpretation model and shows how to specify protocols with SDL All professional systems engineers programmers and students of system development working in telecommunications real time interactive and distributed systems   Formal Methods for Protocol Engineering and Distributed Systems Jianping Wu,Samuel T. Chanson,Quiang Gao,2013-06-05 Formal Methods for Protocol Engineering and Distributed Systems addresses formal description techniques FDTs applicable to distributed systems and communication protocols It aims to present the state of the art in theory application tools an industrialization of FDTs Among the important features presented are FDT based system and protocol engineering FDT application to distributed systems Protocol engineeering Practical experience and case studies Formal Methods for Protocol Engineering and Distributed Systems contains the proceedings of the Joint International Conference on Formal Description Techniques for Distributed Systems and Communication Protocols and Protocol Specification Testing and Verification which was sponsored by the International Federation for Information Processing IFIP and was held in Beijing China in October 1999 This volume is suitable as a secondary text for a graduate level course on Distributed Systems or Communications and as a reference for researchers and industry practitioners   System-on-Chip Methodologies & Design Languages Peter J. Ashenden,Jean Mermet,Ralf Seepold,2013-03-14 System on Chip Methodologies the Forum on Design Languages FDL held in Europe and the Asia Pacific Chip Design Language APChDL Conference The papers cover a range of topics including design methods specification and modeling languages tool issues formal verification simulation and synthesis The results presented in these papers will help researchers and practicing engineers keep abreast of developments in this rapidly evolving field
   Formal Methods for Open Object-Based Distributed Systems Paolo Ciancarini,Alessandro Fantechi,Roberto Gorrieri,2013-11-11 Formal Methods for Open Object Based Distributed Systems presents the leading edge in several related fields specifically object orientated programming open distributed systems and formal methods for object oriented systems With increased support within industry regarding these areas this book captures the most up to date information on the subject Many topics are discussed including the following important areas object oriented design and programming formal specification of distributed systems open distributed platforms types interfaces and behaviour formalisation of object oriented methods This volume comprises the proceedings of the International Workshop on Formal Methods for Open Object based Distributed Systems FMOODS sponsored by the International Federation for Information Processing IFIP which was held in Florence Italy in February 1999 Formal Methods for Open Object Based Distributed Systems is suitable as a

secondary text for graduate level courses in computer science and telecommunications and as a reference for researchers and practitioners in industry commerce and government   Formal Methods for Industrial Critical Systems Stefania Gnesi,Tiziana Margaria,2012-11-27 Today formal methods are widely recognized as an essential step in the design process of industrial safety critical systems In its more general definition the term formal methods encompasses all notations having a precise mathematical semantics together with their associated analysis methods that allow description and reasoning about the behavior of a system in a formal manner Growing out of more than a decade of award winning collaborative work within the European Research Consortium for Informatics and Mathematics Formal Methods for Industrial Critical Systems A Survey of Applications presents a number of mainstream formal methods currently used for designing industrial critical systems with a focus on model checking The purpose of the book is threefold to reduce the effort required to learn formal methods which has been a major drawback for their industrial dissemination to help designers to adopt the formal methods which are most appropriate for their systems and to offer a panel of state of the art techniques and tools for analyzing critical systems   **Testing of Communicating Systems XIV** Ina Schieferdecker,Hartmut König,Adam Wolisz,2013-06-05 Testing of Communicating Systems XIV presents the latest international results in both the theory and industrial practice of the testing of communicating systems ranging from tools and techniques for testing to test standards frameworks notations algorithms fundamentals of testing and industrial experiences and issues The tools and techniques discussed apply to conformance testing interoperability testing performance testing Internet protocols and applications and multimedia and distributed systems in general   *Applications of Graph Transformations with Industrial Relevance* John L. Pfaltz,Manfred Nagl,Boris Böhlen,2004-07-28 This book constitutes the thoroughly refereed post proceedings of the Second International Workshop on Applications of Graph Transformations with Industrial Relevance AGTIVE 2003 held in Charlotesville Virginia USA in September October 2003 The 27 revised full papers and 11 revised demo papers presented together with 2 invited papers and 5 workshop reports were carefully selected during iterated rounds of reviewing and revision The papers are organized in topical sections on Web applications data structures and data bases engineering applications agent oriented and functional programs and distribution object and aspect oriented systems natural languages processing and structuring reengineering reuse and integration modeling languages bioinformatics and multimedia picture and visual languages   **Principles of Verifiable RTL Design** Lionel Bening,Harry D. Foster,2007-05-08 Principles of Verifiable RTL Design A Functional Coding Style Supporting Verification Processes in Verilog explains how you can write Verilog to describe chip designs at the RT level in a manner that cooperates with verification processes This cooperation can return an order of magnitude improvement in performance and capacity from tools such as simulation and equivalence checkers It reduces the labor costs of coverage and formal model checking by facilitating communication between the design engineer and the verification engineer It also orients the RTL style to provide more useful results from the overall verification process The

intended audience for Principles of Verifiable RTL Design A Functional Coding Style Supporting Verification Processes in Verilog is engineers and students who need an introduction to various design verification processes and a supporting functional Verilog RTL coding style A second intended audience is engineers who have been through introductory training in Verilog and now want to develop good RTL writing practices for verification A third audience is Verilog language instructors who are using a general text on Verilog as the course textbook but want to enrich their lectures with an emphasis on verification A fourth audience is engineers with substantial Verilog experience who want to improve their Verilog practice to work better with RTL Verilog verification tools A fifth audience is design consultants searching for proven verification centric methodologies A sixth audience is EDA verification tool implementers who want some suggestions about a minimal Verilog verification subset Principles of Verifiable RTL Design A Functional Coding Style Supporting Verification Processes in Verilog is based on the reality that comes from actual large scale product design process and tool experience **Human-Computer Interactions in Transport** Christophe Kolski,2013-01-24 The human computer interactions are more and more present in our everyday life and lead to many conceptual and methodological problems for the designers and evaluators of interactive systems This book is about Human Computer Interaction in Transport domain in which the traveler becomes a user of information systems particularly before and during the travel s This book will focus on traveler information and personalized systems using a human centered design approach Fundamental Approaches to Software Engineering Mauro Pezzè,2003-07-01 This book constitutesnbsp the refereed proceedings of the 6th International Conference on Fundamental Approaches to Software Engineering FASE 2003 held in Warsaw Poland in April 2003 The 20 revised full papers presented together with a keynote paper were carefully reviewed and selected from 89 submissions The papers are organized in topical sections on software components mobile computing aspects and web applications software measurements formal verficiation analysis and testing and model integration and extension **Model Driven Engineering Languages and Systems** Gregor Engels,Bill Opdyke,Douglas C. Schmidt,Frank Weil,2007-09-12 This book constitutes the refereed proceedings of the 10th International Conference on Model Driven Engineering Languages and Systems formerly the UML series of conferences MODELS 2007 held in Nashville USA September 30 October 5 2007 The 45 revised full papers were carefully reviewed and selected from 158 initial submissions The papers are organized in topical sections Recent Trends in Algebraic Development Techniques Jose L. Fiadeiro,2003-07-31 The European conference situationin the general area of software science has longbeen considered unsatisfactory A fairlylarge number of small and medi sized conferences and workshops take place on an irregular basis competing for high quality contributions and for enough attendees to make them nancially viable Discussions aiming at a consolidation have been underway since at least 1992 with concrete planning beginning in summer 1994 and culminating in a public meeting at TAPSOFT 95 in Aarhus On the basis of a broad consensus it was decided to establish a single annual federated spring conference in the slot that was then occupied by TAPSOFT and CAAP

ESOP CC comprising a number of existing and new conferences and covering a spectrum from theory to practice ETAPS 98 the rst instance of the European Joint Conferences on Theory and Practice of Software is taking place this year in Lisbon It comprises ve conferences FoSSaCS FASE ESOP CC TACAS four workshops ACoS VISUAL WADT CMCS seven invited lectures and nine tutorials **Emerging Personal Wireless Communications** Olli Martikainen,Jari Porras,Jenni Hyvärinen,2005-12-19 The growth of telecommunications has been largely based on mobile and data services in the past 10 years and the growth will continue For instance it is forecasted that after 2005 the mobile traffic turnover in Europe will exceed that of fixed telephone traffic and the penetration of Internet access through mobile will exceed that of fixed access It is expected that the new value added services will be Internet based and the IP traffic will outweigh the amount of traditional ISDN based telephone traffic The transition from the existing telecommunications services to mobile and Internet based ones will change the service infrastructure as well as the customer and service management structures In wireless communications there are several new standards being developed and implemented to improve the data transmission rate over radio channels to combine both voice packet data and multimedia services in the terminals and to improve the service quality and usability Narrow band packet radio standards such as General Packet Radio Service GPRS and i Mode are already operative and broadband IMT 2000 standards also called as Third Generation 3G Mobile have been developed in Japan Europe and US At the same time Wireless LAN and Bluetooth technologies mature and provide short range data access to terminal devices The emerging new technologies create opportunities not only to incumbent teleoperators but also to new network operators IT companies and new service and content providers **Communication Protocol Specification and Verification** Richard Lai,Ajin Jirachiefpattana,2012-12-06 Communication protocols are rules whereby meaningful communication can be exchanged between different communicating entities In general they are complex and difficult to design and implement Specifications of communication protocols written in a natural language e g English can be unclear or ambiguous and may be subject to different interpretations As a result independent implementations of the same protocol may be incompatible In addition the complexity of protocols make them very hard to analyze in an informal way There is therefore a need for precise and unambiguous specification using some formal languages Many protocol implementations used in the field have almost suffered from failures such as deadlocks When the conditions in which the protocols work correctly have been changed there has been no general method available for determining how they will work under the new conditions It is necessary for protocol designers to have techniques and tools to detect errors in the early phase of design because the later in the process that a fault is discovered the greater the cost of rectifying it Protocol verification is a process of checking whether the interactions of protocol entities according to the protocol specification do indeed satisfy certain properties or conditions which may be either general e g absence of deadlock or specific to the particular protocol system directly derived from the specification In the 80s an ISO International Organization for Standardization working group began a programme of

work to develop formal languages which were suitable for Open Systems Interconnection OSI This group called such languages Formal Description Techniques FDTs Some of the objectives of ISO in developing FDTs were enabling unambiguous clear and precise descriptions of OSI protocol standards to be written and allowing such specifications to be verified for correctness There aretwo FDTs standardized by ISO LOTOS and Estelle Communication Protocol Specification and Verification is written to address the two issues discussed above the needs to specify a protocol using an FDT and to verify its correctness in order to uncover specification errors in the early stage of a protocol development process The readership primarily consists of advanced undergraduate students postgraduate students communication software developers telecommunication engineers EDP managers researchers and software engineers It is intended as an advanced undergraduate or postgraduate textbook and a reference for communication protocol professionals   The Industrial Information Technology Handbook Richard Zurawski,2018-10-03 The Industrial Information Technology Handbook focuses on existing and emerging industrial applications of IT and on evolving trends that are driven by the needs of companies and by industry led consortia and organizations Emphasizing fast growing areas that have major impacts on industrial automation and enterprise integration the Handbook covers topics such as industrial communication technology sensors and embedded systems The book is organized into two parts Part 1 presents material covering new and quickly evolving aspects of IT Part 2 introduces cutting edge areas of industrial IT The Handbook presents material in the form of tutorials surveys and technology overviews combining fundamentals and advanced issues with articles grouped into sections for a cohesive and comprehensive presentation The text contains 112 contributed reports by industry experts from government companies at the forefront of development and some of the most renowned academic and research institutions worldwide Several of the reports on recent developments actual deployments and trends cover subject matter presented to the public for the first time

FM 2006: Formal Methods Jayadev Misra,Tobias Nipkow,Emil Sekerinski,2006-08-10 This book presents the refereed proceedings of the 14th International Symposium on Formal Methods FM 2006 held in Hamilton Canada August 2006 The book presents 36 revised full papers together with 2 invited contributions and extended abstracts of 7 invited industrial presentations organized in topical sections on interactive verification formal modelling of systems real time industrial experience specification and refinement programming languages algebra formal modelling of systems and more

Scenarios: Models, Transformations and Tools Stefan Leue,Tarja J. Systä,2005-08-25 Visual notations and languages continue to play a pivotal role in the design of complex software systems In many cases visual notations are used to scribe usage or interaction scenarios of software systems or their components While representing scenarios using a visual notation is not the only possibility a vast majority of scenario description languages is visual Scenarios are used in telecommunications as Message Sequence Charts in object oriented system design as Sequence Diagrams in reverse engineering as execution traces and in requirements engineering as for example Use Case Maps or Life Sequence Charts

These techniques are used to capture requirements to capture use cases in system documentation to specify test cases or to visualize runs of existing systems They are often employed to represent concurrent systems that int act via message passing or method invocation In telecommunications for more than 15 years the International Telecommunication Union has standardized the Message Sequence Charts MSCs notation in its recommendation Z 120 More recently with the emergence of UML as a predominant software design meth ology there has been special interest in the development of the sequence d gram notation As a result the most recent version 2 0 of UML encompasses the Message Sequence Chart notation including its hierarchical modeling f tures Other scenario avored diagrams in UML 2 0 include activity diagrams and timing diagrams

    Reuse Methodology Manual for System-on-a-Chip Designs Pierre Bricaud,2007-05-08 This revised and updated third edition outlines a set of best practices for creating reusable designs for use in an System on a Chip SoC design methodology These practices are based on the authors experience in developing reusable designs as well as the experience of design teams in many companies around the world   <u>Electronic Chips & Systems Design Languages</u> Jean Mermet,2013-03-09 Electronic Chips Systems Design Languagesoutlines and describes the latest advances in design languages The challenge of System on a Chip SOC design requires designers to work in a multi lingual environment which is becoming increasingly difficult to master It is therefore crucial for them to learn almost in real time from the experiences of their colleagues in the use of design languages and how these languages have become more advanced to cope with system design System designers as well as students willing to become system designers often do not have the time to attend all scientific events where they could learn the necessary information This book will bring them a selected digest of the best contributions and industry strength case studies All the levels of abstraction that are relevant from the informal user requirements down to the implementation specifications are addressed by different contributors The author together with colleague authors who provide valuable additional experience presents examples of actual industrial world applications Furthermore the academic concepts presented in this book provide excellent theories to student readers and the concepts described are up to date and in so doing provide most suitable root information for Ph D postgraduates   **Networks** Daniel Hardy,Guy Malleus,Jean-Noel Mereur,2013-12-18 Revolution transformation upheaval and promise Yesterday the technologies of com munication were accessible only to experts today they are a subject of constant discussion in the media New services are advertised on a daily basis and the potential realized or not of these technologies is a constant source of comment and discussion But beyond the media frenzy things really are developing with increasing speed driven by the power of the Internet The network has built up an ongoing relationship between research centres development teams and marketing teams allowing a constructive collaboration between technologies The network has become the catalyst for its own evolution The arrival of IP and GSM has given rise to new corporate giants like Cisco Systems and Nokia Operators witnessing the diversification of their main sources of revenue have been forced to merge or split Entirely new actors from

various horizons are counting on their ability to act as operators without a network to their name Traditional equipment manufacturers have had to rethink their product lines in view of these new foundations Likewise governments have understood the need to create a body of laws that promote the harmonious and rapid development of networks to offer alternatives for operators and service providers These often complex regulations act both as constraint and opportunity for operators and give direction to the actions of actors across the board

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