JAPANESE TECHNOLOGY REVIEWS

Section A: Electronics

MEGA-BIT MEMORY TECHNOLOGY FROM MEGA-BIT TO GIGA-BIT

Edited by Hiroyuki Tango

Vol. 32



Megabit Memory Technology

Robert R. Redfield

Megabit Memory Technology:

Mega-Bit Memory Technology - From Mega-Bit to Giga-Bit Hiroyuki Tango, 2019-10-16 This book describes LSI process technology and focuses on the rapid progress of state of the art dynamic random access memory DRAM process technologies the longstanding technology driver of Si ULSI as they advance from the 1 Kbit to the Gbit DRAM era Fifth Biennial Nonvolatile Memory Technology Review ,1993 Nonvolatile Memory Technologies with Emphasis on Flash Joe Brewer, Manzur Gill, 2011-09-23 Presented here is an all inclusive treatment of Flash technology including Flash memory chips Flash embedded in logic binary cell Flash and multilevel cell Flash The book begins with a tutorial of elementary concepts to orient readers who are less familiar with the subject Next it covers all aspects and variations of Flash technology at a mature engineering level basic device structures principles of operation related process technologies circuit design overall design tradeoffs device testing reliability and applications A Forecast of Space Technology, 1980-2000 ,1976

Silicon Based Unified Memory Devices and Technology Arup Bhattacharyya, 2017-07-06 The primary focus of this book is on basic device concepts memory cell design and process technology integration. The first part provides in depth coverage of conventional nonvolatile memory devices stack structures from device physics historical perspectives and identifies limitations of conventional devices. The second part reviews advances made in reducing and or eliminating existing limitations of NVM device parameters from the standpoint of device scalability application extendibility and reliability The final part proposes multiple options of silicon based unified nonvolatile memory cell concepts and stack designs SUMs The book provides Industrial R D personnel with the knowledge to drive the future memory technology with the established silicon FET based establishments of their own It explores application potentials of memory in areas such as robotics avionics health industry space vehicles space sciences bio imaging genetics etc Electronic Computer Memory Technology Wallace NAND Flash Memory Technologies Seiichi Aritome, 2015-12-29 Offers a comprehensive overview of B. Riley, 1971 NAND flash memories with insights into NAND history technology challenges evolutions and perspectives Describes new program disturb issues data retention power consumption and possible solutions for the challenges of 3D NAND flash memory Written by an authority in NAND flash memory technology with over 25 years experience Signal ,1993 The Digital Consumer Technology Handbook Amit Dhir, 2004-04-30 The consumer electronics market has never been as awash with new consumer products as it has over the last couple of years The devices that have emerged on the scene have led to major changes in the way consumers listen to music access the Internet communicate watch videos play games take photos operate their automobiles even live Digital electronics has led to these leaps in product development enabling easier exchange of media cheaper and more reliable products and convenient services This handbook is a much needed comprehensive engineering guide to the dynamic world of today s digital consumer electronics. It provides complete details on key enabling technologies standards delivery and reception systems products appliances and networking systems Each

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Offering first hand insights by top scientists and industry experts at the forefront of R D into nanoelectronics this book neatly
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overview of present and emerging logic devices memories and power technologies Specific chapters are dedicated to the

enabling factors such as new materials characterization techniques smart manufacturing and advanced circuit design The second part of the book provides detailed coverage of the current state and showcases real future applications in a wide range of fields safety transport medicine environment manufacturing and social life including an analysis of emerging trends in the internet of things and cyber physical systems A survey of main economic factors and trends concludes the book Highlighting the importance of nanoelectronics in the core fields of communication and information technology this is essential reading for materials scientists electronics and electrical engineers as well as those working in the semiconductor and sensor industries **Dynamic RAM** Muzaffer A. Siddiqi, 2017-12-19 Because of their widespread use in mainframes PCs and mobile audio and video devices DRAMs are being manufactured in ever increasing volume both in stand alone and in embedded form as part of a system on chip Due to the optimum design of their components access transistor storage capacitor and peripherals DRAMs are the cheapest and densest semiconductor memory currently available As a result most of DRAM structure research and development focuses on the technology used for its constituent components and their interconnections However only a few books are available on semiconductor memories in general and fewer on DRAMs Dynamic RAM Technology Advancements provides a holistic view of the DRAM technology with a systematic description of the advancements in the field since the 1970s and an analysis of future challenges Topics Include DRAM cells of all types including planar three dimensional 3 D trench or stacked COB or CUB vertical and mechanically robust cells using advanced transistors and storage capacitors Advancements in transistor technology for the RCAT SCAT FinFET BT FinFET Saddle and advanced recess type and storage capacitor realizations How sub 100 nm trench DRAM technologies and sub 50 nm stacked DRAM technologies and related topics may lead to new research Various types of leakages and power consumption reduction methods in active and sleep mode Various types of SAs and yield enhancement techniques employing ECC and redundancy A worthwhile addition to semiconductor memory research academicians and researchers interested in the design and optimization of high density and cost efficient DRAMs may also find it useful as part of a graduate level course

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